## SIKYWORIKS

## DATA SHEET



## AS215-92, AS215-92LF: Single Positive Control PHEMT GaAs IC SPDT Switch 0.5 to $\mathbf{3}$ GHz

## Applications

- $T /$ R switch for Bluetooth $^{\circledR}$ and general purpose telecommunication applications


## Features

- Single bias control
- Operates with 1.8 V control voltage
- Low DC power consumption
- Available lead ( Pb )-free and RoHS-compliant

MSL-1 @ $260^{\circ} \mathrm{C}$ per JEDEC J-STD-020

## Description

The AS215-92 is a medium-power IC FET SPDT switch in a low cost, miniature SC-70 6-lead plastic package. The AS215-92 features low insertion loss and positive voltage operation with very low DC power consumption. This general-purpose switch can be used in a variety of telecommunications applications.

## NEW

Skyworks offers lead (Pb)-free, RoHS (Restriction of Hazardous Substances)-compliant packaging.


Figure 1. AS215-92 Block Diagram

DC blocking capacitors ( $\mathrm{C}_{\mathrm{BL}}$ ) must be supplied externally for positive voltage operation. $\mathrm{C}_{\mathrm{BL}}=100 \mathrm{pF}$ for operation >500 MHz.

Table 1. Electrical Specifications $\mathrm{V}_{\mathrm{S}}=3 \mathrm{~V}, \mathrm{~V}_{\mathrm{CTL}}=0 / 3 \mathrm{~V}, \mathrm{Z}_{0}=50 \Omega$, unless otherwise noted

| Parameter | Frequency | Min. | Typ. | Max. | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Insertion loss(1) | 0.5 to 1.0 GHz |  | 0.75 | 0.5 | dB |
|  | 1.0 to 2 GHz |  | 0.60 | 0.6 |  |
|  | 2.0 to 3 GHz |  | 0.50 | 0.7 |  |
| Isolation | 0.5 to 1.0 GHz | 25 | 28 |  | dB |
|  | 1.0 to 2 GHz | 21 | 24 |  |  |
|  | 2.0 to 3 GHz | 17 | 20 |  |  |
| VSWR(2) | 0.5 to 1.0 GHz |  | 1.1:1 |  | dB |
|  | 1.0 to 2 GHz |  | 1.4:1 |  |  |
|  | 2.0 to 3 GHz |  | 1.2:1 |  |  |

1. Insertion loss changes by $0.003 \mathrm{~dB} /{ }^{\circ} \mathrm{C}$.
2. Insertion loss state.

Table 2. Operating Characteristics at $25^{\circ} \mathrm{C}, \mathrm{V}_{\mathrm{S}}=\mathbf{3 V}, \mathrm{V}_{\mathrm{CTL}}=\mathbf{0} / 3 \mathrm{~V}, \mathrm{Z}_{\mathbf{0}}=50 \Omega$, unless otherwise noted

| Parameter | Condition | Frequency | Min. | Typ. | Max. | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Switching characteristics Rise, fall On, off Video feedthru | 10/90\% or 90/10\% RF 50\% CTL to 90/10\% RF $\mathrm{T}_{\text {RISE }}=1 \mathrm{~ns}, \mathrm{BW}=500 \mathrm{MHz}$ |  |  | $\begin{aligned} & 10 \\ & 20 \\ & 25 \end{aligned}$ |  | $\begin{gathered} \mathrm{ns} \\ \mathrm{~ns} \\ \mathrm{mV} \end{gathered}$ |
| Input power for 1 dB compression | $\begin{aligned} & \mathrm{V}_{\mathrm{CTL}}=0 / 1.8 \mathrm{~V} \\ & \mathrm{~V}_{\mathrm{CTL}}=0 / 3 \mathrm{~V} \end{aligned}$ | 0.5 to 3 GHz |  | $\begin{aligned} & 20 \\ & 27 \end{aligned}$ |  | dBm |
| Intermodulation intercept point (IP3) | For two-tone input power 5 dBm $\mathrm{V}_{\mathrm{CTL}}=0 / 3 \mathrm{~V}$ | 0.5 to 3 GHz |  | 40 |  | dBm |
| Thermal resistance |  |  |  | 25 |  | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Control voltage | $\begin{array}{\|l\|} \hline \text { Low } \\ \text { High } \end{array}$ |  | $\begin{gathered} 0 \\ 1.8 \end{gathered}$ |  | $\begin{aligned} & 5.0 \\ & 0.2 \end{aligned}$ | V |
| Control port current | $\begin{aligned} & \mathrm{V}_{\mathrm{CTL}}=10 \mathrm{~W} \\ & \mathrm{~V}_{\mathrm{CTL}}=2.7 \mathrm{~V} \\ & \mathrm{~V}_{\mathrm{CTL}}=5 \mathrm{~V} \end{aligned}$ |  |  |  | $\begin{gathered} 200 \\ 100 \\ 20 \\ 20 \end{gathered}$ | $\mu \mathrm{A}$ |
| Supply voltage |  |  | $\begin{aligned} & \hline \mathrm{V}_{\mathrm{HIGH}} \\ & -0.2 \end{aligned}$ |  | $\begin{aligned} & \hline \mathrm{V}_{\mathrm{HIGH}} \\ & +0.2 \end{aligned}$ | V |

Table 3. Absolute Maximum Ratings ${ }^{\mathbf{1}}$

| Characteristic | Value |
| :--- | :--- |
| RF input power | 2 W max. $>500 \mathrm{MHz}$ <br> $0 / 8 \mathrm{~V}$ control |
| Supply voltage | 8 V |
| Control voltage | $-0.2 \mathrm{~V},+8 \mathrm{~V}$ |
| Operating temperature | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ |
| Storage temperature | $-65^{\circ} \mathrm{C}$ to $+150^{\circ} \mathrm{C}$ |
| 1 |  |

${ }^{1}$ Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to device with only one parameter set at the limit and all other parameters set at or below their nominal value. Exceeding any of the limits listed here may result in permanent damage to the device.

ESD HANDLING: Industry-standard ESD handling precautions must be adhered to at all times to avoid damage to this device.
Table 4. Truth Table

| V1 | J1-J2 | J1-J3 |
| :---: | :---: | :---: |
| 0 | Isolation | Insertion loss |
| $\mathrm{V}_{\text {HIGH }}$ | Insertion loss | Isolation |
| $1.8 \leq \mathrm{V}_{\text {HIGH }} \leq 5 \mathrm{~V}$ <br> VDD $=\mathrm{V}_{\text {HIGH }} \pm 0.2 \mathrm{~V}$ |  |  |

## Typical Performance Data

$V_{S}=3 \mathrm{~V}, \mathrm{~V}_{\mathrm{CTL}}=0 / 3 \mathrm{~V}, \mathrm{Z}_{\mathbf{0}}=50 \Omega$, unless otherwise noted


## Package Dimensions

## SC-70 6 Lead (SC-88)



## Recommended Solder Reflow Profiles

Refer to the "Recommended Solder Reflow Profile" Application Note.
Tape and Reel Information
Refer to the "Discrete Devices and IC Switch/Attenuators Tape and Reel Package Orientation" Application Note.

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