## SKYWORKS

## DATA SHEET

# SKY13419-365LF: 0.25-2.15 GHz 4x2 Switch Matrix with Tone/Voltage Detector 

## Applications

- DBS switching systems
- cable TV/modems


## Features

- Broadband frequency range: 0.25 to 2.15 GHz
- Tone and voltage control switching
- High isolation: 40 dB @ 900 MHz
- Four RF inputs, two RF outputs
- Low current consumption: $2.5 \mathrm{~mA} @ 5 \mathrm{~V}$
- Alternate truth Table logic using LGCTL pin
- Miniature QFN (20-pin, $4 \times 4 \mathrm{~mm}$ ) package (MSL $260^{\circ} \mathrm{C}$ per JEDEC J-STD-020)


Figure 1. SKY13419-365LF Block Diagram
Load detection and Digital Satellite Equipment Control (DiSEqC) rejection are integrated on the switch. The SKY13419-365LF rejects DiSEqC signals and responds only to continuous-tone and voltage signals or vertical-horizontal mirror control inputs. The switch on/off states are not changed by DiSEqC signals, and only changed by continuous-tone and voltage signals or verticalhorizontal mirror control inputs.
One of the two switch outputs can be deactivated when no-tone and no-voltage are applied to one of the tone/voltage detectors.
The SKY13419-365LF is manufactured in a compact, $4 \times 4 \mathrm{~mm}$, 20-pin Quad Flat No-Lead (QFN) package.
A functional block diagram is shown in Figure 1. The pin configuration and package are shown in Figure 2. Signal pin assignments and functional pin descriptions are provided in Table 1.

Table 1. SKY13419-365LF Signal Description

| Pin \# | Name | Description | Pin \# | Name |  |
| :---: | :--- | :--- | :---: | :--- | :--- |
| 1 | GND | Ground | 11 | T1 | Description |
| 2 | I3 | RF input 3 | 12 | V1 | Stereo right tone detector input |
| 3 | GND | Ground | 13 | GND | Ground |
| 4 | V2 | Stereo left voltage detector input | 14 | I1 | RF input 1 |
| 5 | T2 | Stereo left tone detector input | 15 | GND | Ground |
| 6 | OUT2 | RF output 2 | No connection. Pin may be grounded with no |  |  |
| 7 | N/C | Ponge in performance. | 17 | GND | RF input 2 |
| 8 | VDD | Power supply voltage | 18 | OMR | Ground |
| 9 | LGCTL | Determines switch logic (see Table 4) | 19 | GND | Vertical/horizontal mirror (see Table 4) |
| 10 | OUT1 | RF output 1 | 20 | I4 | Ground |

## Electrical and Mechanical Specifications

The absolute maximum ratings of the SKY13419-365LF are provided in Table 2. Electrical specifications are provided in Table 3.

The state of the SKY13419-365LF is determined by the logic provided in Table 4.

Typical performance characteristics of the SKY13419-365LF are illustrated in Figures 3 through 10.

Table 2. SKY13419-365LF Absolute Maximum Ratings

| Parameter | Symbol | Minimum | Typical | Maximum | Units |
| :--- | :--- | :--- | :--- | :---: | :---: |
| Supply voltage | Vod |  |  | 6 |  |
| RF input power | PIN |  |  | +18 |  |
| Storage temperature | TsTG | -40 |  | +125 | dBm |
| Operating temperature | Top | -40 |  | ${ }^{\circ} \mathrm{C}$ |  |

Note: Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to device with only one parameter set at the limit and all other parameters set at or below their nominal value. Exceeding any of the limits listed here may result in permanent damage to the device.

CAUTION: Although this device is designed to be as robust as possible, Electrostatic Discharge (ESD) can damage this device. This device must be protected at all times from ESD. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD precautions should be used at all times.

Table 3. SKY13419-365LF Electrical Specifications (1 of 2) (Note1)
( $\mathrm{V}_{\mathrm{DD}}=5 \mathrm{~V}, \mathrm{~T}_{\mathrm{OP}}=+25^{\circ} \mathrm{C}, \mathrm{Piw}_{\mathrm{IN}}=\mathbf{0} \mathrm{dBm}$, Characteristic Impedance $\left[Z_{0}\right]=50 \Omega$, Unless Otherwise Noted)

| Parameter | Symbol | Test Condition | Min | Typical | Max | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| RF Specifications |  |  |  |  |  |  |
| Insertion loss | IL | All states <br> 0.25 to 0.95 GHz <br> 0.95 to 2.15 GHz |  | $\begin{aligned} & 7.1 \\ & 8.5 \end{aligned}$ | $\begin{aligned} & 7.5 \\ & 9.0 \end{aligned}$ | $\begin{aligned} & \mathrm{dB} \\ & \mathrm{~dB} \end{aligned}$ |
| Insertion loss flatness |  | All states <br> 0.25 to 0.95 GHz 0.95 to 2.15 GHz |  | $\begin{aligned} & 0.4 \\ & 1.4 \end{aligned}$ | $\begin{aligned} & 1.0 \\ & 2.0 \end{aligned}$ | $\begin{aligned} & \mathrm{dB} \\ & \mathrm{~dB} \end{aligned}$ |
| Isolation | Iso | Normalized to insertion loss, all states <br> 0.25 to 0.95 GHz <br> 0.95 to 2.15 GHz | $\begin{aligned} & 40 \\ & 36 \end{aligned}$ | $\begin{aligned} & 43 \\ & 38 \end{aligned}$ |  | $\begin{aligned} & \mathrm{dB} \\ & \mathrm{~dB} \end{aligned}$ |
| Input return loss |  | $\begin{aligned} & 0.25 \text { to } 2.15 \mathrm{GHz}, \\ & \mathrm{I}, \mathrm{I} 2, \mathrm{I}, \mathrm{I}, \text { all states } \end{aligned}$ | 12 | 18 |  | dB |
| Output return loss |  | $\begin{aligned} & \hline 0.25 \text { to } 2.15 \mathrm{GHz}, \\ & \text { OUT1, OUT2, all states } \end{aligned}$ | 8 | 10 |  | dB |
| 1 dB Input Compression Point | IP1dB | @ 2.15 Ghz |  | +12 |  | dBm |
| $3{ }^{\text {rd }}$ Order Input Intercept Point | IP3 | @ 2.15 Ghz, <br> 1 MHz spacing, Pin $=-12 \mathrm{dBm} /$ tone |  | +25 |  | dBm |
| Tone/Voltage Detector Specifications |  |  |  |  |  |  |
| Polarization select threshold voltage |  | With external 10 nF series capacitor | 14.35 | 15.00 | 15.65 | V |
| Polarization switching time | tPoL | Polarization select voltage $=12$ to 18 V , <br> $50 \%$ to $90 \%$ RF |  | 1.6 |  | $\mu \mathrm{s}$ |

Table 3. SKY13419-365LF Electrical Specifications (2 of 2) (Note 1)
( $\mathrm{V}_{\mathrm{od}}=5 \mathrm{~V}$, $\mathrm{Top}=+2{ }^{\circ} \mathrm{C}, \mathrm{Pix}_{\mathrm{I}}=\mathbf{0 d B m}$, Characteristic Impedance $\left[Z_{0}\right]=50 \Omega$, Unless Otherwise Noted)


Table 4. SKY13419-365LF Truth Table

| State | $\begin{gathered} \text { V1 } \\ \text { Pin 12) } \end{gathered}$ | $\begin{gathered} \text { T1 } \\ \text { (Pin 11) } \end{gathered}$ | $\begin{gathered} \text { V2 } \\ \text { (Pin 4) } \end{gathered}$ | $\begin{gathered} \text { T2 } \\ \text { (Pin 5) } \end{gathered}$ | Signal Path with LGCTL Open Circuit |  | Signal Path with LGCTL Grounded |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  | Pin 18 (OMR) Open | Pin 18 (OMR) <br> Grounded | Pin 18 (0MR) Open | Pin 18 (OMR) Grounded |
| 1 | V Low | No Tone | V Low | No Tone | I1 to OUT2, I1 to OUT1 | 13 to OUT2, I3 to OUT1 | I2 to OUT2, I2 to OUT1 | 14 to OUT2, I4 to OUT1 |
| 2 | V Low | No Tone | V Low | 22 kHz Tone | I2 to OUT2, I1 to OUT1 | 14 to OUT2, I3 to OUT1 | I1 to OUT2, I2 to OUT1 | I3 to OUT2, I4 to OUT1 |
| 3 | V Low | No Tone | VHIGH | No Tone | I3 to OUT2, I1 to OUT1 | 11 to OUT2, 13 to OUT1 | 14 to OUT2, 12 to OUT1 | I2 to OUT2, 14 to OUT1 |
| 4 | V Low | No Tone | VHIGH | 22 kHz Tone | 14 to OUT2, I1 to OUT1 | 12 to OUT2, 13.to OUT1 | I3 to OUT2, I2 to OUT1 | I1 to OUT2, I4 to OUT1 |
| 5 | VLow | 22 kHz Tone | V Low | No Tone | I1 to OUT2, I2 to OUT1 | 3 to OUT2, 4 to 0UT1 | I2 to OUT2, <br> I1 to OUT1 | 14 to OUT2, I3 to OUT1 |
| 6 | V Low | 22 kHz Tone | V Low | 22 kHz Tone |  | 14 to 0UT2, 14 to OUT1 | I1 to OUT2, I1 to OUT1 | I3 to OUT2, I3 to OUT1 |
| 7 | V Low | 22 kHz Tone | VHIGH | No Tone | 13 toOUT2, 12 to OUT1 | 11 to 0UT2, I4 to OUT1 | 14 to OUT2, I1 to OUT1 | I2 to OUT2, I3 to OUT1 |
| 8 | V Low | 22 kHz Tone | VHIGH | 22 kHz Tone | 14 to OUT2, 12 to OUT | I2 to OUT2, I4 to OUT1 | I3 to OUT2, <br> I1 to OUT1 | I1 to OUT2, I3 to OUT1 |
| 9 | VHIGH | No Tone | V Low | No Tone | I1 to OUT2, 13 to OUT1 | I3 to OUT2, I1 to OUT1 | I2 to OUT2, I4 to OUT1 | 14 to OUT2, I2 to OUT1 |
| 10 | VHIGH | No Tone | V Low |  | I2 to OUT2, I3 to OUT1 | 14 to OUT2, I1 to OUT1 | I1 to OUT2, 14 to OUT1 | I3 to OUT2, <br> 2 to OUT1 |
| 11 | VHIGH | No Tone | VHIGH | No Tone | I3 to OUT2, I3 to OUT1 | I1 to OUT2, I1 to OUT1 | I4 to OUT2, I4 to OUT1 | I2 to OUT2, I2 to OUT1 |
| 12 | VHIGH | No Tone | VHIGH | 22 kHz Tone | 14 to OUT2, I3 to OUT1 | I2 to OUT2, I1 to OUT1 | I3 to OUT2, I4 to OUT1 | I1 to OUT2, I2 to OUT1 |
| 13 | VHIGH | 22 kHz Tone | V Low | No Tone | I1 to OUT2, I4 to OUT1 | I3 to OUT2, I2 to OUT1 | I2 to OUT2, I3 to OUT1 | I4 to OUT2, I1 to OUT1 |
| 14 | VHIGH | 22 kHz Tone | V Low | 22 kHz Tone | I2 to OUT2, I4 to OUT1 | 14 to OUT2, I2 to OUT1 | I1 to OUT2, I3 to OUT1 | I3 to OUT2, I1 to OUT1 |
| 15 | VHIGH | 22 kHz Tone | VHIGH | No Tone | I3 to OUT2, I4 to OUT1 | I1 to OUT2, I2 to OUT1 | 14 to OUT2, I3 to OUT1 | I2 to OUT2, I1 to OUT1 |
| 16 | VHIGH | 22 kHz Tone | VHIGH | 22 kHz Tone | I4 to OUT2, I4 to OUT1 | I2 to OUT2, I2 to OUT1 | I3 to OUT2, I3 to OUT1 | I1 to OUT2, I1 to OUT1 |
| 17 | No Voltage | No Tone | V Low | No Tone | 11 to OUT2 | 13 to OUT2 | 12 to OUT2 | 14 to OUT2 |
| 18 | No Voltage | No Tone | V Low | 22 kHz Tone | 12 to OUT2 | 14 to OUT2 | 11 to OUT2 | 13 to OUT2 |
| 19 | No Voltage | No Tone | VHIGH | No Tone | 13 to OUT2 | 11 to OUT2 | 14 to OUT2 | 12 to OUT2 |
| 20 | No Voltage | No Tone | VHIGH | 22 kHz Tone | 14 to OUT2 | 12 to OUT2 | 13 to OUT2 | 11 to OUT2 |
| 21 | VLow | No Tone | No Voltage | No Tone | 11 to OUT1 | 13 to OUT1 | 12 to 0UT1 | 14 to 0UT1 |
| 22 | V Low | 22 kHz Tone | No Voltage | No Tone | 12 to OUT1 | 14 to 0UT1 | 11 to OUT1 | 13 to OUT1 |
| 23 | VHIGH | No Tone | No Voltage | No Tone | I3 to OUT1 | 11 to OUT1 | 14 to OUT1 | 12 to OUT1 |
| 24 | VHIGH | 22 kHz Tone | No Voltage | No Tone | 14 to OUT1 | I2 to OUT1 | I3 to OUT1 | 11 to OUT1 |

Note: $\quad$ VLow $=10 \mathrm{~V} \sim 14.35 \mathrm{~V}$
VHIGH $=15.65 \mathrm{~V} \sim 21 \mathrm{~V}$
No Tone $=$ No 22 kHz tone present
22 kHz Tone $=22 \mathrm{kHz}$ tone present with amplitude greater than $100 \mathrm{mVp}-\mathrm{p}$
No Voltage $=<5 \mathrm{~V}$
Any state other than described in this Table places the switch into an undefined state. An undefined state will not damage the device.

## Typical Performance Characteristics

( $\mathrm{V}_{\mathrm{od}}=5 \mathrm{~V}$, $\mathrm{Top}_{\mathrm{op}}=+\mathbf{2 5}{ }^{\circ} \mathbf{C}$, $\mathrm{Pin}_{\mathrm{IN}}=\mathbf{0 d B m}$, Characteristic Impedance $\left[Z_{0}\right]=50 \Omega$, Unless Otherwise Noted)


Figure 3. I1 to OUT1 for States 1 to 24


Figure 5. I3 to OUT1 for States 1 to 24


Figure 4. $\mathbf{1 2}$ to OUT1 for States 1 to 24


Figure 6. I4 to OUT1 for States 1 to $\mathbf{2 4}$


Figure 7. I1 to OUT2 for States 1 to 24


Figure 9. I3 to OUT2 for States 1 to $\mathbf{2 4}$


Figure 8. I2 to OUT2 for States 1 to $\mathbf{2 4}$


Figure 10. 14 to OUT2 for States $\mathbf{1}$ to $\mathbf{2 4}$

## Evaluation Board Description

The SKY13419-365LF Evaluation Board is used to test the performance of the SKY13419-365LF 4x2 Switch Matrix. An Evaluation Board schematic diagram is provided in Figure 11. Component values for the SKY13419-365LF Evaluation Board are listed in Table 5. An assembly drawing for the Evaluation Board is shown in Figure 12.

## Package Dimensions

The PCB layout footprint for the SKY13419-365LF is provided in Figure 13. Typical case markings are shown in Figure 14. Package dimensions for the 20-pin QFN are shown in Figure 15, and tape and reel dimensions are provided in Figure 16.

## Package and Handling Information

Instructions on the shipping container label regarding exposure to moisture after the container seal is broken must be followed. Otherwise, problems related to moisture absorption may occur when the part is subjected to high temperature during solder assembly.
THE SKY13419-365LF is rated to Moisture Sensitivity Level 1 (MSL1) at $260^{\circ} \mathrm{C}$. It can be used for lead or lead-free soldering. For additional information, refer to the Skyworks Application Note, Solder Reflow Information, document number 200164.
Care merst be taken when attaching this product, whether it is done manuallyor in a production solder reflow environment. Production $q u a n t i t i e s ~ o f ~ t h i s ~ p r o d u c t ~ a r e ~ s h i p p e d ~ i n ~ a ~ s t a n d a r d ~$ tape and reel format.


Figure 11. SKY13419-365LF Evaluation Board Schematic

Table 5. SKY13419-365LF Evaluation Board Bill of Materials

| Component | Value |
| :--- | :--- |
| C1 | $10 \mu \mathrm{~F}$ |
| C2 | $1 \mu \mathrm{~F}$ |
| C3, C12, C13 | 10 nF |
| C4, C5, C14 | 1 nF |
| C6, C7, C8, C9, C10, C11 | $0 \Omega$ |
| R1, R5, R10, R12 | $0 \Omega$ |
| R2, R6 | $18 \mathrm{k} \Omega$ |
| R3, R4, R7, R8, R9, R11, R13 | DNP |

Figure 12. SKY13419-365LF Evaluation Board Assembly Diagram


Figure 14. Typical Case Markings
(Top View)


Figure 15. SKY13419-365LF 20-Pin QFN Package Dimensions


Figure 16. SKY13419-365LF Tape and Reel Dimensions

## Ordering Information

| Model Name | Manufacturing Part Number | Evaluation Board Part Number |
| :---: | :---: | :---: |
| SKY13419-365LF 4x2 Switch Matrix | SKY13419-365LF | SKY13419-365LF-EVB |



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