

DATA SHEET

AWB7223: 1.930 to 1.995 GHz Small-Cell Power Amplifier Module

Applications

- LTE, WCDMA, and HSDPA air interfaces
- Picocell, femtocell, home nodes
- Customer premises equipment
- Data cards and terminals

Features

- InGaP HBT technology
- -51 dBc ACPR @ ± 5 MHz, +27 dBm
- 30.5 dB gain
- High efficiency
- Low transistor junction temperature
- Matched for a 50 Ω system
- Low profile miniature surface-mount package; RoHS compliant
- Multi-carrier capability
- Surface-mount (14-pin, 7 × 7 × 1.3 mm) package (MSL3, 260 °C per JEDEC J-STD-020)



Skyworks Green™ products are compliant with all applicable legislation and are halogen-free. For additional information, refer to *Skyworks Definition of Green™*, document number SQ04-0074.



Description

The AWB7223 is a fully matched multi-chip module (MCM) designed for picocell, femtocell, and customer premises equipment (CPE) applications. Its high linearity and high-power high efficiency meet the extremely demanding needs of small-cell infrastructure architectures.

Designed for LTE, WCDMA, HSDPA air interfaces operating in the 1.930 GHz to 1.995 GHz band, the AWB7223 delivers up to +27 dBm of WCDMA (64 DPCH) power with an ACPR better than -50 dBc. The device operates from a convenient +4.5 V supply and provides 30.5 dB of gain. The device is manufactured using an advanced InGaP HBT MMIC technology offering state-of-the-art reliability, temperature stability, and ruggedness. The self-contained 7 mm x 7 mm x 1.3 mm surface-mount package incorporates RF matching networks optimized for output power, efficiency, and linearity in a 50 Ω system.

A block diagram of the AWB7223 is shown in Figure 1. The device package and pinout are shown in Figure 2. Signal pin assignments and functional pin descriptions are described in Table 1.

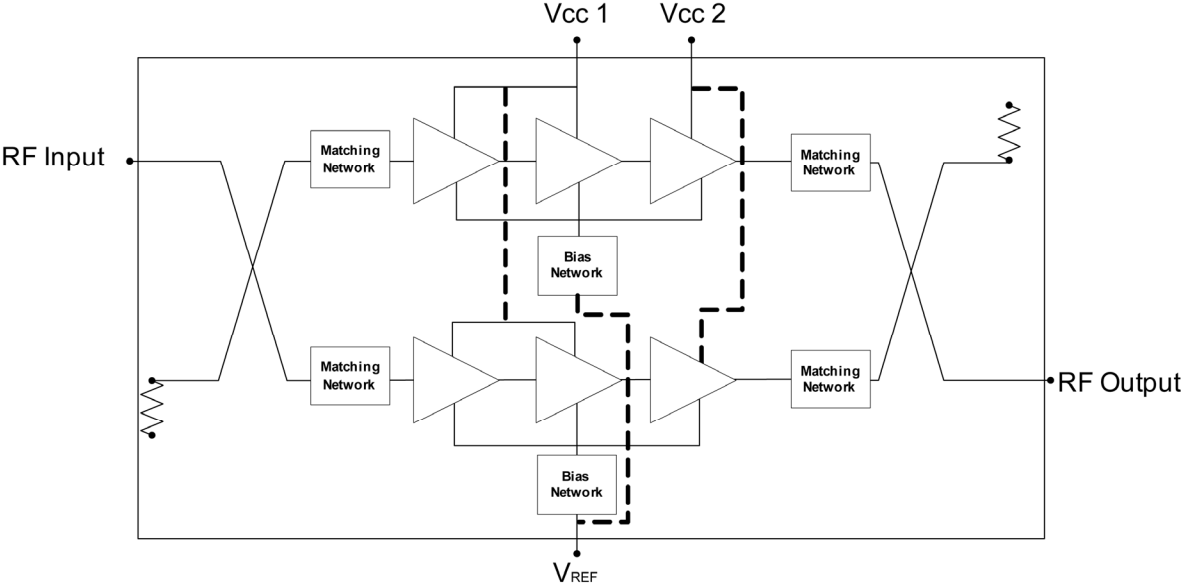


Figure 1. AWB7223 Block Diagram

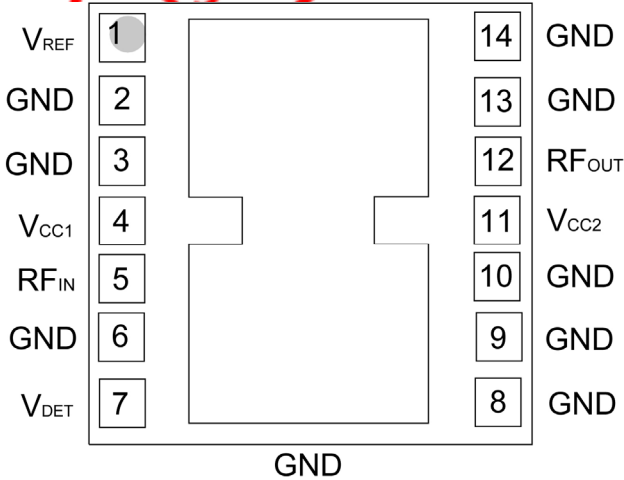


Figure 2. AWB7223 Pinout (Top View)

Table 1. AWB7223 Signal Pin Descriptions

Pin	Name	Description	Pin	Name	Description
1	VREF	Reference voltage	8	GND	Ground
2	GND	Ground	9	GND	Ground
3	GND	Ground	10	GND	Ground
4	VCC1	Supply voltage	11	VCC2	Supply voltage
5	RFIN	RF input	12	RFOUT	RF output
6	GND	Ground	13	GND	Ground
7	VDET	Detector output	14	GND	Ground

Electrical and Mechanical Specifications

The absolute maximum ratings of the AWB7223 are provided in Table 2. Recommended operating conditions are specified in Table 3, and electrical specifications are provided in Table 4.

Table 2. AWB7223 Absolute Maximum Ratings¹

Parameter	Minimum	Maximum	Units
Supply voltage (V _{CC})	0	+5	V
Reference voltage (V _{REF})	0	+3.5	V
RF output power (P _{OUT})		+30	dBm, modulated
RF input power (P _{IN})		+10	dBm, CW
Junction temperature (T _J)		+150	°C
Storage temperature (T _{STG})	-40	+150	°C
Electrostatic discharge:			
Human Body Model, Class 1C ²		2000	V
Charged Device Model, Class 4 ³		1000	V

¹ Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to device with only one parameter set at the limit and all other parameters set at or below their nominal value. Exceeding any of the limits listed here may result in permanent damage to the device.

² JEDEC JS-001-2010.

³ JEDEC JESD22-C101D.

ESD HANDLING: Although this device is designed to be as robust as possible, electrostatic discharge (ESD) can damage this device. This device must be protected at all times from ESD when handling or transporting. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD handling precautions should be used at all times.

Table 3. AWB7223 Recommended Operating Conditions¹

Parameter	Symbol	Min	Typ	Max	Units
Operating frequency	f	1930		1995	MHz
Supply voltage	V _{CC}	+3.6	+4.5	+4.65	V
Reference voltage:					
PA ON	V _{REF}	+2.80	+2.85	+2.90	V
PA OFF		0		+0.5	V
RF output power ²	P _{OUT}		+27		dBm
Case temperature ³	T _C	-40		+85	°C

¹ The device may be operated safely over these conditions; however, parametric performance is guaranteed only over the conditions defined in the electrical specifications.

² Typ RF output power is used during production test.

³ Case temperature references the board temperature at the ground paddle on the backside of the package.

Table 4. AWB7223 Electrical Specifications¹**(T_C = +25 °C, V_{CC} = +4.5 V, V_{REF} = +2.85 V, 50 Ω System)**

Parameter	Symbol	Test Condition	Min	Typ	Max	Units
Gain ²	G		27	30.5	34	dB
ACPR: ^{1,2,3} @ 10 MHz offset @ 20 MHz offset		10 MHz LTE BW		-51 -63	-47 -60	dBc dBc
Power-added efficiency ^{1,2,3}			12	15.5		%
Thermal resistance ⁴	R _{JC}	Junction to case		12		°C/W
Supply current ^{1,2,3}	V _{CC}	Total through VCC pins		720	930	mA
Quiescent current	I _{CQ}		196	227	290	mA
Reference current		Through V _{REF} pin	10	13	18	mA
Leakage current		V _{CC} = +5 V, V _{REF} = 0 V		3	10	μA
Harmonics: 2f _o 3f _o , 4f _o				-50 -54	-45 -50	dBc dBc
Input return loss			15	20		dB
Output return loss			15	20		dB
P1dB		CW tone		+35		dBm
Spurious output level (all spurious outputs)		P _{OUT} ≤ +27 dBm, in-band load VSWR < 5:1, Out-of-band load VSWR < 10:1, applies over all voltage and temperature operating ranges			-60	dBc
Load mismatch stress with no permanent degradation or failure		V _{CC} = +4.5 V, P _{OUT} = +27 dBm Applies over full operating temperature range	8:1			VSWR

¹ Measured at 1960 MHz.² P_{OUT} = +27 dBm.³ TM1 WCDMA 64 DPCH.⁴ Use only V_{CC2} (pin 11) current when calculating device junction temperature.

Evaluation Board Description

The AWB7223 Evaluation Board is used to test the performance of the AWB7223 device. A schematic of a typical application circuit is shown in Figure 3.

Shutdown Mode

The power amplifier can be placed in shutdown mode by applying logic low levels (see Operating Ranges table) to the V_{REF} voltage.

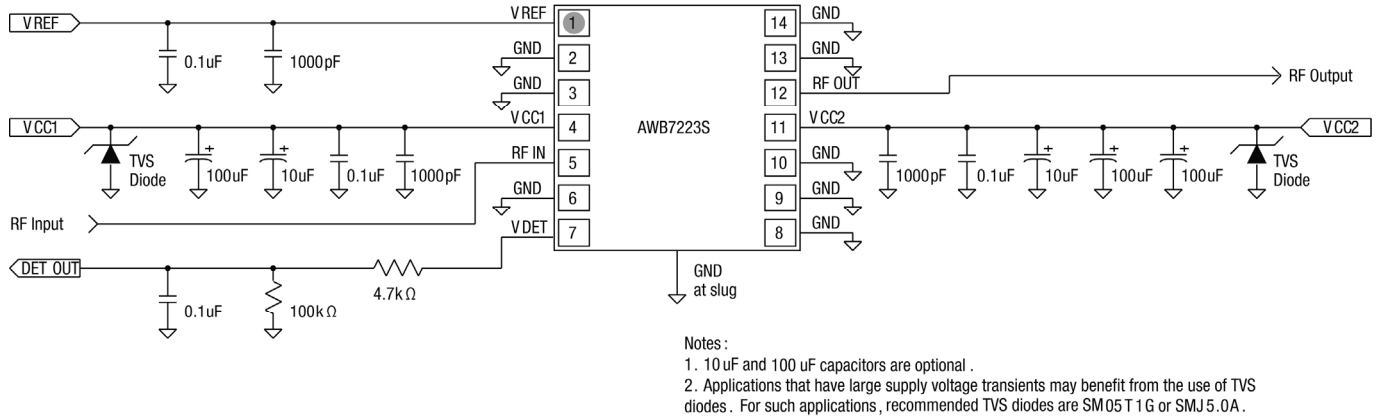


Figure 3. AWB7223 Evaluation Board Schematic

Package Dimensions

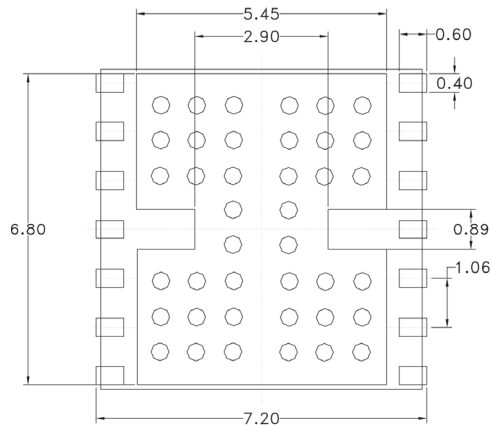
The PCB layout footprint drawing for the AWB7223 is shown in Figure 4. Typical part markings are shown in Figure 5. The package dimensions for the AWB7223 are shown in Figure 6. The tape and reel dimensions are provided in Figure 7.

Package and Handling Information

Since the device package is sensitive to moisture absorption, it is baked and vacuum packed before shipping. Instructions on the shipping container label regarding exposure to moisture after the container seal is broken must be followed. Otherwise, problems related to moisture absorption may occur when the part is subjected to high temperature during solder assembly.

The AWB7223 is rated to Moisture Sensitivity Level 3 (MSL3) at 260 °C. It can be used for lead or lead-free soldering. For additional information, refer to the Skyworks Application Note, *Solder Reflow Information*, document number 200164.

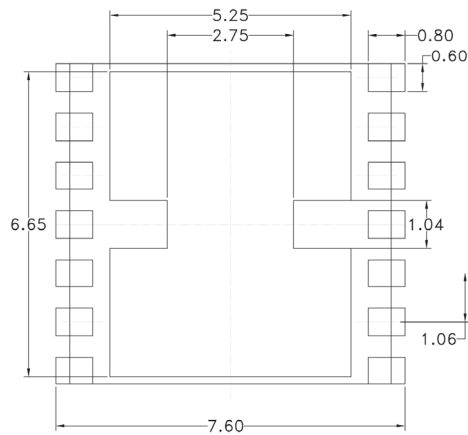
Care must be taken when attaching this product, whether it is done manually or in a production solder reflow environment. Production quantities of this product are shipped in a standard tape and reel format.



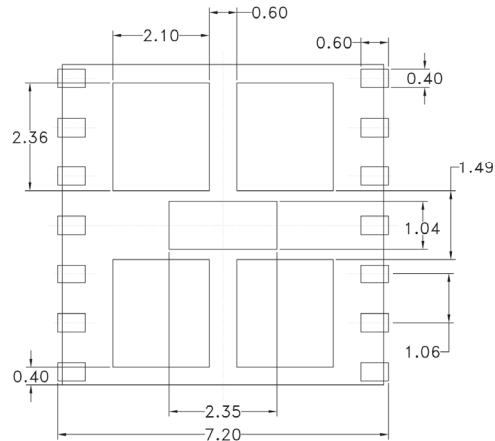
ONLY PACKAGE I/O's AND
GROUND REQUIREMENTS
SHOWN.

NOTES:

- (1) UNLESS SPECIFIED DIMENSIONS ARE SYMMETRICAL ABOUT CENTER LINES SHOWN.
- (2) DIMENSIONS IN MILLIMETERS.
- (3) VIAS SHOWN IN PCB METAL VIEW ARE FOR REFERENCE ONLY. NUMBER & SIZE OF THERMAL VIAS REQUIRED DEPENDENT ON HEAT DISSIPATION REQUIREMENT AND THE PCB PROCESS CAPABILITY.



PCB SOLDER MASK
TOP (X-RAY) VIEW



STENCIL APERTURE
TOP (X-RAY) VIEW

Figure 4. AWB7223 PCB Layout Footprint Dimensions

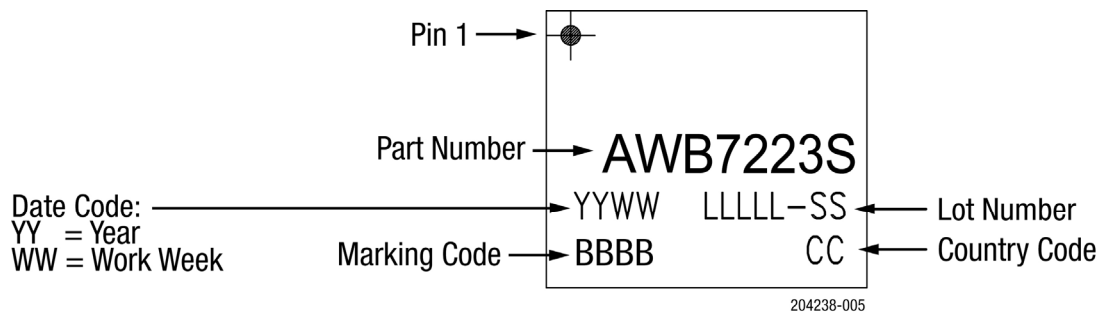
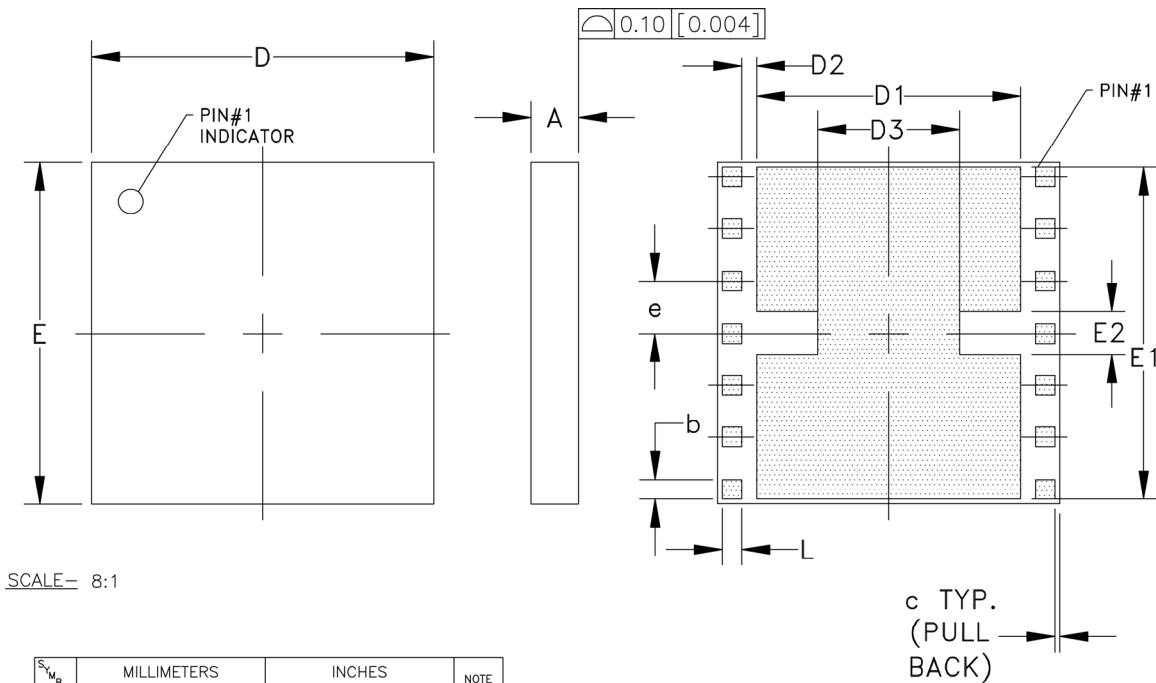


Figure 5. AWB7223 Typical Part Marking



S _W BOL	MILLIMETERS			INCHES			NOTE
	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.	
A	1.17	1.27	1.37	0.046	0.05	0.054	—
b	0.375	0.400	0.425	0.0148	0.0157	0.0167	14X
c	—	0.10	—	—	0.004	—	—
D	6.90	7.00	7.10	0.272	0.276	0.280	—
D1	—	5.40	—	—	0.213	—	—
D2	—	0.30	—	—	0.0118	—	—
D3	—	2.90	—	—	0.114	—	—
E	6.90	7.00	7.10	0.272	0.276	0.280	—
E1	—	6.80	—	—	0.268	—	—
E2	—	0.89	—	—	0.035	—	—
e	—	1.067	—	—	0.0420	—	6X
L	0.375	0.400	0.425	0.0148	0.0157	0.0167	14X

NOTES:

1. CONTROLLING DIMENSIONS: MILLIMETERS
2. UNLESS SPECIFIED TOLERANCE=±0.076[0.003].
3. PADS (INCLUDING CENTER) SHOWN UNIFORM SIZE FOR REFERENCE ONLY. ACTUAL PAD SIZE AND LOCATION WILL VARY WITHIN MIN. AND MAX. DIMENSIONS ACCORDING TO SPECIFIC LAMINATE DESIGN.

Figure 6. AWB7223 Package Dimensions

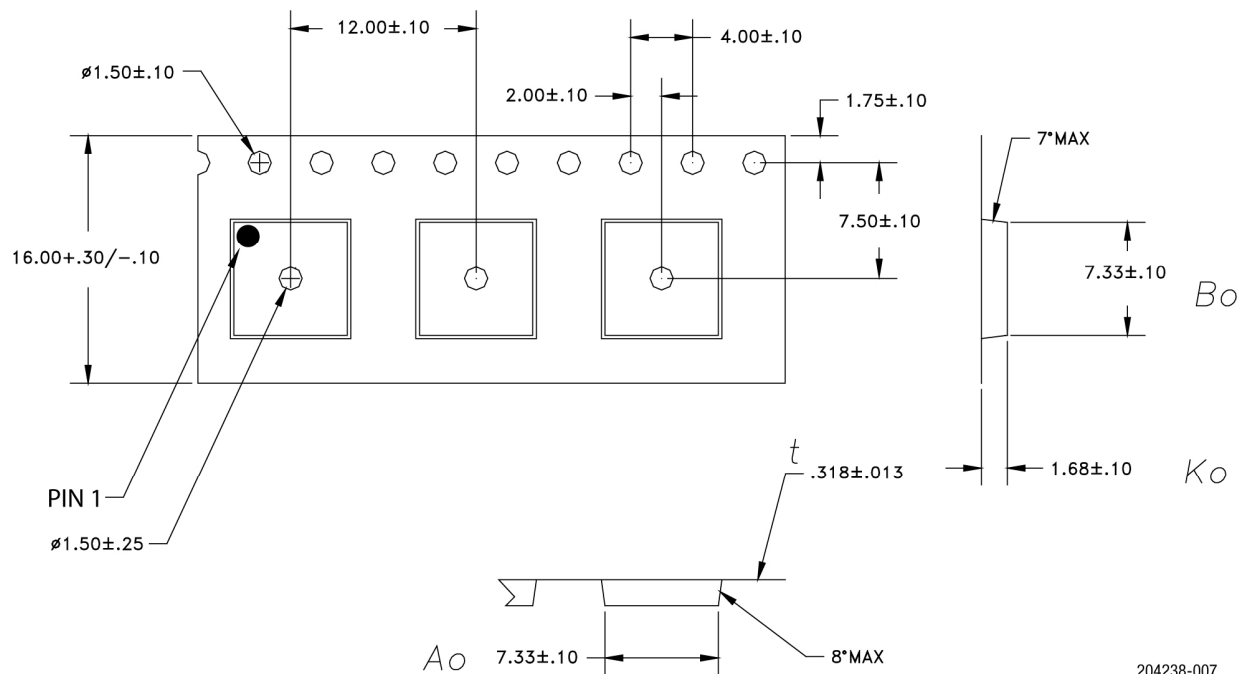


Figure 7. AWB7223 Tape and Reel Dimensions

Ordering Information

Part Number	Product Description	Component Packaging
AWB7223SM52P7	RoHS-compliant 14-pin 7 x 7 x 1.3 mm surface-mount module	Bags
AWB7223SM52P8	RoHS-compliant 14-pin 7 x 7 x 1.3 mm surface-mount module	Tape and reel, 2500 pieces per reel
AWB7223SM52P9	RoHS-compliant 14-pin 7 x 7 x 1.3 mm surface-mount module	Partial reel
EVB7222		Evaluation Board part number



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