## FEATURES

- InGaP HBT Technology
- -47 dBc ACPR @ $\pm 10 \mathrm{MHz},+27 \mathrm{dBm}$
- 29 dB Gain
- High Efficiency
- Low Transistor Junction Temperature
- Matched for a $50 \Omega$ System
- Low Profile Miniature Surface Mount Package; RoHS Compliant
- Multi-Carrier Capability


## APPLICATIONS

- LTE, WCDMA and HSDPA Air Interfaces
- FDD and TDD Systems
- Picocell, Femtocell, Home Nodes
- Customer Premises Equipment (CPE)



Figure 2: Pinout (X-ray Top View)

Table 1: Pin Description

| PIN | NAME | DESCRIPTION |
| :---: | :---: | :--- |
| 1 | VREF | Reference Voltage |
| 2 | GND | Ground |
| 3 | GND | Ground |
| 4 | Vcc1 | Supply Voltage |
| 5 | RFiN | RF Input |
| 6 | N/C | No Connection |
| 7 | N/C | No Connection |
| 8 | GND | Ground |
| 9 | GND | Ground |
| 10 | GND | Ground |
| 11 | Vcc2 | Supply Voltage |
| 12 | RFout | RF Output |
| 13 | GND | Ground |
| 14 | GND | Ground |

## ELECTRICAL CHARACTERISTICS

Table 2: Absolute Minimum and Maximum Ratings

| PARAMETER | MIN | MAX | UNIT |
| :---: | :---: | :---: | :---: |
| Supply Voltage (Vcc) | 0 |  |  |
| Reference Voltage (VREF) |  | +8.5 |  |
| RF Output Power (Pout) |  |  | odula |
| RF Input Power (Pin) |  |  | dBm, C |
| ESD Rating Human Body Model ${ }^{(1)}$ Charged Device Model |  | - |  |
| MSL Rating ${ }^{(3)}$ | 4 | - |  |
| Junction Temperature ( $\mathrm{JJ}^{\text {) }}$ | - | +150 | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature (Tsts) | -40 | +150 | ${ }^{\circ} \mathrm{C}$ |

Functional operation is not implied under these conditions. Exceeding any one or a combination of the Absolute Maximum Rating Conditions may cause permanent damage to the device. Exposure to absolute ratings for extended periods of time may adversely affect reliability.
Notes:
(1) JEDEC JS-001-2010.
(2) JEDEC JESD22-C101D.
(3) $260^{\circ} \mathrm{C}$ peak reflow.

Table 3: Operating Ranges

| PARAMETER | MIN | TYP | MAX | UNIT | COMMENTS |
| :--- | :---: | :---: | :---: | :---: | :--- |
| Operating Frequency (f) | 2300 | - | 2400 | MHz |  |
| Supply Voltage (Vcc) | +3.6 | +4.5 | +4.65 | V |  |
| Reference Voltage (VREF) | +2.80 <br> 0 | +2.85 <br> - | +2.90 <br> +0.5 | V | PA "on" <br> PA "shut down" |
| RF Output Power (PouT) ${ }^{(1)}$ | - | +27 | - | dBm |  |
| Case Temperature (Tc) ${ }^{(2)}$ | -40 | - | +85 | ${ }^{\circ} \mathrm{C}$ |  |

The device may be operated safely over these conditions; however, parametric performance is guaranteed only over the conditions defined in the electrical specifications.
Notes:
(1) Typ RF Output Power is used during production test.
(2) Case Temperature references the board temperature at the ground paddle on the backside of the package.

Table 4: Electrical Specifications
(Tc = +25 ${ }^{\circ} \mathrm{C}, \mathrm{Vcc}=+4.5 \mathrm{~V}, \mathrm{~V}_{\mathrm{ref}}=+2.85 \mathrm{~V}, 50 \Omega$ system)

| PARAMETER | MIN | TYP | MAX | UNIT | COMMENTS |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Gain ${ }^{(2)}$ | 26.5 | 29 | 33 | dB |  |
| ACPR ${ }^{(1), ~(2), ~(3)}$ <br> @ 10 MHz <br> @ 20 MHz | - | $\begin{aligned} & -47 \\ & -57 \end{aligned}$ | $\begin{aligned} & -45 \\ & -54 \end{aligned}$ | dBc |  |
| Power-Added Efficiency ${ }^{(1), ~(2), ~(3) ~}$ | 12 | 15 |  |  |  |
| Thermal Resistance (RJc) ${ }^{(4)}$ | - | $112$ |  | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ | Junction to Case |
| Supply Current ${ }^{(1), ~(2), ~(3) ~}$ | - | 740 | 92 | m | total through Vcc pins |
| Quiescent Current (lcq) | - | 250 | 00 | mA |  |
| Reference Current | 10 |  |  | mA | through Vref pin |
| Leakage Current | - |  |  | $\mu \mathrm{A}$ | $\mathrm{Vcc}=+4.65 \mathrm{~V}, \mathrm{~V}_{\text {ref }}=0 \mathrm{~V}$ |
| Harmonics <br> 2fo, 4fo <br> 3fo |  | $\begin{aligned} & -50 \\ & -45 \end{aligned}$ | $\begin{aligned} & -45 \\ & -40 \end{aligned}$ | dBc |  |
| Input Return Loss | 15 | 18 | - | dB |  |
| Output Return Loss | 15 | 18 | - | dB |  |
| P1dB | - | +34.5 | - | dBm | CW Tone |
| RF Switching Time ${ }^{(5)}$ Rise Time (PA "off" to "on") Fall Time (PA "on" to "off") | - | - | $\begin{gathered} 12 \\ 4 \end{gathered}$ | $\mu \mathrm{S}$ | Vcc $=+4.5 \mathrm{~V}$, Vref switched between 0 V and +2.85 V |
| Spurious Output Level (all spurious outputs) | - | - | -60 | dBc | Pout $\leq+27 \mathrm{dBm}$ <br> In-band load VSWR < 5:1 <br> Out-of-band load VSWR < 10:1 <br> Applies over all voltage and temperature operating ranges |
| Load mismatch stress with no permanent degradation or failure | 8:1 | - | - | VSWR | $\mathrm{Vcc}=+4.5 \mathrm{~V}$, Pout $=+27 \mathrm{dBm}$ Applies over full operating temperature range |

## Notes:

(1) Measured at 2350 MHz .
(2) Pout $=+27 \mathrm{dBm}$.
(3) E-TM1.1 LTE 10 MHz BW.
(4) Use only Vcc2 (pin 11) current when calculating device junction temperature.
(5) Rise Time defined from time at which VREF is switched from 0 V to +2.85 V , to time at which the RF output power achieves $90 \%$ of the average steady-state "on" level; Fall Time defined from time at which VREF is switched from +2.85 V to 0 V , to time at which the RF output power decreases to $10 \%$ of the average steady-state "on" level.

## APPLICATION INFORMATION

To ensure proper performance, refer to all related Application Notes.

## Shutdown Mode

The power amplifier may be placed in a shutdown mode by applying logic low levels (see Operating Ranges table) to the Vref voltage.


## Notes:

1. Applications that have large supply voltage transients may benefit from the use of TVS diodes. For such applications, recommended TVS diodes are SM05T1G or SMJ5.0A.
2. To achieve the RF Switching Time specifications listed in Table 4, the maximum recommended capacitance on the $V_{\text {REF }}$ line is $0.01 \mu \mathrm{~F}$. The noise on the $\mathrm{V}_{\text {REF }}$ line should be kept as low as possible to minimize required capacitance.

Figure 3: Application Circuit Schematic


Figure 4: PCB Footprint

## PACKAGE OUTLINE



SCALE- 8:1

|  | MILLIMETERS |  |  | INCHES |  |  | note |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | MIN. | NоM. | max. | MIN. | NoM. | max. |  |
| A | 1.17 | 1.27 | 1.37 | 0.046 | 0.05 | 0.054 | - |
| b | 0.375 | 0.400 | 0.425 | 0.0148 | 0.0157 | 0.0167 | 14X |
| c | - | 0.10 | - | - | 0.004 | - | - |
| D | 6.90 | 7.00 | 7.10 | 0.272 | 0.276 | 0.280 | - |
| D1 | - | 5.40 | - | - | 0.213 | - | - |
| D2 | - | 0.30 | - | - | 0.0118 | - |  |
| D3 | - | 2.90 | - | - | 0.114 | - |  |
| E | 6.90 | 7.00 | 7.10 | 0.272 | 0.276 | 0.280 | - |
| E1 | - | 6.80 | - | - | 0.268 | - | - |
| E2 | - | 0.89 | - | - | 0.035 | - |  |
| e | - | 1.067 | - | - | 0.0420 | - | 6X |
| L | 0.375 | 0.400 | 0.425 | 0.0148 | 0.0157 | 0.0167 | 14X |



## NOTES:

1. CONTROLLING DIMENSIONS: MILLIMETERS
2. UNLESS SPECIFIED TOLERANCE $= \pm 0.076[0.003]$.
3. PADS (INCLUDING CENTER) SHOWN

UNIFORM SIZE FOR REFERENCE ONLY.
ACTUAL PAD SIIE AND LOCATON WLI
ACCORDING TO SPECIFIC LAMINATE DESIGN.

Figure 5: Package Outline - 14 Pin $7 \mathrm{~mm} \times 7 \mathrm{~mm} \times 1.3 \mathrm{~mm}$ Surface Mount Module


Figure 6: Branding Specification

## COMPONENT PACKAGING



Figure 7: Tape \& Reel Packaging

Table 5: Tape \& Reel Dimensions

| PACKAGE TYPE | TAPE WIDTH | POCKET PITCH | REEL <br> CAPACITY | MAX REEL DIA |
| :---: | :---: | :---: | :---: | :---: |
| $7 \mathrm{~mm} \times 7 \mathrm{~mm} \times 1.3 \mathrm{~mm}$ | 16 mm | 12 mm | 2500 | $13^{\prime \prime}$ |

ORDERING INFORMATION

| ORDER NUMBER | TEMPERATURE RANGE | PACKAGE DESCRIPTION | COMPONENT PACKAGING |
| :---: | :---: | :---: | :---: |
| AWB7232P7 | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | RoHS-compliant 14 Pin $7 \mathrm{~mm} \times 7 \mathrm{~mm} \times 1.3 \mathrm{~mm}$ Surface Mount Module |  |
| AWB7232P8 | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | RoHS-compliant 14 Pin $7 \mathrm{~mm} \times 7 \mathrm{~mm} \times 16 \mathrm{~mm}$ Surface MountModrle | and Reel, 2500 pieces per Reel |
| AWB7232P9 | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | RoHS-compliant 14 in $7 \mathrm{~mm} \times 7 \mathrm{~mm} \times 1, \mathrm{~mm}$ Surface Mount Module | Partial Reel |


#### Abstract

AWB7232

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