

#### DATA SHEET

# **OLH5500/5501: Hermetic High-Speed Optocouplers**

#### **Features**

- Rugged, reliable hermetic Dual Inline Package (DIP)
- Performance guaranteed over full military temperature range
- High isolation voltage, 3000 Vpc
- High-speed, 400 Kbps typical
- Open collector output
- High common mode transient immunity >10,000 V/µs at  $V_{\text{CM}}=350$  V
- Radiation tolerant design

#### **Description**

The OLH5500/5501 are hermetic 8-pin DIP optocouplers for wide bandwidth analog applications, as well as for interfacing Transistor-to-Transistor Logic (TTL) to Low-Power Schottky Transistor-Transistor Logic (LSTTL) or Complementary Metal Oxide Semiconductors (CMOS). The OLH5501 product is a 100 percent high-reliability screened version of the OLH5500.

Each unit consists of an Aluminum Gallium Arsenide (AlGaAs) LED optically coupled to an integrated photodiode transistor detector. The separate photodiode and transistor configuration improves speed performance significantly over phototransistors. The base of the transistor is available for gain and bandwidth adjustments.

The OLH5500/5501 products are functionally compatible to 6N135, 6N136, 4N55, and HCPL5500/5501 optocouplers, but with better common mode transient immunity. Special Current Transfer Ratio (CTR) selection or no-transistor base connection versions for improved noise immunity is available upon request.

The performance of the OLH5500/5501 under a radiation environment is significantly improved over standard phototransistors.

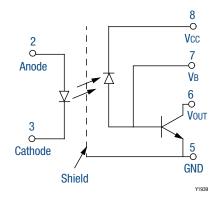


Figure 1. OLH5500/5501 Block Diagram

Figure 1 shows the OLH5500/5501 functional block diagram. Table 1 provides the OLH5500/5501 absolute maximum ratings. Table 2 provides the OLH5500/5501 electrical specifications.

Figures 2 through 4 illustrate the 0LH5500/5501 typical performance characteristics. Figure 5 shows the 0LH5500/5501 switching test circuit. Figure 6 provides the 0LH5500/5501 package dimensions.

Parameter	Symbol	Minimum	Maximum	Units
Coupled				
Input to output isolation voltage	VDC	-3000	+3000	V
Storage temperature range	Тята	-65	+150	٥°
Operating temperature range	Та	-55	+125	°C
Lead temperature (1.6 mm below seating plane)			+260 for 10 sec	°C
Input Diode				
Average input current	lod		20	mA
Peak forward current (≤1 ms duration)	lF		40	mA
Reverse voltage	VR		3	V
Input power dissipation	IPD		36	mW
Output Detector				
Average output current			8	mA
Peak output current			16	mA
Supply voltage	Vcc	-0.5	+18.0	۷
Output voltage	Vout	-0.5	+18.0	۷
Power dissipation	PD		50	mW
Emitter base reverse voltage	Vebr		3	۷
Base current			5	mA

Note 1: Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to device with only one parameter set at the limit and all other parameters set at or below their nominal value. Exceeding any of the limits listed here may result in permanent damage to the device.

**CAUTION**: Although this device is designed to be as robust as possible, electrostatic discharge (ESD) can damage this device. This device must be protected at all times from ESD. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD precautions should be used at all times.

Parameter	Symbol	Test Condition	Minimum	Typical	Maximum	Units
Current transfer ratio (Note 2)	CTR	$I_F = 16 \text{ mA}, V_0 = 0.4 \text{ V}, V_{CC} = 4.5 \text{ V}$	12	25		%
Logic high output current	Іон	$I_F = 0 \text{ mA}, V_0 = V_{CC} = 18 \text{ V}$		5	100	μA
Logic low supply current	ICCL	IF = 16 mA, Vcc = 18 V		35	250	μA
Logic high supply current	Іссн	$I_F = 0 \text{ mA}, \text{Vcc} = 18 \text{ V}$		0.1	10.0	μA
Input forward voltage	VF	IF = 10 mA		1.75	2.5	V
Input reverse breakdown voltage	Bvr	Ir = 10 μA	3			V
Input to output leakage current (Note 3)	lı_o	$\label{eq:relative} \begin{array}{l} \mbox{Relative humidity} \le 45\%, \mbox{ TA} = 25 \ ^{\circ}\mbox{C}, \\ \mbox{V}_{I\_0} = 3000 \ \mbox{V}_{DC}, \mbox{ t} = 1s \end{array}$			1	μA
Propagation Delay Time:						
Logic high to low	tphL	$\label{eq:lf} \begin{array}{l} {\sf I}_{\sf F}=16 \mbox{ mA}, \mbox{ R}_{\sf L}=8.2  k\Omega, \mbox{ Vcc}=5 \mbox{ V}, \\ {\sf C}_{\sf L}=50 \mbox{ pF} \end{array}$		0.3	1.0	μs
Logic low to high	tplh	$\label{eq:lf} \begin{array}{l} {\sf I}_{\sf F}=16 \mbox{ mA},  {\sf R}_{\sf L}=8.2  k\Omega,  {\sf V}_{\sf CC}=5 \mbox{ V}, \\ {\sf C}_{\sf L}=50  {\sf p}{\sf F} \end{array}$		0.8	3.0	μs
Common mode transient immunity:						
Logic high level	СМн	$\label{eq:lf} \begin{array}{l} I_F=0 \mbox{ mA, RL}=8.2 \mbox{ k}\Omega, \\ V_{CM}=350 \mbox{ V p-p, TA}=25 \mbox{ °C} \end{array}$	5	>10		kV/µs
Logic low level	CM∟	$\label{eq:lf} \begin{array}{l} {\sf I}_{\sf F}=16 \mbox{ mA}, \mbox{ R}_{\sf L}=8.2 \mbox{ k}\Omega, \\ {\sf V}_{\sf CM}=350 \mbox{ V} \mbox{ p-p}, \mbox{ T}_{\sf A}=25 ^{\circ}\mbox{C} \end{array}$	5	>10		kV/µs

## Table 2. 0LH5500/5501 Electrical Specifications (Note 1) ( $T_A = -55$ °C to +125 °C, Unless Otherwise Noted)

Note 1: Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to the device with only one parameter set at the limit and all other parameters set at or below their nominal value. Exceeding any of the limits listed here may result in permanent damage to the device.

Note 2: Current transfer ratio is defined as the ratio of the output collector current Ic to the forward LED current IF, multiplied by 100%.

Note 3: Measured between pins 1, 2, 3, and 4 shorted together, and pins 5, 6, 7, and 8 shorted together.

### **Typical Performance Characteristics**

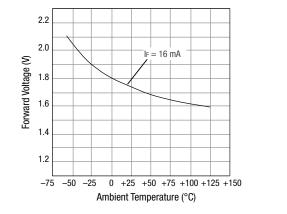


Figure 2. LED Forward Voltage vs Temperature

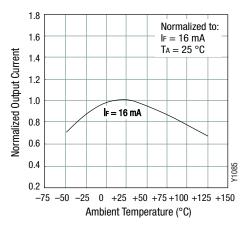


Figure 3. Normalized Output Current vs Temperature

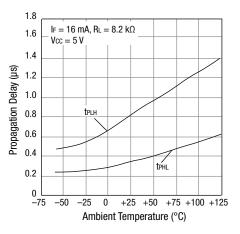


Figure 4. Propagation Delay vs Temperature

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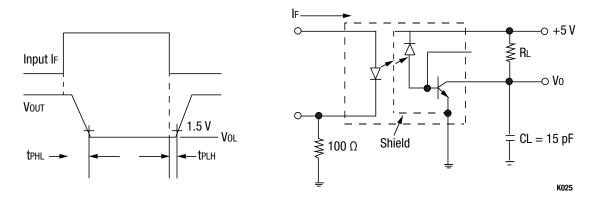
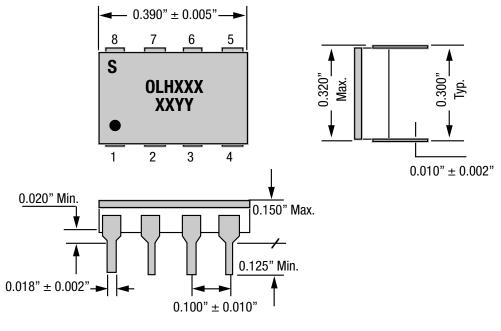


Figure 5. OLH5500/5501 Switching Test Circuit



K021

Figure 6. 0LH5500/5501 Package Dimensions

#### **Ordering Information**

Model Name	Manufacturing Part Number
OLH5500/5501: Hermetic High-Speed Optocouplers	OLH5500/5501

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