

DATA SHEET

SKY77340 PA Module for Quad-Band GSM / EDGE

Applications

Quad-band cellular handsets:

GMSK Modulation

- Class 4 GSM850/900
- Class 1 DCS1800/PCS1900
- Class 12 GPRS multi-slot operation

EDGE modulation

- Class E2 GSM850/900
- Class E2 DCS1800/ PCS1900

Features

- High efficiency:
 - GSM850 54%
 - GSM900 53%
 - DCS 52%
 - PCS 52%
- Input/Output matching 50 Ω internal (with DC blocking)
- 16-pin MCM
- Small outline
- 6 x 8 mm
- Low profile
- 1.2 mm
- Gold-plated, lead-free contacts
- contactsLow APC current
 - 10 uA

Description

The SKY77340 Power Amplifier Module (PAM) is designed in a compact form factor for quad-band cellular handsets comprising GSM850/900, DCS1800, PCS1900, supporting GMSK and linear EDGE modulation. Class 12 General Packet Radio Service (GPRS) multi-slot operation is also supported.

The module consists of a GSM850/900 PA block and a DCS1800/PCS1900 PA block, impedancematching circuitry for 50 Ω input and output impedances, and a Multi-function Power Amplifier Control (MFC) block. A custom CMOS integrated circuit provides the internal MFC function and interface circuitry.

Two separate Heterojunction Bipolar Transistor (HBT) PA blocks are fabricated onto InGaP die; one supports the GSM850/900 bands, the other supports the DCS1800 and PCS1900 bands. Both PA blocks share common power supply pins to distribute current. The InGaP die, the silicon die, and the passive components are mounted on a multi-layer laminate substrate. The assembly is encapsulated with plastic overmold.

RF input and output ports are internally matched to 50 Ω to reduce the number of external components. Extremely low leakage current (2.5 μ A, typical) maximizes handset standby time. Band select (BS) circuitry selects GSM transmit frequency band (logic 0) and DCS/PCS transmit frequency band (logic 1). MODE circuitry selects GMSK modulation (logic 0) or EDGE modulation (logic 1). VRAMP controls the output power for GMSK modulation and provides bias optimization for EDGE modulation depending on the state of MODE control.

The integrated multi-function control (MFC) provides envelope amplitude control in GMSK mode, reducing sensitivity to input drive, temperature, power supply, and process variation. In EDGE mode, the MFC configures the PA for fixed gain, and provides the ability to optimize the PA bias operation at different power levels. This circuitry regulates PA bias conditions, reducing sensitivity to temperature, power supply, and process variation. The Enable input signal (pin 8) provides a standby state to minimize battery drain.

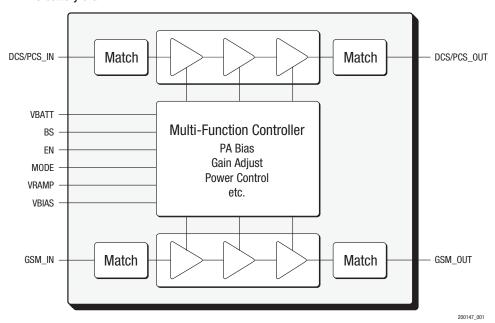


Figure 1. SKY77340 Functional Block Diagram

NEW Skyworks offers lead (Pb)-free "environmentally friendly" packaging that is RoHS compliant (European Parliament for the Restriction of Hazardous Substances).

Electrical Specifications

The absolute maximum ratings of the SKY77340 are provided in Table 1 and the recommended operating conditions are specified

in Table 2. Table 3 provides the control logic and Table 4 provides the electrical specifications.

Table 1. Absolute Maximum Ratings

Parameter	Symbol	Minimum	Typical	Maximum	Unit				
RF Input Power	Pin	—	—	12	dBm				
Case Storage Temperature	Tstg	-55	—	150	°C				
Supply Voltage	VBATT	-0.3	—	7.0	V				
Power Control Voltage	VRAMP	-0.3	—	VBATT	V				
PA Bias Voltage	VBIAS	-0.3	—	VBATT	V				
Transmit enable	EN	-0.3	—	VBATT	V				
Band select	BS	-0.3	—	VBATT	V				
Mode select	MODE	-0.3		VBATT	V				

Table 2. SKY77340 Recommended Operating Conditions

Parameter	Symbol	Minimum	Typical	Maximum	Unit
Transmit Duty Cycle ¹	Dтx	1/8	_	1/2	_
Case Operating Temperatures ¹ 1-Slot (12.5% duty cycle) 2-Slot (25% duty cycle) 3-Slot (37.5% duty cycle) 4-Slot (50% duty cycle)	(Trange)	25 25 25 25		100 100 85 85	°C
Voltage Operating Range	Vrange	3.0	3.5	4.8	V

¹ TFRAME = 4.615 mS

Operational State	EN	BS	MODE	NOTES
Standby	0	Х	Х	X = don't care
Low band GMSK	1	0	0	VRAMP controls output power
Low band EDGE	1	0	1	VBIAS sets PA bias condition, fixed gain PA
High band GMSK	1	1	0	VRAMP controls output power
High band EDGE	1	1	1	VBIAS sets PA bias condition, fixed gain PA

Table 3. SKY77340 Control Logic

			General				
Par	rameter	Symbol	Test Condition	Minimum	Typical	Maximum	Units
Supply voltage		VBATT	_	3.0	3.5	4.8	۷
Analog power control imp	pedance	Zramp	-		200	—	kΩ
Analog PA bias control in	npedance	Zbias	—	—	200	—	kΩ
ENable	Control voltage LOW Control voltage HIGH	Ven	_	0.0 1.5		0.5 Vbatt	V
ENable current		IEN	—		_	30	μA
Band select	Control voltage LOW Control voltage HIGH	VBS	_	0.0 1.5		0.5 Vbatt	V
Band select current		IBS	—		_	30	μA
MODE		Vmode	GMSK EDGE	0.0 1.5		0.5 Vbatt	V
		Imode	$V_{MODE} \le 2.7 V$	—	_	30	μA
"Off" Current		Ioff	Sum Current on all pins $V_{BATT} = 3.5 V$ $EN \le 0.1 V$ $V_{RAMP} \le 0.1 V$ $MODE \le 0.1 V$ Temp = 25 °C	_	_	30	μA
"On" Current		Іватт мах		_	_	2.2	А

Table 4. SKY77340 Electrical Specifications (1 of 17)

Table 4. SKY77340 Electrical Specifications (2 of 17)

	Mode: Ti	ransmit GMSK and EDGE ¹ Band: CEL					
		General Test Conditions					
Frequency = 824-849 MHz Pulse Rate = 12.5% Duty Cycle	TFRAME = 4.615 r Control States: E		RF Ports = GSM_IN, GSM_OUT				
Parameter	Symbol	Condition	Minimum	Typical	Maximum	Unit	
	Pgsm	$ \begin{array}{l} \mbox{Vcc} = 3.5 \mbox{ V} \\ \mbox{Tcase} = 25 \mbox{ °C} \\ \mbox{-1 } \mbox{dBm} \leq \mbox{Pin} \leq 6 \mbox{ dBm} \end{array} $	34.5	35.1	_		
	Pgsm_ex-l	$\begin{array}{l} Vcc = 3.0 \ V \\ Tcase = Trange \\ -1 \ dBm \leq Pin \leq 6 \ dBm \end{array}$	32.0	33.7	—		
Output Power	Pgsm_ex-h	Vcc = 4.8 V Tcase = Trange $-1 dBm \le Pin \le 6 dBm$	34.5	35.9	—	dBm	
	Pedge	$V_{CC} = 3.5 V$ $T_{CASE} = 25 °C$ $P_{OUT} = P_{IN} + G_{EDGE}$	28.5	_	_		
	Pedge_ex	Vcc = Vrange Tcase = Trange	26.0	_	_		
	Gedge	Vcc = 3.5 V VBIAS = 1.4 V POUT = PEDGE TCASE = 25 °C MODE = HIGH Load = 50 ohms	31.5	33.0	34.5	dB	
Linear Gain		$\begin{array}{l} \mbox{Vcc} = 3.0 \mbox{ V SBATT} < 4.8 \mbox{ V} \\ \mbox{VBIAS} = 1.4 \mbox{ V} \\ \mbox{Pout} = \mbox{Pedge} \\ \mbox{Tcase} = \mbox{Trange} \\ \mbox{MODE} = \mbox{HIGH} \\ \mbox{Load VSWR} \leq 3:1 \mbox{ all phase angles} \end{array}$	27.0	33.0	36.0	ŭĐ	
	Gvar	Vcc = 3.5 V Tcase = Trange MODE = HIGH	_	-0.034	_	dB/C	
Power Added Efficiency	PAEgsm	Vcc = 3.5 V $Tcase = 25 °C$ $Vramp = 1.6 V$ $PiN = 3 dBm$	49	54	_	%	
	PAEEdge	Vcc = 3.5 V Tcase = 25 °C Pout = 28.5 dBm	20	23	_		
	Igsm_low_power	Vcc = 3.5 V Tcase = 25 °C Pout = 6.5 dBm	_	116	130		
Low Power Current Consumption	ledge_low_power	Vcc = 3.5 V VBIAS = 0.3 V TCASE = 25 °C POUT = 6.5 dBm	_	120	_	mA	

		Mode: Transmit	t GMSK and EDGE ¹ Band: CEL [cont	tinued}				
			General Test Conditions					
Frequency = 824-849 MHz Pulse Rate = 12.5% Duty Cycle	9	TFRAME = 4.615 Control States:		RF Ports = GS	RF Ports = GSM_IN, GSM_OUT			
Paramet	er	Symbol	Condition	Minimum	Typical	Maximum	Unit	
	20 MHz Offset	NxSat	$\label{eq:Vcc} \begin{array}{l} \mbox{Vcc} = 3.5 \mbox{ V} \\ \mbox{Tcase} = 25 \mbox{ °C} \\ \mbox{Pout} \leq \mbox{Pgsm} \\ \mbox{RBW} = 100 \mbox{ kHz} \end{array}$	_	-85.0	-83.5		
Noise Power -	20 1112 011361	NxLin	$\begin{array}{l} \mbox{Vcc} = 3.5 \mbox{ V} \\ \mbox{Tcase} = 25 \mbox{ °C} \\ \mbox{Pout} \leq \mbox{Pedge} \\ \mbox{RBW} = 100 \mbox{ kHz} \end{array}$	_	-84.0	-83.5	dBm	
	1930 to 1990 MHz	NXSAT _PCS	$\label{eq:Vcc} \begin{array}{l} \mbox{Vcc} = 3.5 \mbox{ V} \\ \mbox{Tcase} = 25 \mbox{ °C} \\ \mbox{Pout} \leq \mbox{Pgsm} \\ \mbox{RBW} = 100 \mbox{ kHz} \end{array}$	_	-100.0	-84.0	UDIII	
	1920 (0 1990 MHZ	NxLin _PCS	$Vcc = 3.5 V$ $Tcase = 25 °C$ $Pout \le Pedge$ $RBW = 100 \text{ kHz}$		-100.0	-100.0 -84.0		
Forward Isolation		lso	$\begin{array}{l} Vcc = V_{RANGE} \\ T_{CASE} = T_{RANGE} \\ P_{IN} \leq 6 \ dBm \\ EN = 0 \end{array}$	_	-40	-30	dBm	
Crossover Isolation	Fundamental	lso_CEL	Vcc = Vrange Tcase = Trange	_	-10	0	dBm	
	2 nd Harmonic	lso_DCS	Vcc = Vrange Tcase = Trange	_	-30	-20	UDIII	
Input VSWR		VSWR_SAT	$\begin{array}{l} 6.5 \text{ dBm} \leq \text{Pout} \leq \text{Pgsm} \\ \text{Vcc} = \text{Vrange} \\ \text{Tcase} = \text{Trange} \end{array}$	_	1.8	2.25	Ratio	
input vown		VSWR_LIN	$\begin{array}{l} \text{Vcc} = \text{Vrange} \\ \text{Tcase} = \text{Trange} \\ \text{Pout} \leq \text{Pedge} \end{array}$	_	1.2	2.0	nauo	
		2fo	Measured at GSM_OUT POUT \leq PGSM VCC = VRANGE TCASE = TRANGE Load = 50 ohms	_	-25	-10	40	
Harmonics		3fo to 15fo	Measured at GSM_OUT Pout \leq PGSM Vcc = VRANGE TcASE = TRANGE Load = 50 ohms	_	-25	-17	dBm	

Table 4. SKY77340 Electrical Specifications (3 of 17)

		Mode: Transmit	GMSK and EDGE ¹ Band: CEL [continue	ed}			
			General Test Conditions				
Frequency = 824-849 MHz		TFRAME = 4.615 r		RF Ports = GS	M_IN, GSM_	DUT	
Pulse Rate = 12.5% Duty Cycle Parameter		Control States: E Symbol	N = 1, BS = 0 Condition	Minimum	Typical	Maximum	Unit
	ACPR1		$\begin{array}{l} \text{RBW} = 30 \text{ kHz} \\ \text{Pout} \leq \text{Pedge} \\ \text{Vcc} = 3.5 \text{ V} \\ \text{Tcase} = 25 \ ^{\circ}\text{C} \\ \text{MODE} = \text{HIGH} \\ \text{Offset} = \pm 200 \text{ kHz} \\ \text{Load} = 50 \text{ Ohms} \end{array}$	_	-37	-33	
			$\begin{array}{l} \text{RBW} = 30 \text{ kHz} \\ \text{POUT} \leq \text{PEDGE}_\text{EX} \\ \text{VCC} = \text{VRANGE} \\ \text{TCASE} = \text{TRANGE} \\ \text{MODE} = \text{HIGH} \\ \text{Offset} = \pm 200 \text{ kHz} \\ \text{Load VSWR} \leq 3:1, \text{ all phase angles} \end{array}$	_	-35	_	
			$\begin{array}{l} \text{RBW} = 30 \text{ kHz} \\ \text{Pout} \leq \text{Pedge} \\ \text{Vcc} = 3.5 \text{ V} \\ \text{Tcase} = 25 \ ^{\circ}\text{C} \\ \text{MODE} = \text{HIGH} \\ \text{Offset} = \pm 400 \text{ kHz} \\ \text{Load} = 50 \text{ Ohms} \end{array}$	_	-60	-58	
ACF	ACPR2		$\begin{array}{l} \text{RBW} = 30 \text{ kHz} \\ \text{Pout} \leq \text{Pedge}_\text{EX} \\ \text{Vcc} = \text{VRANGE} \\ \text{Tcase} = \text{TRANGE} \\ \text{MODE} = \text{HIGH} \\ \text{Offset} = \pm 400 \text{ kHz} \\ \text{Load VSWR} \leq 3:1, \text{ all phase angles} \end{array}$	_	-57	_	
Adjacent Channel Leakage			$\begin{array}{l} \text{RBW} = 30 \text{ kHz} \\ \text{Pout} \leq \text{Pedge} \\ \text{Vcc} = 3.5 \text{ V} \\ \text{Tcase} = 25 \text{ °C} \\ \text{MODE} = \text{HIGH} \\ \text{Offset} = \pm 600 \text{ kHz} \\ \text{Load} = 50 \text{ Ohms} \end{array}$	_	-77	-63	dBc
	ACPR3		$\begin{array}{l} \text{RBW} = 30 \text{ kHz} \\ \text{Pout} \leq \text{Pedge}_\text{EX} \\ \text{Vcc} = \text{VRANGE} \\ \text{TCASE} = \text{TRANGE} \\ \text{MODE} = \text{HIGH} \\ \text{Offset} = \pm 600 \text{ kHz} \\ \text{Load VSWR} \leq 3:1, \text{ all phase angles} \end{array}$	_	-65	_	
			$\begin{array}{l} \text{RBW} = 100 \text{ kHz} \\ \text{Pout} \leq \text{Pedge} \\ \text{Vcc} = 3.5 \text{ V} \\ \text{TCASE} = 25 \ ^{\circ}\text{C} \\ \text{MODE} = \text{HIGH} \\ \text{Offset} = \pm 1.8 \text{ MHz} \\ \text{Load} = 50 \text{ Ohms} \end{array}$	_	-81	-66	
	ACPR4		$\begin{array}{l} RBW = 100 \mbox{ kHz} \\ Pout \leq Pedge_ex \\ Vcc = VRANGE \\ TCASE = TRANGE \\ MODE = HIGH \\ Offset = \pm 1.8 \mbox{ MHz} \\ Load \mbox{ VSWR} \leq 3:1, \mbox{ all phase angles} \end{array}$	_	-75	_	

Table 4. SKY77340 Electrical Specifications (4 of 17)

	Mode: Transmit	GMSK and EDGE ¹ Band: CEL [continued	}			
		General Test Conditions				
Frequency = 824-849 MHz Pulse Rate = 12.5% Duty Cycle	TFRAME = 4.615 Control States: E		RF Ports = GS	GM_IN, GSM_C	DUT	
Parameter	Symbol	Condition	Minimum	Typical	Maximum	Unit
Error Vector Magnitude	EVM1	$\label{eq:Vcc} \begin{array}{l} \text{Vcc} = 3.5 \text{ V} \\ \text{T}_{\text{CASE}} = 25 \ ^{\circ}\text{C} \\ \text{RBW} = 30 \ \text{kHz} \\ \text{Pout} \leq \text{Pedge} \\ \text{Load} = 50 \ \Omega \end{array}$	_	1.5	5.0	%
	EVM2	$\label{eq:Vcc} \begin{array}{l} \mbox{Vcc} = \mbox{Vrange} \\ \mbox{Tcase} = \mbox{Trange} \\ \mbox{RBW} = \mbox{30 kHz} \\ \mbox{Pour} \leq \mbox{Pedge_ex} \\ \mbox{Load VSWR} \leq \mbox{3:1} \ , \mbox{all phase angles} \end{array}$	_	3.0	9.0	70
Stability (all spurious)		$\label{eq:Vcc} \begin{array}{l} \mbox{Vcc} = \mbox{Vrange} \\ \mbox{Tcase} = \mbox{Trange} \\ \mbox{6.5 dBm} \leq \mbox{Pout} \leq \mbox{Pgsm} \\ \mbox{Load VSWR} = 8:1, \mbox{ all phase angles} \end{array}$	_		-36	dBm
Ruggedness		Vcc = 4.8 V Tcase = Trange Pout = PGSM PIN = 6 dBm Load VSWR = 10:1, all phase angles	No degradation No damage			
Mode Switching Time (Time does not include loop lock time [pedestal] for GMSK PAC operation.)	τmode	Vcc = Vrange Tcase = Trange		2	4	μS

Table 4. SKY77340 Electrical Specifications (5 of 17)

¹ All specifications related to modulated waveforms are for the EDGE waveform (i.e., EVM/ ACPR).

	Mode: Tran	nsmit GMSK and EDGE ¹ Band: EGSM					
		General Test Conditions					
Frequency = 880-915 MHz Pulse Rate = 12.5% Duty Cycle	TFRAME = 4.615 m Control States: E		RF Ports = GSM_IN, GSM_OUT				
Parameter	Symbol	Condition	Minimum	Typical	Maximum	Unit	
	Рдум	Vcc = 3.5 V TCASE = 25 °C $-1 \text{ dBm} \le \text{PiN} \le 6 \text{ dBm}$	34.5	34.9	_		
Dutput Power	Pgsm_ex-l	$\begin{array}{l} Vcc = 3.0 \ V \\ Tcase = Trange \\ -1 \ dBm \leq Pin \leq 6 \ dBm \end{array}$	32.0	33.4	_		
	Pgsm_ex-h	$V_{CC} = 4.8 V$ TCASE = TRANGE -1 dBm \leq PIN \leq 6 dBm	34.5	35.5		dBm	
	Pedge	Vcc = 3.5 V TCASE = 25 °C POUT = PIN + GEDGE	28.5	_	_		
	Pedge_ex	Vcc = Vrange Tcase = Trange	26.0	_	_		
	Gedge	$V_{CC} = 3.5 V$ $V_{BIAS} = 1.4 V$ $P_{OUT} = P_{EDGE}$ $T_{CASE} = 25 °C$ $MODE = HIGH$ $Load = 50 ohms$	31.5	33.0	34.5	dB	
Linear Gain		$\begin{array}{l} \mbox{Vcc} = 3.0 \mbox{ V < VBATT < 4.8 \mbox{ V}} \\ \mbox{Vbias} = 1.4 \mbox{ V} \\ \mbox{Pout} = \mbox{Pedge} \\ \mbox{Tcase} = \mbox{Trange} \\ \mbox{MODE} = \mbox{HiGH} \\ \mbox{Load VSWR} \leq 3:1 \mbox{ all phase angles} \end{array}$	27.0	33.0	36.0		
	Gvar	Vcc = 3.5 V Tcase = Trange MODE = HIGH	_	-0.034	_	dB/C	
Power Added Efficiency	PAEgsm	Vcc = 3.5 V TCASE = 25 °C VRAMP = 1.6 V PIN = 3 dBm	49	53	_	%	
	PAEedge	Vcc = 3.5 V Tcase = 25 °C Pout = 28.5 dBm	20	23			
	Igsm_low_power	Vcc = 3.5 V Tcase = 25 °C Pout = 6.5 dBm	_	110	130		
Low Power Current Consumption	ledge_low_power	$V_{CC} = 3.5 V$ $V_{BIAS} = 0.3 V$ $T_{CASE} = 25 °C$ $P_{OUT} = 6.5 dBm$	_	120	_	mA	

Table 4. SKY77340 Electrical Specifications (6 of 17)

		Mode: Transmit	GMSK and EDGE ¹ Band: EGSM [c	ontinued}				
			General Test Conditions					
Frequency = 880-915 MHz Pulse Rate = 12.5% Duty Cycle		TFRAME = 4.615 n Control States: El		RF Ports = GSI	RF Ports = GSM_IN, GSM_OUT			
Paramete	r	Symbol	Condition	Minimum	Typical	Maximum	Unit	
	20 MHz Offset	NxSat	$V_{CC} = 3.5 V$ TCASE = 25 °C POUT \leq PGSM RBW = 100 kHz	_	-86.0	-83.5		
Noise Power		NxLin	$V_{CC} = 3.5 V$ TCASE = 25 °C POUT \leq PEDGE RBW = 100 kHz	_	-84.0	-83.5		
	10 MHz Offset	NxSat_10 MHz	Vcc = 3.5 V Tcase = 25 °C Pout $\leq \text{Pgsm}$ RBW = 100 kHz	_	-86.0	-76.0	dBm	
		NxLin _10 MHz	Vcc = 3.5 V TCASE = 25 °C POUT \leq PEDGE RBW = 100 KHz	_	-84.0	-78.0	ubiii	
	1805 to 1880 MHz	NxSat _DCS	$V_{CC} = 3.5 V$ TCASE = 25 °C POUT \leq PGSM RBW = 100 kHz	_	-100.0	-84.0		
		NxLin _DCS	Vcc = 3.5 V TCASE = 25 °C POUT \leq PEDGE RBW = 100 kHz	_	-100.0	-84.0		
Forward Isolation		Iso	$\begin{array}{l} Vcc = Vrange \\ Tcase = Trange \\ Pin \leq 6 \ dBm \\ EN = 0 \end{array}$	_	-40	-30	dBm	
Crossover Isolation	Fundamental	lso_EGSM-DCS	Vcc = Vrange Tcase = Trange	—		0	dBm	
	2 nd Harmonic		Vcc = Vrange Tcase = Trange	_		-20	udili	
Input VSWR		VSWR_SAT	$\begin{array}{l} \text{Vcc} = \text{Vrange} \\ \text{Tcase} = \text{Trange} \\ \text{6.5 dBm} \leq \text{Pout} \leq \text{Pgsm} \end{array}$	_	1.7	2.25	Ratio	
		VSWR_LIN	Vcc = Vrange Tcase = Trange $Pout \le Pedge$	_	1.5	2.0	nuto	

Table 4. SKY77340 Electrical Specifications (7 of 17)

		Mode: Transmit	t GMSK and EDGE ¹ Band: EGSM [contin	nued}				
			General Test Conditions					
Frequency = 880-915 MHz Pulse Rate = 12.5% Duty Cycle		TFRAME = 4.615 Control States: I		RF Ports = GS	RF Ports = GSM_IN, GSM_OUT			
Parameter		Symbol	Condition	Minimum	Typical	Maximum	Unit	
Harmonics		2fo	Measured at GSM_OUT Pout \leq Pgsm Vcc = Vrange Tcase = Trange Load = 50 ohms	_	-20	-10	dBm	
		3fo to 15fo	Measured at GSM_OUT Pout \leq Pgsm Vcc = Vrange Tcase = Trange Load = 50 ohms	_	-25	-17	UDIII	
Adjacent Channel Leakage	ACPR1		$\begin{array}{l} \text{RBW} = 30 \text{ kHz} \\ \text{Pout} \leq \text{Pedge} \\ \text{MODE} = \text{HIGH} \\ \text{Vcc} = 3.5 \text{ V} \\ \text{Tcase} = 25 \ ^{\circ}\text{C} \\ \text{Offset} = \pm 200 \text{ kHz} \\ \text{Load} = 50 \text{ Ohms} \end{array}$	_	-36	-33		
	, in the second s		$\begin{array}{l} \text{RBW} = 30 \text{ kHz} \\ \text{Pout} \leq \text{Pedge}_\text{EX} \\ \text{MODE} = \text{HIGH} \\ \text{Vcc} = \text{VRANGE} \\ \text{Tcase} = \text{TRANGE} \\ \text{Offset} = \pm 200 \text{ kHz} \\ \text{Load VSWR} \leq 3:1, \text{ all phase angles} \end{array}$	_	-35	_	dBc	
	ACPB2		$\begin{array}{l} RBW = 30 \; kHz \\ Pout \leq Pedge \\ MODE = HIGH \\ Vcc = 3.5 \; V \\ TcAse = 25 \; ^{\circ}C \\ Offset = \pm 400 \; kHz \\ Load = 50 \; Ohms \end{array}$	_	-60	-58	ubu	
	AUT NZ		$\begin{array}{l} \text{RBW} = 30 \text{ kHz} \\ \text{Pout} \leq \text{Pedge_ex} \\ \text{MODE} = \text{HIGH} \\ \text{Vcc} = \text{VRANGE} \\ \text{Tcase} = \text{TRANGE} \\ \text{Offset} = \pm 400 \text{ kHz} \\ \text{Load VSWR} \leq 3:1, \text{ all phase angles} \end{array}$	_	-57	_		

Table 4. SKY77340 Electrical Specifications (8 of 17)

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		Mode: Transmit	GMSK and EDGE ¹ Band: EGSM [contin	iued}				
			General Test Conditions					
Frequency = 880-915 MHz Pulse Rate = 12.5% Duty Cycle		TFRAME = 4.615 n Control States: El		RF Ports = GSI	F Ports = GSM_IN, GSM_OUT			
Parameter		Symbol	Condition	Minimum	Typical	Maximum	Unit	
	ACPR3	2	$\begin{array}{l} \text{RBW} = 30 \text{ kHz} \\ \text{Pout} \leq \text{Pedge} \\ \text{MODE} = \text{HIGH} \\ \text{Vcc} = 3.5 \text{ V} \\ \text{Tcase} = 25 \ ^{\circ}\text{C} \\ \text{Offset} = \pm 600 \text{ kHz} \\ \text{Load} = 50 \text{ Ohms} \end{array}$	_	-77	-63		
Adjacent Channel Leakage [continued]			$\begin{array}{l} \text{RBW} = 30 \text{ kHz} \\ \text{Pout} \leq \text{Pedge_ex} \\ \text{MODE} = \text{HIGH} \\ \text{Vcc} = \text{Vrange} \\ \text{Tcase} = \text{Trange} \\ \text{Offset} = \pm 600 \text{ kHz} \\ \text{Load VSWR} \leq 3:1, \text{ all phase angles} \end{array}$	_	-65	_		
	ACPR4		$\begin{array}{l} \text{RBW} = 100 \text{ kHz} \\ \text{POUT} \leq \text{PEDGE} \\ \text{MODE} = \text{HIGH} \\ \text{Vcc} = 3.5 \text{ V} \\ \text{TcASE} = 25 \ ^{\circ}\text{C} \\ \text{Offset} = \pm 1.8 \text{ MHz} \\ \text{Load} = 50 \text{ Ohms} \end{array}$	_	-81	-66		
			$\begin{array}{l} \text{RBW} = 100 \text{ kHz} \\ \text{POUT} \leq \text{PEDGE}_\text{EX} \\ \text{MODE} = \text{HIGH} \\ \text{Vcc} = \text{VRANGE} \\ \text{TCASE} = \text{TRANGE} \\ \text{Offset} = \pm 1.8 \text{ MHz} \\ \text{Load VSWR} \leq 3:1, \text{ all phase angles} \end{array}$	_	-75	_		
Fror Vector Magnitude		EVM1	$\begin{array}{l} RBW = 30 \; KHz \\ Pout \leq Pedge \\ MODE = HIGH \\ Vcc = 3.5 \; V \\ Tcase = 25 \; ^{\circ}C \\ Load = 50 \; \Omega \end{array}$	_	1.5	5.0	%	
Error Vector Magnitude		EVM2	$\begin{array}{l} \text{RBW} = 30 \text{ kHz} \\ \text{Pout} \leq \text{Pedge_ex} \\ \text{MODE} = \text{HIGH} \\ \text{Vcc} = \text{Vrange} \\ \text{Tcase} = \text{Trange} \\ \text{Load} \text{ VSWR} \leq 3:1 \text{ , all phase angles} \end{array}$	_	3.0	9.0	70	
Stability (all spurious)			$6.5 \text{ dBm} \le P_{OUT} \le P_{GSM}$ Vcc = Vrange Tcase = Trange Load VSWR = 8:1, all phase angles	_		-36	dBm	
Ruggedness			Pout = PGSM PIN = 6 dBm Vcc = 4.8 V Tcase = Trange		No degradation			
			Load VSWR = 10:1, all phase angles		No dar	nage		
Mode Switching Time (Time does not include loop lock time [p GMSK PAC operation.)	edestal] for	τmode	Vcc = Vrange Tcase = Trange		2	4	μS	

Table 4. SKY77340 Electrical Specifications (9 of 17)

¹ All specifications related to modulated waveforms are for the EDGE waveform (i.e., EVM/ ACPR).

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Mode: Transmit GMSK and EDGE ¹ Band: DCS										
		General Test Conditions								
Frequency = 1710-1785 MHz Pulse Rate = 12.5% Duty Cycle	TFRAME = 4.615 r Control States: E		RF Ports = DCS/PCS_IN, DCS/PCS_0UT							
Parameter	Symbol	Condition	Minimum	Typical	Maximum	Unit				
Output Power	Pgsm	$\begin{array}{l} \mbox{Vcc} = 3.5 \mbox{ V} \\ \mbox{Tcase} = 25 \mbox{ °C} \\ \mbox{0 dBm} \leq \mbox{Pin} \leq 6 \mbox{ dBm} \end{array}$	32.5	33.5	_					
	Pgsm_ex-l	$\begin{array}{l} \mbox{Vcc} = 3.0 \mbox{ V} \\ \mbox{Tcase} = \mbox{Trange} \\ \mbox{0 dBm} \leq \mbox{Pin} \leq 6 \mbox{ dBm} \end{array}$	29.0	32.0	_					
	Pgsm_ex-h	$\begin{array}{l} V_{CC} = 4.8 \ V \\ T_{CASE} = T_{RANGE} \\ 0 \ dBm \leq P_{IN} \leq 6 \ dBm \end{array}$	29.0	34.2		dBm				
	Pedge	$V_{CC} = 3.5 V$ TCASE = 25 °C Pout = PIN + GEDGE	27.3		_					
	Pedge_ex	Vcc = Vrange Tcase = Trange	25.0	_	_					
Linear Gain	GEDGE	$V_{CC} = 3.5 V$ $V_{BIAS} = 1.3 V$ $P_{OUT} = P_{EDGE}$ $T_{CASE} = 25 °C$ $MODE = HIGH$ $Load = 50 ohms$	33.0	34.8	36.0	dB				
		$\begin{array}{l} \mbox{Vcc} = 3.0 \mbox{ V < VBATT < 4.8 \mbox{ V}} \\ \mbox{Vbias} = 1.3 \mbox{ V} \\ \mbox{Pout} = \mbox{Pedge} \\ \mbox{Tcase} = \mbox{Trange} \\ \mbox{MODE} = \mbox{HIGH} \\ \mbox{Load VSWR} \leq 3:1 \mbox{ all phase angles} \end{array}$	28.0	34.8	38.0	ŭĐ				
	Gvar	Vcc = 3.5 V Tcase = Trange MODE = HIGH	_	-0.025	_	dB/C				
Power Added Efficiency	PAEgsm	$V_{CC} = 3.5 V$ $T_{CASE} = 25 °C$ $V_{RAMP} = 1.6 V$ $P_{IN} = 3 dBm$	48	52	_	%				
	PAEedge	Vcc = 3.5 V Tcase = 25 °C Pout = 27.3 dBm	20	25						
Low Power Current Consumption	Igsm_low_power	Vcc = 3.5 V Tcase = 25 °C Pout = 1.5 dBm	_	80	100					
	Iedge_low_power	$V_{CC} = 3.5 V$ $V_{BIAS} = 0.4 V$ $T_{CASE} = 25 °C$ $P_{OUT} = 1.5 dBm$	_	110	_	mA				

Table 4. SKY77340 Electrical Specifications (10 of 17)

		Mode: Transmit	GMSK and EDGE ¹ Band: DCS [continu	ied}			
			General Test Conditions				
Frequency = 1710-1785 MHz Pulse Rate = 12.5% Duty Cycle		TFRAME = 4.615 n Control States: El		RF Ports = DCS/PCS_IN, DCS/PCS_OUT			
Parameter	r	Symbol	Condition	Minimum	Typical	Maximum	Unit
20 MHz Offset Noise Power 925 to 960 MHz	20 MHz Offset	NxSat	Vcc = 3.5 V TCASE = 25 °C POUT $\leq P$ GSM RBW = 100 KHz	_	-82	-80	
	NxLin	Vcc = 3.5 V TCASE = 25 °C POUT \leq PEDGE RBW = 100 kHz	_	-82	-80	dBm	
	NxSat_EGSM	$\begin{array}{l} \mbox{Vcc} = 3.5 \mbox{ V} \\ \mbox{Tcase} = 25 \mbox{ °C} \\ \mbox{Pout} \leq \mbox{Pgsm} \\ \mbox{RBW} = 100 \mbox{ kHz} \end{array}$	_	-88	-84	dBm	
	NxLin_EGSM	Vcc = 3.5 V TCASE = 25 °C POUT \leq PEDGE RBW = 100 kHz	_	-86	-84		
Forward Isolation		lso	$\begin{array}{l} Vcc = V \text{RANGE} \\ Tcase = T \text{RANGE} \\ \text{PIN} \leq 6 \ \text{dBm} \\ \text{EN} = 0 \end{array}$	_	-33	-30	dBm
Crossover Isolation	Fundamental	lso_DCS-EGSM	Vcc = Vrange Tcase = Trange	_	-17	-10	dBm
Input VSWR		VSWR_SAT	$\begin{array}{l} \text{Vcc} = \text{Vrange} \\ \text{Tcase} = \text{Trange} \\ \text{1.5 dBm} \leq \text{Pout} \leq \text{Pgsm} \end{array}$	_	1.5	2.25	Ratio
		VSWR_LIN	Vcc = Vrange Tcase = Trange $Pout \leq Pedge$	_	1.2	2.0	natio
Harmonics		2fo to 4fo	Measured at GSM_OUT Pout \leq PGSM Vcc = VRANGE TCASE = TRANGE Load = 50 ohms	_	-25	-10	dBm
		5fo to 7fo	Measured at GSM_OUT Pout \leq PgsmVcc = Vrange Tcase = Trange Load = 50 ohms	_	_	-17	

Table 4. SKY77340 Electrical Specifications (11 of 17)

		Mode: Transmit	t GMSK and EDGE ¹ Band: DCS [contin	ued}			
			General Test Conditions				
Frequency = 1710-1785 MHz		TFRAME = 4.615 r		RF Ports = DCS	S/PCS_IN, DCS	S/PCS _OUT	
Pulse Rate = 12.5% Duty Cycle Parameter		Control States: E Symbol	N = 1, BS = 1 Condition	Minimum	Typical	Maximum	Unit
i arameter		Symbol	RBW = 30 kHz	Willingth	турісаі	WIdAIIIIUIII	Unit
ACPR1 ACPR2 Adjacent Channel Leakage ACPR3			POUT \leq PEDGE VCC = 3.5 V TCASE = 25 °C Offset = ± 200 kHz Load = 50 Ohms	_	-36	-33	
	ACPR1		$\begin{array}{l} \text{RBW} = 30 \text{ kHz} \\ \text{POUT} \leq \text{PEDGE}_\text{EX} \\ \text{MODE} = \text{HIGH} \\ \text{Vcc} = \text{VRANGE} \\ \text{TCASE} = \text{TRANGE} \\ \text{Offset} = \pm 200 \text{ kHz} \\ \text{Load VSWR} \leq 3:1, \text{ all phase angles} \end{array}$	_	-30	_	
		$\begin{array}{l} \text{RBW} = 30 \text{ kHz} \\ \text{Pout} \leq \text{Peoge} \\ \text{MODE} = \text{HIGH} \\ \text{Vcc} = 3.5 \text{ V} \\ \text{TcAse} = 25 \ ^{\circ}\text{C} \\ \text{Offset} = \pm 400 \text{ kHz} \\ \text{Load} = 50 \text{ Ohms} \end{array}$	_	-60	-57		
		$\begin{array}{l} \text{RBW} = 30 \text{ kHz} \\ \text{Pout} \leq \text{Pedge}_\text{EX} \\ \text{MODE} = \text{HIGH} \\ \text{Vcc} = \text{VRANGE} \\ \text{TcASE} = \text{TRANGE} \\ \text{Offset} = \pm 400 \text{ kHz} \\ \text{Load VSWR} \leq 3:1, \text{ all phase angles} \end{array}$	_	-55	_		
	40000		$\begin{array}{l} \text{RBW} = 30 \text{ kHz} \\ \text{POUT} \leq \text{PEDGE} \\ \text{MODE} = \text{HIGH} \\ \text{Vcc} = 3.5 \text{ V} \\ \text{TCASE} = 25 \ ^{\circ}\text{C} \\ \text{Offset} = \pm 600 \text{ kHz} \\ \text{Load} = 50 \text{ Ohms} \end{array}$	_	-75	-63	dBc
	АСРКЗ		$\begin{array}{l} \text{RBW} = 30 \text{ kHz} \\ \text{Pout} \leq \text{Pedge_ex} \\ \text{MODE} = \text{HIGH} \\ \text{Vcc} = \text{VRANGE} \\ \text{TcAse} = \text{TRANGE} \\ \text{Offset} = \pm 600 \text{ kHz} \\ \text{Load VSWR} \leq 3:1, \text{ all phase angles} \end{array}$	_	-65	_	
		$\begin{array}{l} \text{RBW} = 100 \text{ kHz} \\ \text{POUT} \leq \text{PEDGE} \\ \text{MODE} = \text{HIGH} \\ \text{Vcc} = 3.5 \text{ V} \\ \text{TCASE} = 25 \ ^{\circ}\text{C} \\ \text{Offset} = \pm 1.8 \text{ MHz} \\ \text{Load} = 50 \text{ Ohms} \end{array}$	_	-77	-66		
	ACPR4		$\begin{array}{l} \text{RBW} = 100 \text{ kHz} \\ \text{Pout} \leq \text{Pedge_ex} \\ \text{MODE} = \text{HIGH} \\ \text{Vcc} = \text{Vrange} \\ \text{Tcase} = \text{Trange} \\ \text{Offset} = \pm 1.8 \text{ MHz} \end{array}$	_	-77	_	

Table 4. SKY77340 Electrical Specifications (12 of 17)

	Mode: Transm	it GMSK and EDGE ¹ Band: DCS [continu	ed}			
		General Test Conditions				
Frequency = 1710-1785 MHz Pulse Rate = 12.5% Duty Cycle	TFRAME = 4.615 Control States:		RF Ports = DCS/PCS_IN, DCS/PCS _OUT			
Parameter	Symbol	Symbol Condition			Maximum	Unit
Error Vector Magnitude	EVM1	$\begin{array}{l} \text{RBW} = 30 \text{ kHz} \\ \text{Pout} \leq \text{Pedge} \\ \text{MODE} = \text{HIGH} \\ \text{Vcc} = 3.5 \text{ V} \\ \text{Tcase} = 25 \ ^{\circ}\text{C} \\ \text{Load} = 50 \ \Omega \end{array}$	_	1.9	5.0	%
	EVM2	$\begin{array}{l} RBW = 30 \ \text{kHz} \\ \text{Pout} \leq \text{Pedge_ex} \\ \text{MODE} = \text{HIGH} \\ \text{Vcc} = \text{VRange} \\ \text{Tcase} = \text{Trange} \\ \text{Load VSWR} \leq 2.5:1 \ , \ \text{all phase angles} \end{array}$	_	6.0	9.0	70
Stability (all spurious)		$\begin{array}{l} 1.5 \text{ dBm} \leq \text{Pout} \leq \text{Pgsm} \\ \text{Vcc} = \text{Vrange} \\ \text{Tcase} = \text{Trange} \\ \text{Load VSWR} = 8:1, \text{ all phase angles} \end{array}$	_	_	-36	dBm
Ruggedness		$\begin{array}{l} P_{OUT} = PGSM \\ P_{IN} = 6 \ dBm \\ V_{CC} = 4.8 \ V \\ T_{CASE} = T_{RANGE} \\ Load \ VSWR = 10:1, \ all \ phase \ angles \end{array}$	No degradation No damage			
Mode Switching Time (Time does not include loop lock time [pedestal] for GMSK PAC operation.)	TMODE	Vcc = Vrange Tcase = Trange		2	4	μS

Table 4. SKY77340 Electrical Specification	is (13 of 17)
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¹ All specifications related to modulated waveforms are for the EDGE waveform (i.e., EVM/ACPR).

	Mode: Tra	ansmit GMSK and EDGE ¹ Band: PCS					
		General Test Conditions					
Frequency = 1850-1910 MHz Pulse Rate = 12.5% Duty Cycle	TFRAME = 4.615 r Control States: E		RF Ports = DCS/PCS_IN, DCS/PCS_0UT				
Parameter	Symbol	Condition	Minimum	Typical	Maximum	Unit	
Output Power	Pgsm	$\begin{array}{l} \mbox{Vcc} = 3.5 \mbox{ V} \\ \mbox{Tcase} = 25 \mbox{ °C} \\ \mbox{0 dBm} \le \mbox{Pin} \le 6 \mbox{ dBm} \end{array}$	32.5	33.2	_		
	Pgsm_ex-l	$\begin{array}{l} \mbox{Vcc} = 3.0 \mbox{ V} \\ \mbox{Tcase} = \mbox{Trange} \\ \mbox{0 dBm} \le \mbox{Pin} \le 6 \mbox{ dBm} \end{array}$	29.0	32.0	_		
	Pgsm_ex-h	$\begin{array}{l} V_{CC} = 4.8 \ V \\ T_{CASE} = T_{RANGE} \\ 0 \ dBm \leq P_{IN} \leq 6 \ dBm \end{array}$	29.0	34.0	_	dBm	
	Pedge	$V_{CC} = 3.5 V$ TCASE = 25 °C Pout = PIN + GEDGE	27.3		_		
	Pedge_ex	Vcc = Vrange Tcase = Trange	25.0	_	_		
Linear Gain	Gedge	Vcc = 3.5 V $VBIAS = 1.3 V$ $POUT = PEDGE$ $TCASE = 25 °C$ $MODE = HIGH$ $Load = 50 ohms$	33.0	35.0	36.0	dB	
		$\begin{array}{l} \mbox{Vcc} = 3.0 \mbox{ V < VBATT} < 4.8 \mbox{ V} \\ \mbox{VBIAS} = 1.3 \mbox{ V} \\ \mbox{Pout} = \mbox{Pedge} \\ \mbox{Tcase} = \mbox{Trange} \\ \mbox{MODE} = \mbox{HIGH} \\ \mbox{Load} \mbox{VSWR} \leq 3:1 \mbox{ all phase angles} \end{array}$	28.0	35.0	38.0		
	Gvar	Vcc = 3.5 V Tcase = Trange MODE = HIGH	_	-0.025	_	dB/C	
Power Added Efficiency	PAEgsm	$V_{CC} = 3.5 V$ $T_{CASE} = 25 °C$ $V_{RAMP} = 1.6 V$ $P_{IN} = 3 dBm$	48	52	_	%	
	PAEedge	Vcc = 3.5 V Tcase = 25 °C Pout = 27.3 dBm	20	25	_		
	Igsm_low_power	Vcc = 3.5 V Tcase = 25 °C Pout = 1.5 dBm	_	80	100	mA	
Low Power Current Consumption	Iedge_low_power	$V_{CC} = 3.5 V$ $V_{BIAS} = 0.4 V$ $T_{CASE} = 25 °C$ $P_{OUT} = 1.5 dBm$	_	110	_		

Table 4. SKY77340 Electric3al Specifications (14 of 17)

		Mode: Transmit	t GMSK and EDGE ¹ Band: PCS [conti	inued}				
			General Test Conditions					
Frequency = 1850-1910 MHz Pulse Rate = 12.5% Duty Cycle		TFRAME = 4.615 n Control States: El		RF Ports = DCS/PCS_IN, DCS/PCS_OUT				
Parameter	r	Symbol	Condition	Minimum	Typical	Maximum	Unit	
	20 MHz Offset	NxSat	Vcc = 3.5 V TCASE = 25 °C POUT $\leq \text{PGSM}$ RBW = 100 kHz	_	-82	-80		
Noise Power 869 to 894 MHz		NxLin	Vcc = 3.5 V TCASE = 25 °C POUT $\leq \text{PEDGE}$ RBW = 100 kHz	_	-82	-80	dBm	
	960 to 904 MHz	NxSat_CEL	Vcc = 3.5 V Tcase = 25 °C Pout $\leq P$ GSM RBW = 100 kHz	_	-88	-84	UDIII	
	NxLin_CEL	Vcc = 3.5 V Tcase = 25 °C Pout $\leq \text{Pedge}$ RBW = 100 kHz	_	-86	-84			
Forward Isolation		lso	$\begin{array}{l} \text{Vcc} = \text{Vrange} \\ \text{Tcase} = \text{Trange} \\ \text{Pin} \leq 6 \text{ dBm} \\ \text{EN} = 0 \end{array}$	_	-33	-30	dBm	
Crossover Isolation	Fundamental	Iso_PCS-EGSM	Vcc = Vrange Tcase = Trange	_	_	-10	dBm	
Input VSWR		VSWR_SAT	Vcc = Vrange Tcase = Trange 1.5 dBm ≤ Pout ≤ Pgsm	_	2.1	2.25	Ratio	
		VSWR_LIN	$V_{CC} = V_{RANGE}$ $T_{CASE} = T_{RANGE}$ $P_{OUT} \le P_{EDGE}$	_	1.5	2.0	nauo	
Harmonics		2fo to 3fo	Measured at DCS/PCS_OUT POUT_RANGE Vcc = VRANGE Tcase = TRANGE Load = 50 ohms	_	-10	-7		
		4fo	Measured at DCS/PCS_OUT Pout_RANGE Vcc = VRANGE Tcase = TRANGE Load = 50 ohms	_	-11	-5	dBm	
			Measured at DCS/PCS _OUT Pout_range Vcc = Vrange Tcase = Trange Load = 50 ohms	_	-30	-17		

Table 4. SKY77340 Electrical Specifications (15 of 17)

	Mode	e: Transmit	GMSK and EDGE ¹ Band: PCS [continu	ied}			
			General Test Conditions				
Frequency = 1850-1910 MHz		ı∈ = 4.615 m		RF Ports = DCS	S/PCS_IN, DCS	S/PCS_OUT	
Pulse Rate = 12.5% Duty Cycle Parameter		ol States: EN Symbol	N = 1, BS =1 Condition	Minimum	Typical	Maximum	Unit
		Jymbol	RBW = 30 kHz	inininani	Typical	maximam	onit
	ACPR1		POUT \leq PEDGE MODE = HIGH Vcc = 3.5 V TCASE = 25 °C Offset = ± 200 kHz Load = 50 Ohms	_	-36	-33	
			$\begin{array}{l} \text{RBW} = 30 \text{ kHz} \\ \text{Pout} \leq \text{Pedge_ex} \\ \text{MODE} = \text{HIGH} \\ \text{Vcc} = \text{VRANGE} \\ \text{Tcase} = \text{TRANGE} \\ \text{Offset} = \pm 200 \text{ kHz} \\ \text{Load VSWR} \leq 3:1, \text{ all phase angles} \end{array}$	_	-30	_	
	10000		$RBW = 30 \text{ kHz}$ $Pout \leq Pedge$ $MODE = HIGH$ $Vcc = 3.5 \text{ V}$ $TcAse = 25 \text{ °C}$ $Offset = \pm 400 \text{ kHz}$ $Load = 50 \text{ Ohms}$		-60	-57	
ACPR2 Adjacent Channel Leakage ACPR3	AUPK2		$\begin{array}{l} \text{RBW} = 30 \text{ kHz} \\ \text{Pout} \leq \text{Pedge_ex} \\ \text{MODE} = \text{HIGH} \\ \text{Vcc} = \text{VRANGE} \\ \text{Tcase} = \text{TRANGE} \\ \text{Offset} = \pm 400 \text{ kHz} \\ \text{Load VSWR} \leq 3:1, \text{ all phase angles} \end{array}$	_	-55	_	-ID-
	10000		$RBW = 30 \text{ kHz}$ $Pout \leq PEDGE$ $MODE = HIGH$ $Vcc = 3.5 \text{ V}$ $TcAsE = 25 \text{ °C}$ $Offset = \pm 600 \text{ kHz}$ $Load = 50 \text{ Ohms}$		-75	-63	dBc
	ACPR3		$\begin{array}{l} \text{RBW} = 30 \text{ kHz} \\ \text{Pout} \leq \text{Pedge}_{\text{EX}} \\ \text{MODE} = \text{HIGH} \\ \text{Vcc} = \text{VRANGE} \\ \text{Tcase} = \text{TRANGE} \\ \text{Offset} = \pm 600 \text{ kHz} \\ \text{Load VSWR} \leq 3:1, \text{ all phase angles} \end{array}$	_	-65	_	
	4000.4		$\begin{array}{l} \text{RBW} = 100 \text{ kHz} \\ \text{Pout} \leq \text{PEDGE} \\ \text{MODE} = \text{HIGH} \\ \text{Vcc} = 3.5 \text{ V} \\ \text{TCASE} = 25 ^{\circ} \text{ C} \\ \text{Offset} = \pm 1.8 \text{ MHz} \\ \text{Load} = 50 \text{ Ohms} \end{array}$	_	-77	-66	
	ACPR4		$\begin{array}{l} \text{RBW} = 100 \text{ KHz} \\ \text{Pout} \leq \text{Pedge_ex} \\ \text{MODE} = \text{HIGH} \\ \text{Vcc} = \text{VRANGE} \\ \text{Tcase} = \text{TRANGE} \\ \text{Offset} = \pm 1.8 \text{ MHz} \end{array}$	_	-77	_	

Table 4. SKY77340 Electrical Specifications (16 of 17)

	Mode: Transm	it GMSK and EDGE ¹ Band: PCS [continue	ed}				
		General Test Conditions					
Frequency = 1850-1910 MHz Pulse Rate = 12.5% Duty Cycle	TFRAME = 4.615 Control States:		RF Ports = DCS/PCS_IN, DCS/PCS_0UT				
Parameter	Symbol	Condition	Minimum	Typical	Maximum	Unit	
Error Vector Magnitude	EVM1	$\begin{array}{l} RBW = 30 \; kHz \\ Pout \leq Pedge \\ MODE = HIGH \\ Vcc = 3.5 \; V \\ Tcase = 25 \; C \\ Load = 50 \; \Omega \end{array}$	_	1.9	5.0	%	
	EVM2	$\begin{array}{l} RBW = 30 \ \text{kHz} \\ Pout \leq \text{Pedge_ex} \\ \text{MODE} = \text{HIGH} \\ \text{Vcc} = \text{Vrange} \\ \text{Tcase} = \text{Trange} \\ \text{Load VSWR} \leq 2.5:1 \ \text{, all phase angles} \end{array}$	_	6.0	9.0	70	
Stability (all spurious)		$\begin{array}{l} 1.5 \text{ dBm} \leq \text{Pout} \leq \text{Pgsm} \\ \text{MODE} = \text{HIGH} \\ \text{Vcc} = \text{Vrange} \\ \text{Tcase} = \text{Trange} \\ \text{Load VSWR} = 8:1, \text{ all phase angles} \end{array}$	_		-36	dBm	
Ruggedness		$\begin{array}{l} P_{OUT} = PGSM \\ P_{IN} = 6 \ dBm \\ V_{CC} = 4.8 \ V \\ T_{CASE} = T_{RANGE} \\ Load \ VSWR = 10:1, \ all \ phase \ angles \end{array}$	No degradation No damage				
Mode Switching Time (Time does not include loop lock time [pedestal] for GMSK PAC operation.)	τmode	Vcc = Vrange Tcase = Trange		2	4	μS	

Table 4. SKY77340 Electrical Specifications (17 of 17)

¹ All specifications related to modulated waveforms are for the EDGE waveform (i.e., EVM/ACPR).

	Mode: Closed Loo	op PA Control (GMSK Mode) Band: CEL	& EGSM							
General Test Conditions										
Frequency = 824-849 MHz & 880-915 MHz	Pulse Rate = 12	2.5% Duty Cycle, Tframe = 4.615 mS	Control States: EN = 1,MODE = 0, BS = 0, $V_{BIAS} = don't care V_{RAMP} = active$							
Parameter	Symbol	Condition	Minimum	Typical	Maximum	Unit				
VRAMP Input Voltage	Vramp	$\label{eq:Vcc} \begin{array}{l} \mbox{Vcc} = 3.5 \mbox{ V} \\ \mbox{Tcase} = \mbox{Trange} \\ \mbox{6.5 dBm} \le \mbox{Pout} \le 34.5 \mbox{ dBm} \end{array}$	0.2	_	1.6	V				
Dynamic Range		Vcc = Vrange Tcase = Trange	37.0	40.0	_	dB				
Tolerance		$V_{CC} = V_{RANGE}$ $T_{CASE} = T_{RANGE}$ $P_{OUT} = 6.5 \text{ dBm}$	-4.0	_	3.0	dB				
TURIAILE		Vcc = Vrange Tcase = Trange Pout = 34.5 dBm	-3.0	_	2.0	üВ				
Power Control Slope	Pcs	$\label{eq:Vcc} \begin{array}{l} \mbox{Vcc} = \mbox{Vrange} \\ \mbox{Tcase} = \mbox{Trange} \\ \mbox{6.5 dBm} \le \mbox{Pout} \le 34.5 \mbox{ dBm} \end{array}$	-	_	250.0	dB/V				
PAC Loop Enable Time	τen	$\label{eq:Vcc} \begin{array}{l} \mbox{Vcc} = \mbox{V}_{RANGE} \\ \mbox{T}_{CASE} = \mbox{T}_{RANGE} \\ \mbox{After EN transitions LOW} \geq \mbox{HIGH} \end{array}$	_	1	2	μS				
Minimum Pedestal Duration	τped	$\begin{array}{l} \mbox{Vcc} = \mbox{V}\mbox{range} \\ \mbox{T}\mbox{case} = \mbox{T}\mbox{range} \\ \mbox{6.5 dBm} \le \mbox{P}\mbox{ut} \le 34.5 \mbox{dBm} \end{array}$	_	2	_	μS				

Table 5. SKY77340 Functional Specifications – GMSK PA Control Operation (1 of 4)

Note:

Response is monotonic over frequency, temperature, and POUT

Table 5. SKY77340 Functional Specifications – EDGE PA Control Operation (2 of 4)

Mode: Open Loop, Fixed PA Gain (EDGE Mode) Band: CEL & EGSM									
		General Test Conditions							
Frequency = 824-849 MHz & 880-915 MHz Pulse Rate = 12.5% Duty Cycle, TFRAME = 4.615 mS Control States: EN = 1, MODE = 1, BS = 0, VRAMP = don't care, VBIAS = active									
Parameter	Symbol	Condition	Minimum	Typical	Maximum	Unit			
VBIAS Input Voltage	Veias	$\begin{array}{l} \text{Vcc} = 3.5 \text{ V} \\ \text{Tcase} = \text{Trange} \\ \text{POUT} = 28.5 \text{ dBm} \\ \text{EVM1} \leq 5\% \\ \text{ACPR2} \leq 58 \text{ dBc} \\ \text{Gain} = \text{Gedge} \end{array}$	_	1.4	1.6	V			

Mode: Closed Loop PA Control (GMSK Mode) Band: DCS & PCS **General Test Conditions** Control States: EN = 1,MODE = 0, BS = 1,Frequency = 1710-1785 MHz & 1850-1910 MHz Pulse Rate = 25% Duty Cycle, TFRAME = 4.615 mS VBIAS = don't care, VRAMP = active Minimum Maximum Parameter Symbol Condition Typical Unit Vcc = 3.5 VVRAMP Input Voltage ۷ VRAMP TCASE = TRANGE 0.2 1.6 $1.5 \text{ dBm} \le P_{\text{OUT}} \le 32.5 \text{ dBm}$ VCC = VRANGE Dynamic Range 32.0 35.0 dB TCASE = TRANGE VCC = VRANGE TCASE = TRANGE -5.0 4.0 ____ Роит = 1.5 dBm dB Tolerance VCC = VRANGE-3.0 2.0 TCASE = TRANGE _ POUT = 32.5 dBm VCC = VRANGE dB/V Power Control Slope Pcs TCASE = TRANGE 250 ____ $1.5 \text{ dBm} \le P_{\text{OUT}} \le 32.5 \text{ dBm}$ $V_{CC} = V_{RANGE}$ PAC Loop Enable Time 2 au_{EN} TCASE = TRANGE1 μS After EN transitions $LOW \ge HIGH$ VCC = VRANGEMinimum Pedestal Duration τped TCASE = TRANGE 2.0 μS

Table 5. SKY77340 Functional Specifications – GMSK PA Control Operation (3 of 4)

Note:

Response is monotonic over frequency, temperature, and POUT

Table 5. SKY77340 Functional Specifications – EDGE PA Control Operation (4 of 4)

 $1.5 \text{ dBm} \leq \text{Pout} \leq 32.5 \text{ dBm}$

I	Node: Open Loop,	Fixed PA Gain (EDGE Mode) Band: DC	S & PCS			
		General Test Conditions				
Frequency = 1710-1785 MHz & 1850-1910 MHz	Control States EN = 1, MODE $V_{RAMP} = don't$	= 1, BS = 1,	ctive			
Parameter	Symbol	Condition	Minimum	Typical	Maximum	Unit
VBIAS Input Voltage	Vbias	$\label{eq:VCC} \begin{array}{l} Vcc = 3.5 \text{ V} \\ Tcase = Trange \\ POUT = 27.3 \text{ dBm} \\ EVM1 \leq 5\% \\ ACPR2 \leq 58 \text{ dBc} \\ Gain = Gedge \end{array}$	_	1.3	1.6	V

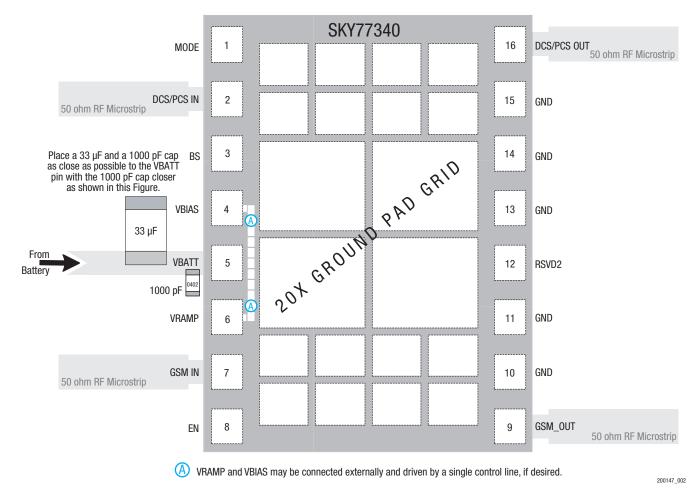
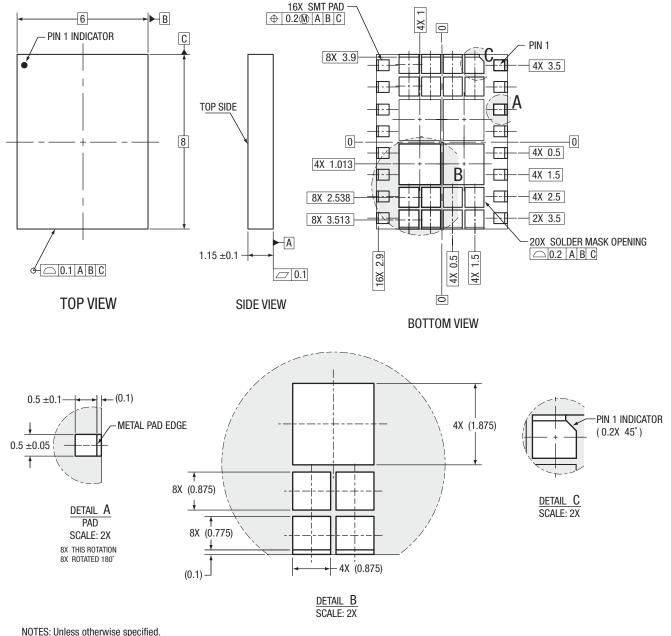


Figure 2. Typical SKY77340 Application Circuit

Package Dimensions and Pin Descriptions

Figure 3 is a mechanical diagram of the pad layout for the SKY77340, a 16-pin leadless quad-band PA module. Figure 4 provides a recommended phone board layout footprint for the PAM to help the designer attain optimum thermal conductivity, good grounding, and minimum RF discontinuity for the 50-ohm terminals.

Figure 5 shows the device pin configuration and numbering convention, which starts with pin 1 at the upper left, as indicated, and increments counter-clockwise around the package. Table 6 lists the pin names and signal descriptions. Figure 6 interprets typical Case Markings.

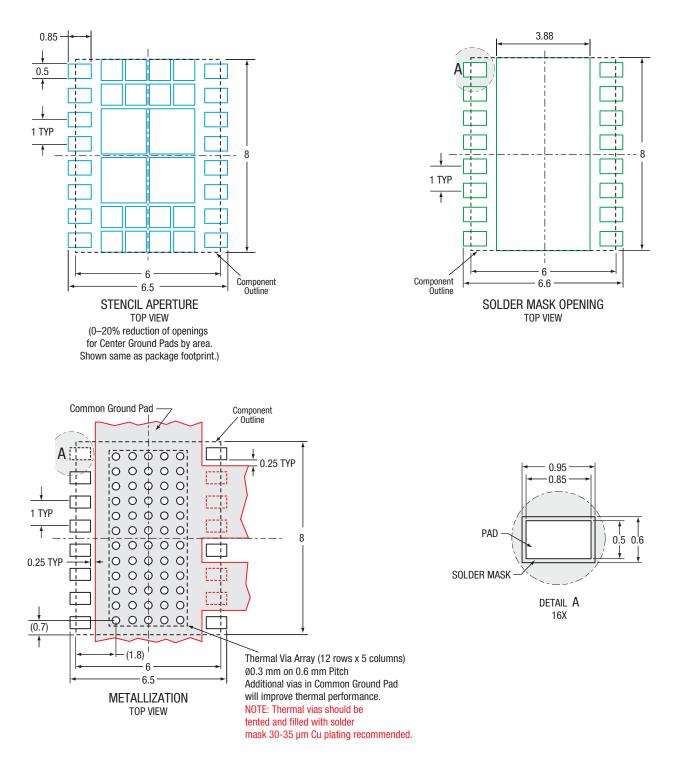


1. Dimensioning and Tolerancing in accordance with ASME Y14.5M-1994.

2. Pads are solder mask defined on 3 edges.



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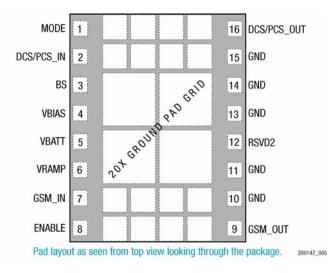


Figure 5. SKY77340 Pin Configuration (Top View)

Table 6.	SKY77340	Pin Names and	Signal Descriptions
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Pad	Name	Description
1	MODE	GMSK/EDGE Power Control Mode: Low = GMSK, High = EDGE
2	DCS/PCS_IN	RF Input (DCS / PCS Bands) DC Blocked
3	BS	Band Select
4	VBIAS	Analog PA Bias Control (ALL BANDS, EDGE MODE)
5	VBATT	DC Supply
6	VRAMP	Analog Output Power Control (ALL BANDS, GMSK MODE)
7	GSM_IN	RF Input (CEL / EGSM Bands) DC Blocked
8	EN	Transmit Enable / Disable. Low = Disable
9	GSM_OUT	RF Output (CEL / EGSM Bands) DC Blocked
*12	RSVD2	Reserved
*16	DCS/PCS_OUT	RF Output (DCS / PCS Bands) DC Blocked
*10, 11, 13–15	GND	Ground
Pad	gnd pad grid	Ground pad grid is device underside.

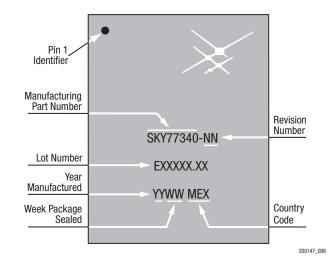


Figure 6. Typical Case Markings

Package and Handling Information

Because of its sensitivity to moisture absorption, this device package is baked and vacuum-packed prior to shipment in accordance with IPC J-STD 033 guidelines. Instructions on the shipping container label are in accordance with IPC J-STD 020B regarding exposure to moisture after the container seal is broken. These instructions must be followed; otherwise, problems related to moisture absorption may occur when the part is subjected to high temperature during solder assembly.

The SKY77340 is capable of withstanding an MSL3/250 °C solder reflow. Care must be taken when attaching this product, whether it is done manually or in a production solder reflow environment. If the part is attached in a reflow oven, the temperature ramp rate should not exceed 3 °C per second; maximum temperature should not exceed 250 °C. If the part is manually attached, precaution should be taken to insure that the part is not subjected to temperatures exceeding 250 °C for more than 10 seconds. For details on attachment techniques, precautions, and handling procedures recommended by Skyworks, please refer to Skyworks Application Note: *PCB Design and SMT Assembly/Rework*, Document Number 101752. Additional information on standard SMT reflow profiles can also be found in the *JEDEC Standard J-STD–020*.

Production quantities of this product are shipped in the standard tape-and-reel format. For packaging details, refer to Skyworks Application Note: *Tape and Reel Information – RF Modules,* Document Number 101568.

Electrostatic Discharge Sensitivity

The SKY77340 is a Class 1 device. ESD testing was performed in compliance with JEDEC standards JESD22-A114 (Human Body Model), JESD22-A115 (Machine Model), and JESD22-C101 (Charged Device Model).

Various failure criteria can be utilized when performing ESD testing. Many vendors employ relaxed ESD failure standards, which fail devices only after "the pin fails the electrical specification limits" or "the pin becomes completely non-functional". Skyworks' most stringent criteria fail devices as soon as the pin begins to show any degradation on a curve tracer. To avoid ESD damage, both latent and visible, it is very important that the product assembly and test areas follow the Class-1 ESD handling precautions listed in Table 7.

Table 7. Precautions for Handling GaAs IC-based Products to Avoid ESD-Induced Damage

Avoiu ESD-induceu Damage		
	Wrist Straps	
Personnel	Conductive Smocks	
Grounding	Gloves and Finger Cots	
	Antistatic ID Badges	
Facility	Relative Humidity Control and Air Ionizers	
Facility	Dissipative Floors (less than $10^9 \Omega$ to GND)	
	Dissipative Table Tops	
Protective	Protective Test Equipment (Properly Grounded)	
Workstation	Grounded Tip Soldering Irons	
workstation	Conductive Solder Suckers	
	Static Sensors	
	Bags and Pouches (Faraday Shield)	
Protective Packaging	Protective Tote Boxes (Conductive Static Shielding)	
and	Protective Trays	
Transportation	Grounded Carts	
	Protective Work Order Holders	

Ordering Information

Model Number	Manufacturing Part Number	Product Revision	Package	Operating Temperature
SKY77340	SKY77340		6 x 8 x 1.2 mm	–25 °C to 85 °C

Revision History

Revision	Level	Date	Description
А		October 17, 2006	Initial Release

References

Application Note: Tape and Reel Information – RF Modules, Document Number 101568

Application Note: PCB Design and SMT Assembly/Rework, Document Number 101752

JEDEC JESD22-A114 (Human Body Model)

JEDEC JESD22-A115 (Machine Model)

JEDEC JESD22-C101 (Charged Device Model)

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