DATA SHEET

SKY73089-11: 1200 to 1700 MHz High Gain and Linearity Diversity Downconversion Mixer

Applications

- 2G/3G base station transceivers:
  - GSM/EDGE, CDMA, UMTS/WCDMA
- Land mobile radio
- High performance radio links

Features

- Operating frequency range: 1200 to 1700 MHz
- IF frequency range: 50 to 500 MHz
- Conversion gain: 9.3 dB
- Input IP3: +26.8 dBm
- Output IP3: +36.1 dBm
- Noise Figure: 9.3 dB
- Integrated LO drivers
- Integrated low loss RF baluns
- High linearity IF amplifiers
- On-chip SPDT LO switch (greater than 40 dB LO-to-LO isolation)
- Small, MCM (36-pin, 6 x 6 mm) package (MSL3, 260 °C per JEDEC J-STD-020)

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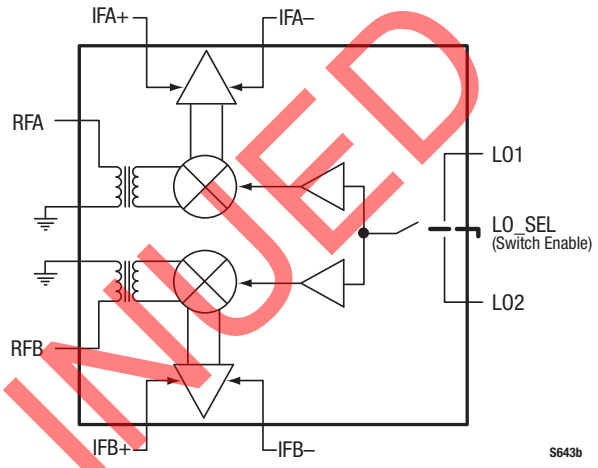


Figure 1. SKY73089-11 Block Diagram

Description

The SKY73089-11 is a fully integrated diversity mixer that includes Local Oscillator (LO) drivers, an LO switch, high linearity mixers, and large dynamic range intermediate frequency (IF) amplifiers. Low loss RF baluns have also been included to reduce design complications and lower system cost.

The SKY73089-11 features an input IP3 of +26.8 dBm and a noise figure (NF) of 9.3 dB, making the device an ideal solution for high dynamic range systems such as 2G/3G base station receivers. The LO switch provides more than 40 dB of isolation between LO inputs and supports the switching time required for GSM/EDGE base stations.

The SKY73089-11 is manufactured using a robust silicon BiCMOS process and has been designed for optimum long-term reliability. The SKY73089-11 diversity downconversion mixer is provided in a compact, 36-pin Multi-Chip Module (MCM). A functional block diagram is shown in Figure 1. The pin configuration and package are shown in Figure 2. Signal pin assignments and functional pin descriptions are provided in Table 1.

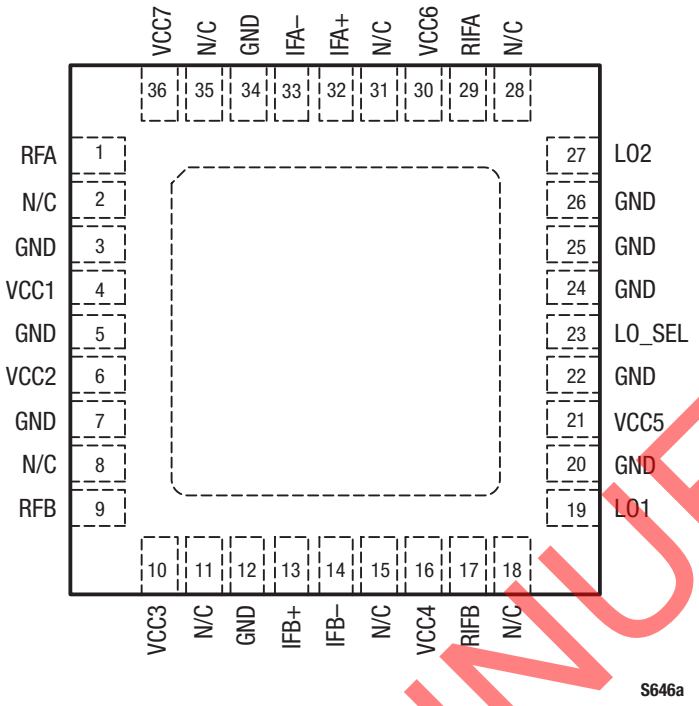


Figure 2. SKY73089-11 Pinout (Top View)

Table 1. SKY73089-11 Signal Descriptions

Pin	Name	Description	Pin	Name	Description
1	RFA	RF channel A input	19	LO1	Local oscillator #1 input
2	NC	No connect	20	GND	Ground
3	GND	Ground	21	VCC5	DC supply, +5 V
4	VCC1	DC supply, +5 V	22	GND	Ground
5	GND	Ground	23	LO_SEL	Local oscillator switch select
6	VCC2	DC supply, +5 V	24	GND	Ground
7	GND	Ground	25	GND	Ground
8	NC	No connect	26	GND	Ground
9	RFB	RF channel B input	27	LO2	Local oscillator #2 input
10	VCC3	DC supply, +5 V	28	NC	No connect
11	NC	No connect	29	RIFA	IF channel A bias control
12	GND	Ground	30	VCC6	DC supply, +5 V
13	IFB+	IF channel B positive output	31	NC	No connect
14	IFB-	IF channel B negative output	32	IFA+	IF channel A positive output
15	NC	No connect	33	IFA-	IF channel A negative output
16	VCC4	DC supply, +5 V	34	GND	Ground
17	RIFB	IF channel B bias control	35	NC	No connect
18	NC	No connect	36	VCC7	DC supply, +5 V

Functional Description

The SKY73089-11 is a high gain diversity mixer, optimized for base station receiver applications. The device consists of two diversity channels, each consisting of a low loss RF balun, high linearity passive mixer, and a low noise IF amplifier.

LO amplifiers are also included that allow the SKY73089-11 to connect directly to the output of a voltage controlled oscillator (VCO). This eliminates the extra gain stages needed by most discrete passive mixers. A single-pole, double-throw (SPDT) switch has been included to select between two different LO inputs for frequency hopping applications (i.e., GSM).

RF Baluns and Passive Mixer

The RF baluns provide a single ended input, which can easily be matched to 50 Ω using a simple matching circuit. The RF baluns offer very low loss and excellent amplitude and phase balance.

The high linearity mixer is a passive, double balanced mixer that provides a very low insertion loss, and excellent third order input insertion point (IIP3) and linearity performance.

Additionally, the balanced nature of the mixer provides for excellent port-to-port isolation.

LO Buffers and SPDT LO Switch

The LO buffers allow the input power of the SKY73089-11 to be programmed in the range of -6 to +6 dBm. The LO section has been optimized for high-side LO injection. However, the LO can be driven over a wide frequency range with only slight degradation in performance.

A high isolation SPDT switch allows the SKY73089-11 to be used for frequency hopping applications. This switch provides greater than 60 dB of LO1 to LO2 isolation:

LO_SEL Logic:	State:
High	LO1 enabled
Low	LO2 enabled

For applications that do not require frequency hopping, LO_SEL is fixed to one state and the appropriate LO input is used.

IF Amplifier

The SKY73089-11 includes high dynamic range IF amplifiers that follow the passive mixers in the signal path. The outputs require a supply voltage connection using inductive chokes. These choke inductors should be high-Q and have the ability to handle 200 mA or greater.

A simple matching network allows the output ports to be matched to a balanced 200 Ω impedance. The IF amplifiers are optimized for IF frequencies between 50 and 500 MHz. The IF amplifiers can be operated outside of this range, but with a slight degradation in performance.

Electrical and Mechanical Specifications

The absolute maximum ratings of the SKY73089-11 are provided in Table 2. The recommended operating conditions are specified in Table 3 and electrical specifications are provided in Table 4.

Spurious suppression measurements are provided in Table 5.

Typical performance characteristics of the SKY73089-11 are illustrated in Figures 3 through 29.

Table 2. SKY73089-11 Absolute Maximum Ratings[1]

Parameter	Symbol	Min	Typ	Max	Units
Supply voltage, +5 V (VCC1 - VCC7)	VCC	4.5	5.0	5.5	V
Supply current	I_{CC}		370	430	mA
RF input power	P_{RF}			+20	dBm
LO input power	P_{LO}		0	+20	dBm
Operating case temperature	T_C	-40		+85	°C
Junction temperature[2]	T_J			+150	°C
Storage case temperature	T_{STG}	-40		+125	°C

1 Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to device with only one parameter set at the limit and all other parameters set at or below their nominal value. Exceeding any of the limits listed here may result in permanent damage to the device.

2 Nominal thermal resistance (junction to center ground pad) is 5.1 °C/W.

***ESD HANDLING**: Although this device is designed to be as robust as possible, electrostatic discharge (ESD) can damage this device. This device must be protected at all times from ESD when handling or transporting. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD handling precautions should be used at all times.*

Table 3. SKY73089-11 Recommended Operating Conditions

Parameter	Symbol	Min	Typ	Max	Units
RF frequency range	F_{RF}	1200		1700	MHz
LO frequency range[1]	F_{LO}	1250		2150	MHz
IF frequency range	F_{IF}	50		450	MHz
Supply voltage, +5 V (VCC1 - VCC7)	VCC	4.75	5.00	5.25	V
Supply current	I_{CC}		370		mA
LO input power	P_{LO}	-6	0	+6	dBm
LO select logic: high low	 LO_SEL_H LO_SEL_L	 2.2 		 0.8	 V V
Operating case temperature	T_C	-40		+85	°C

1 The SKY73089-11 has been optimized for high-side LO injection. However, the LO can be used outside of the specified frequency range with degraded performance.

Table 4. SKY73089-11 Electrical Specifications[1]
(Voltage Supply = +5 V, T_C = +25 °C, LO = 0 dBm, RF Frequency = 1445 MHz, IF Frequency = 350 MHz, LO Frequency = 1795 MHz, Unless Otherwise Noted)

Parameter	Symbol	Test Condition	Min	Typ	Max	Units
Conversion gain	G		8.6	9.3		dB
Conversion gain variation over temperature	G	-40 °C to +85 °C		±0.6		dB
Noise figure	NF			9.3	11.0	dB
Noise figure variation over temperature	NF	-40 °C to +85 °C		±0.8		dB
Noise figure with a blocker signal	NF_{BLK}	Blocking signal input power = +8 dBm			25	dB
Third order input intercept point	IIP3	F_{RF} = 1445 and 1445.8 MHz, P_{RF} = -10 dBm/each tone	+24.0	+26.8		dBm
Third order input intercept point variation over temperature	IIP3	-40 °C to +85 °C		±0.5		dB
Third order output intercept point	OIP3	F_{RF} = 1445 and 1445.8 MHz, P_{RF} = -10 dBm/each tone		+36.1		dBm
2LO - 2RF	2x2	P_{RF} = -10 dBm		-71	-60	dBc
3LO - 3RF	3x3	P_{RF} = -10 dBm		-83	-70	dBc
Input 1 dB compression point	IP1dB		+11.7	+13.9		dBm
Input 1 dB compression point variation over temperature	IP1dB	-40 °C to +85 °C		±0.6		dB
Output 1 dB compression point	OP1dB			+22.2		dBm
LO1 to LO2 isolation			40	49		dB
Channel-to-channel isolation			46	54		dB
RF to IF isolation			60	67		dB
LO leakage:						
1xLO to RF port				-28	-25	dBm
2xLO to RF port				-22	-20	dBm
3xLO to RF port				-52	-32	dBm
4xLO to RF port				-68	-50	dBm
1xLO to IF port				-62	-30	dBm
LO_SEL input			-20	+150	+250	µA
LO switching time					1.0	µs
RF port input return loss	Z_{IN_RF}	With external matching components	14			dB
LO port input return loss	Z_{IN_LO}	With external matching components	14			dB
IF port input return loss	Z_{OUT_IF}	With external matching components	14			dB

[1] Performance is guaranteed only under the conditions listed in this table.

Table 5. SKY73089-11 Spur Suppression Measurements, 3GPP Bands 11 and 21[1]

Parameter	Symbol	Test Condition	Min	Typ	Max	Units
4RF-3LO	4X3	P_{RF} = 0 dBm, F_{RF} = 1440 MHz, IF spur frequency = 375 MHz		-98	-85	dBc
6RF-5LO	6X5	P_{RF} = 0 dBm, F_{RF} = 1445 MHz, IF spur frequency = 305 MHz		-112	-85	dBc
7RF-6LO	7X6	P_{RF} = 0 dBm, F_{RF} = 1480 MHz, IF spur frequency = 410 MHz		-122	-85	dBc
9RF-7LO	9X7	P_{RF} = 0 dBm, F_{RF} = 1430 MHz, IF spur frequency = 305 MHz		-119	-85	dBc
10RF-8LO	10X8	P_{RF} = 0 dBm, F_{RF} = 1400 MHz, IF spur frequency = 360 MHz P_{RF} = 0 dBm, F_{RF} = 1465 MHz, IF spur frequency = 290 MHz		-114 -120	-85 -85	dBc dBc

[1] Performance is guaranteed only under the conditions listed in this table.

Typical Performance Characteristics

(Voltage Supply = +5 V, Tc = +25 °C, LO = 0 dBm, RF Frequency = 350 MHz, IF Frequency = 90 MHz, LO Frequency = 440 MHz, Unless Otherwise Noted)

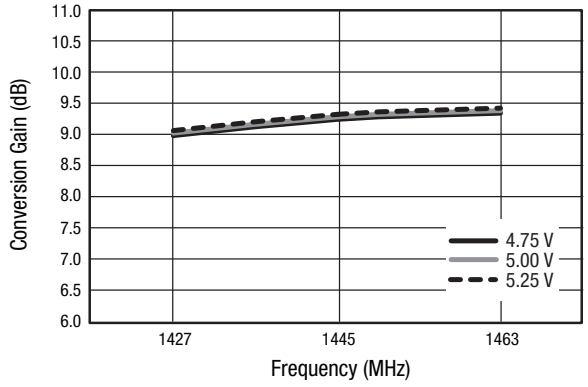


Figure 3. Channel A Gain Over Frequency and Supply Voltage

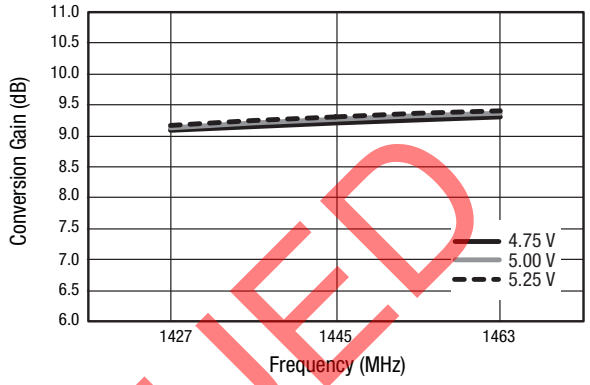


Figure 4. Channel B Gain Over Frequency and Supply Voltage

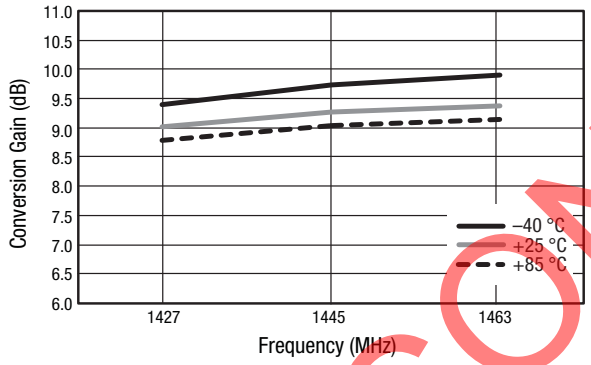


Figure 5. Channel A Gain Over Frequency and Temperature

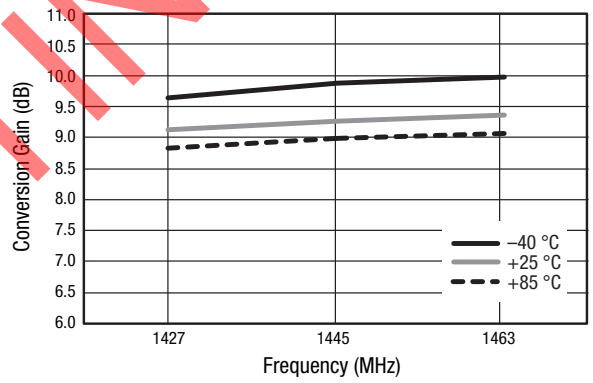


Figure 6. Channel B Gain Over Frequency and Temperature

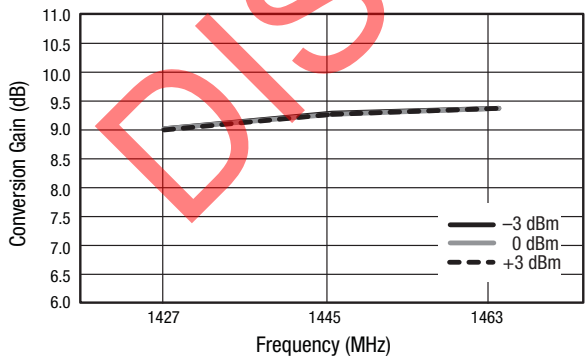


Figure 7. Channel A Gain Over Frequency and LO Power

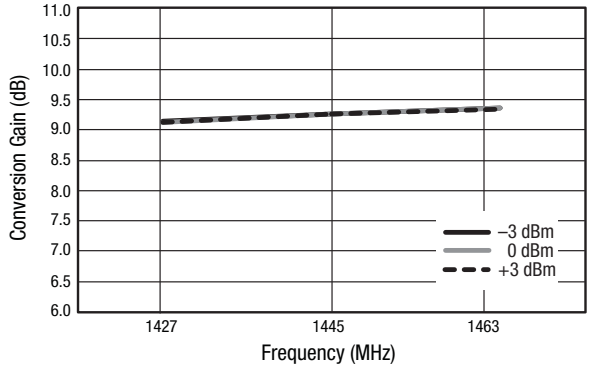


Figure 8. Channel B Gain Over Frequency and LO Power

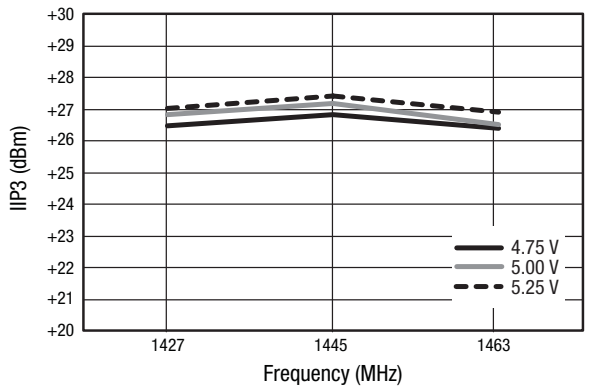


Figure 9. Channel A IIP3 Over Frequency and Supply Voltage

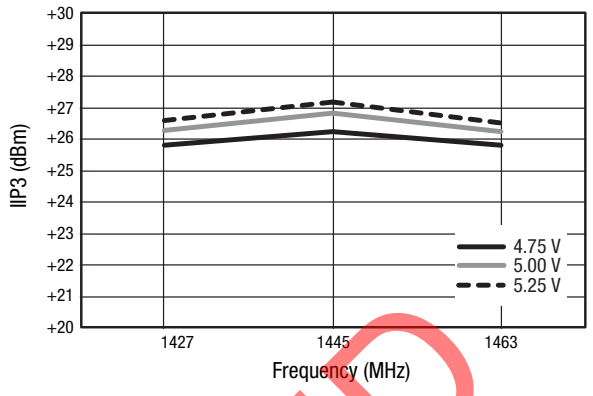


Figure 10. Channel B IIP3 Over Frequency and Supply Voltage

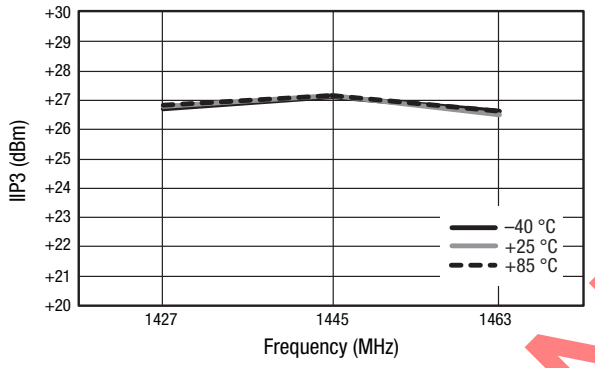


Figure 11. Channel A IIP3 Over Frequency and Temperature

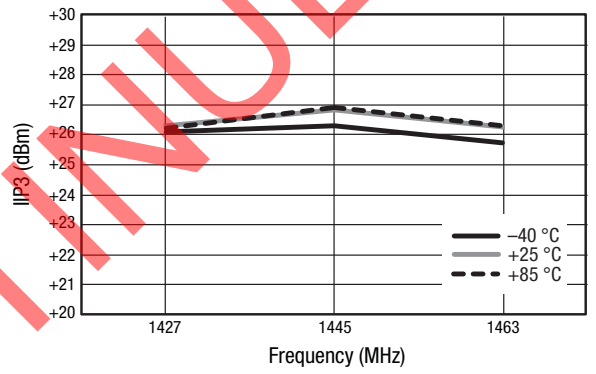


Figure 12. Channel B IIP3 Over Frequency and Temperature

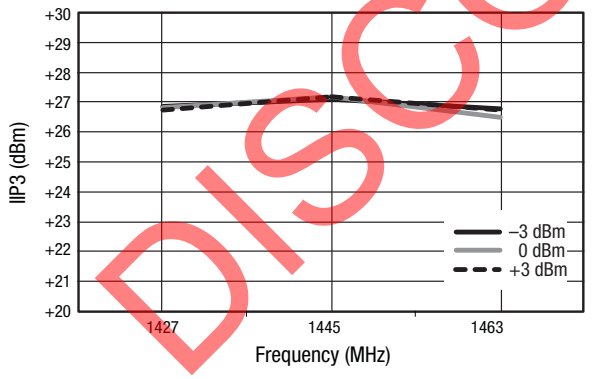


Figure 13. Channel A IIP3 Over Frequency and LO Power

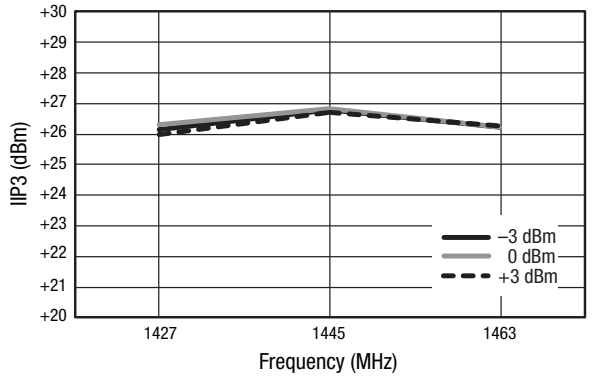


Figure 14. Channel B IIP3 Over Frequency and LO Power

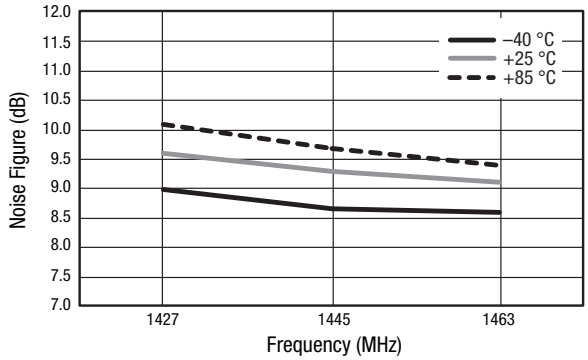


Figure 15. Channel A Noise Figure Over Frequency and Temperature

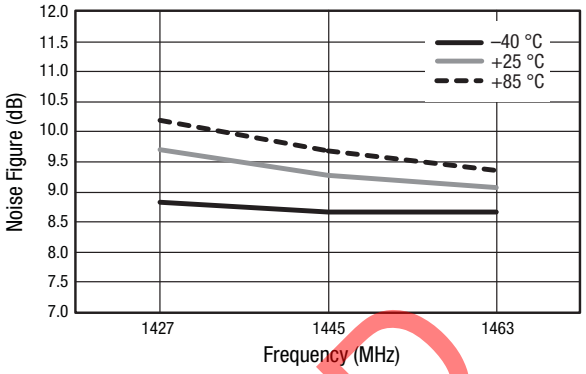


Figure 16. Channel B Noise Figure Over Frequency and Temperature

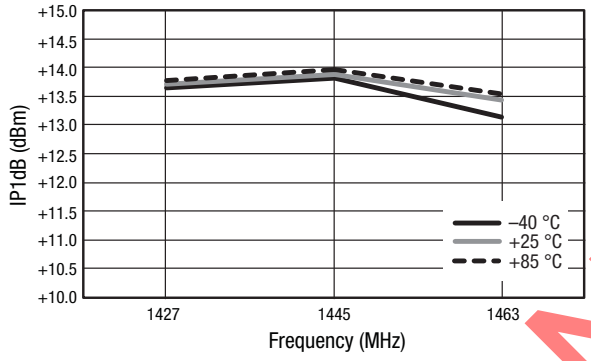


Figure 17. Channel A IP1dB Over Frequency and Temperature

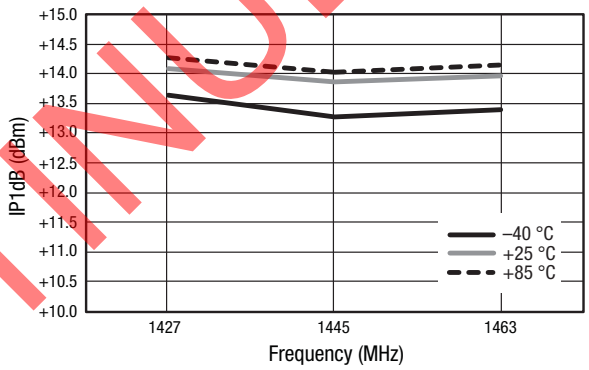


Figure 18. Channel B IP1dB Over Frequency and Temperature

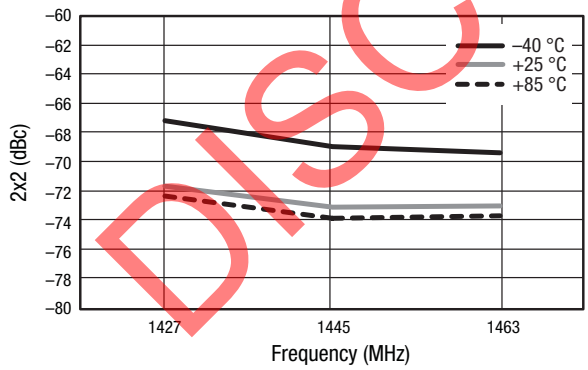


Figure 19. Channel A 2x2 Over Frequency and Temperature

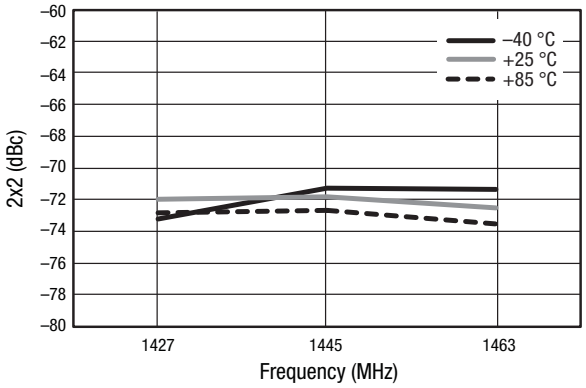


Figure 20. Channel B 2x2 Over Frequency and Temperature

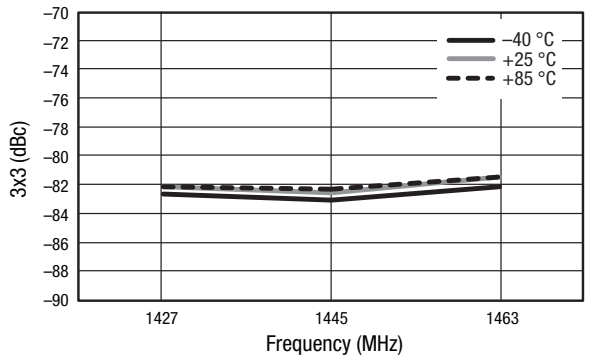


Figure 21. Channel A 3x3 Over Frequency and Temperature

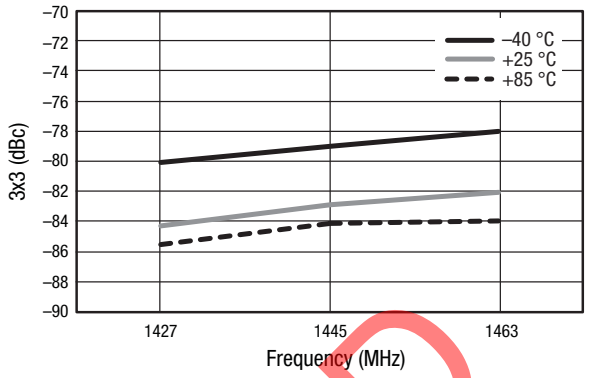


Figure 22. Channel B 3x3 Over Frequency and Temperature

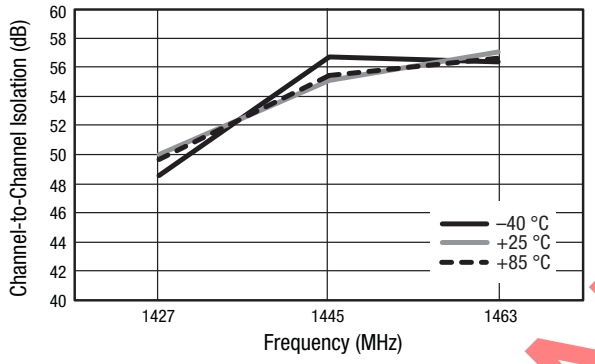


Figure 23. Channel-to-Channel Isolation Over Frequency and Temperature

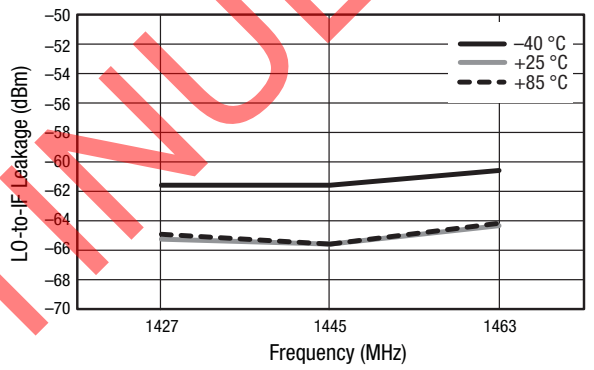


Figure 24. Channel A LO to IF Leakage Over Frequency and Temperature

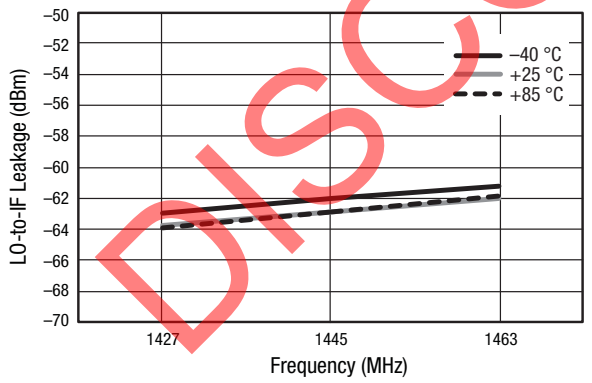


Figure 25. Channel B LO to IF Leakage Over Frequency and Temperature

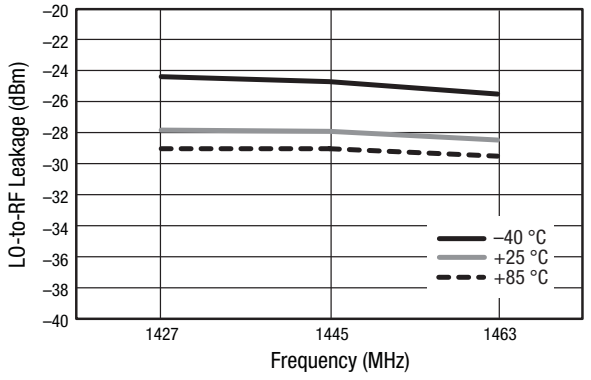


Figure 26. Channel A LO to RF Leakage Over Frequency and Temperature

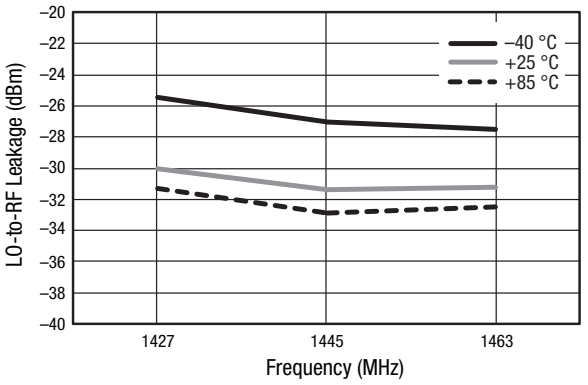


Figure 27. Channel B LO to RF Leakage Over Frequency and Temperature

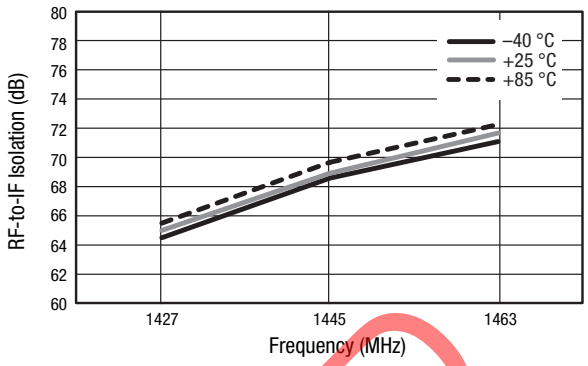


Figure 28. Channel A RF to IF Isolation Over Frequency and Temperature

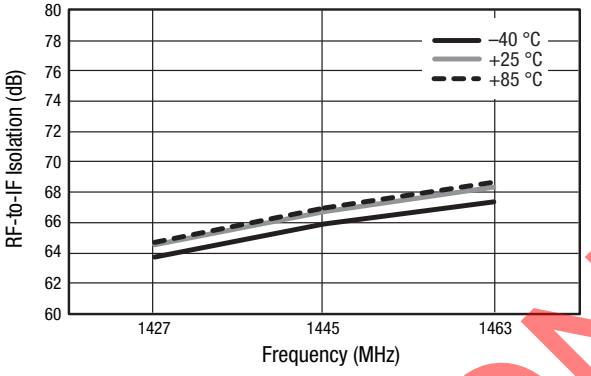


Figure 29. Channel B RF to IF Isolation Over Frequency and Temperature

Evaluation Board Description

The SKY73089-11 Evaluation Board is used to test the performance of the SKY73089-11 downconversion mixer. An assembly drawing for the Evaluation Board is shown in Figure 30, and the layer detail is provided in Figure 31. A schematic diagram of the SKY73089-11 Evaluation Board is shown in Figure 32.

Circuit Design Configurations

The following design considerations are general in nature and must be followed regardless of final use or configuration:

- Paths to ground should be made as short as possible.
- The ground pad of the SKY73089-11 has special electrical and thermal grounding requirements. This pad is the main thermal conduit for heat dissipation. Since the circuit board acts as the heat sink, it must shunt as much heat as possible from the device. Therefore, design the connection to the ground pad to dissipate the maximum wattage produced by the circuit board.
- Skyworks recommends including external bypass capacitors on the VCC voltage inputs of the device.
- Components L5, L6, L14, and L15 (see Figure 32) are high-Q low loss inductors. These inductors must be able to pass currents in excess of 200 mA DC.
- Components R1 and R2 (see Figure 32) set the bias current for the IF amplifiers. Skyworks recommends that these resistors have a tolerance of ±1% to optimize performance consistency of the SKY73089-11. These resistors are not required for the Evaluation Board to operate as specified in Tables 3 and 4.

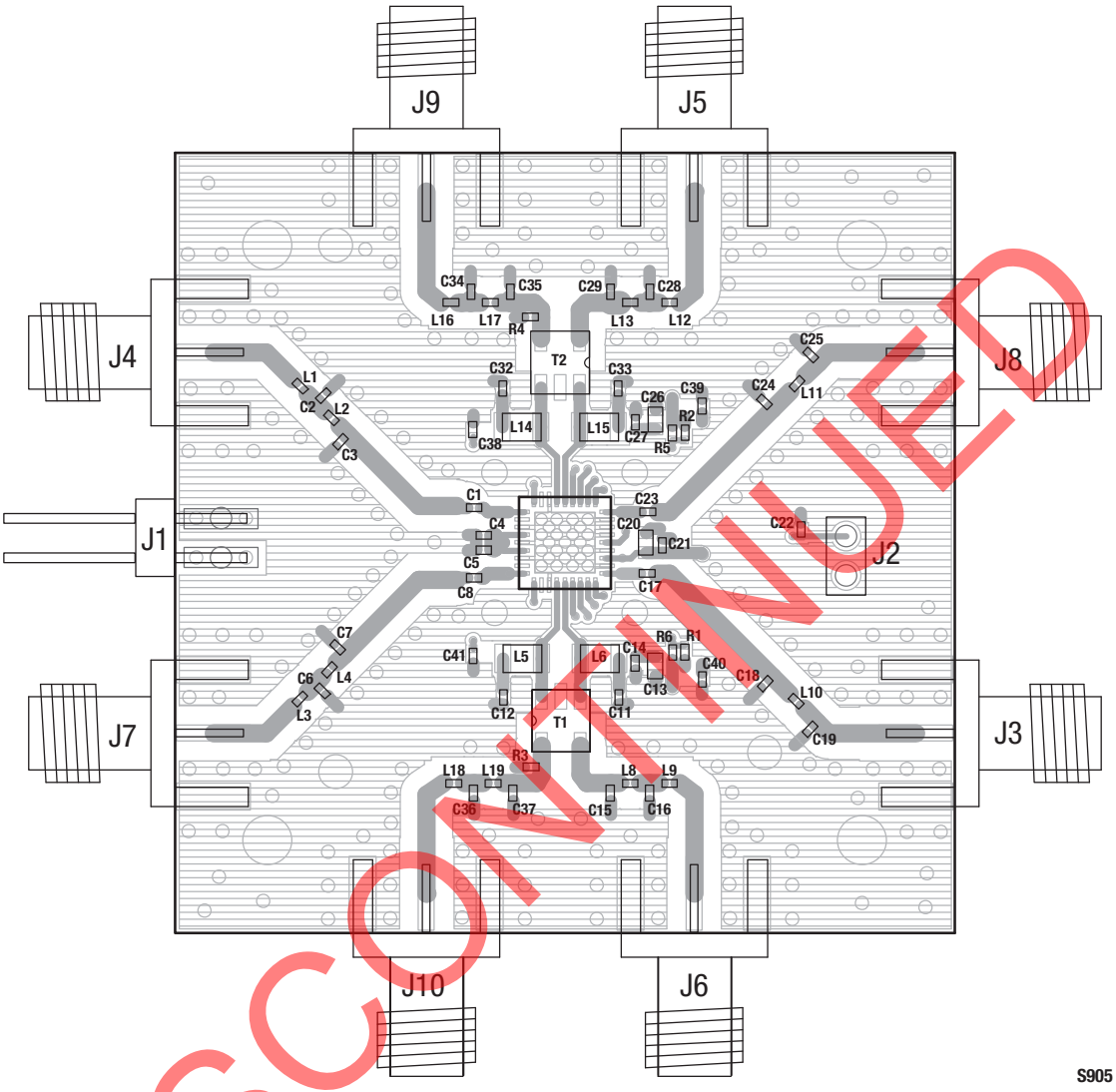
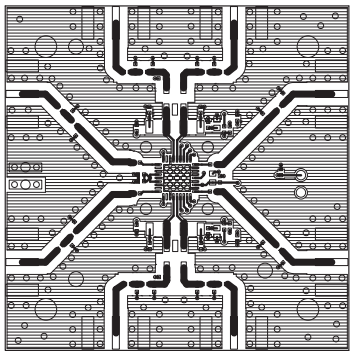
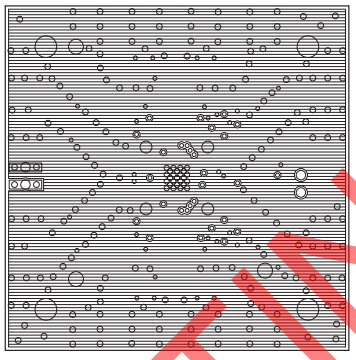


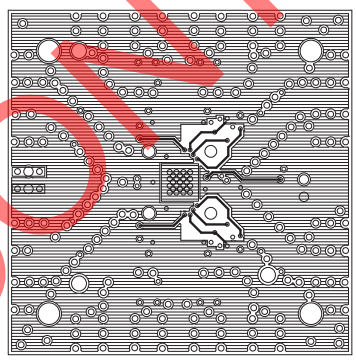
Figure 30. SKY73089-11 Evaluation Board Assembly Diagram



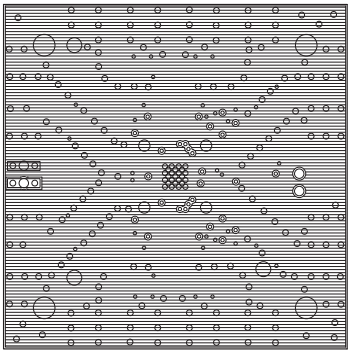
Layer 1: Top -- Metal



Layer 2: Ground



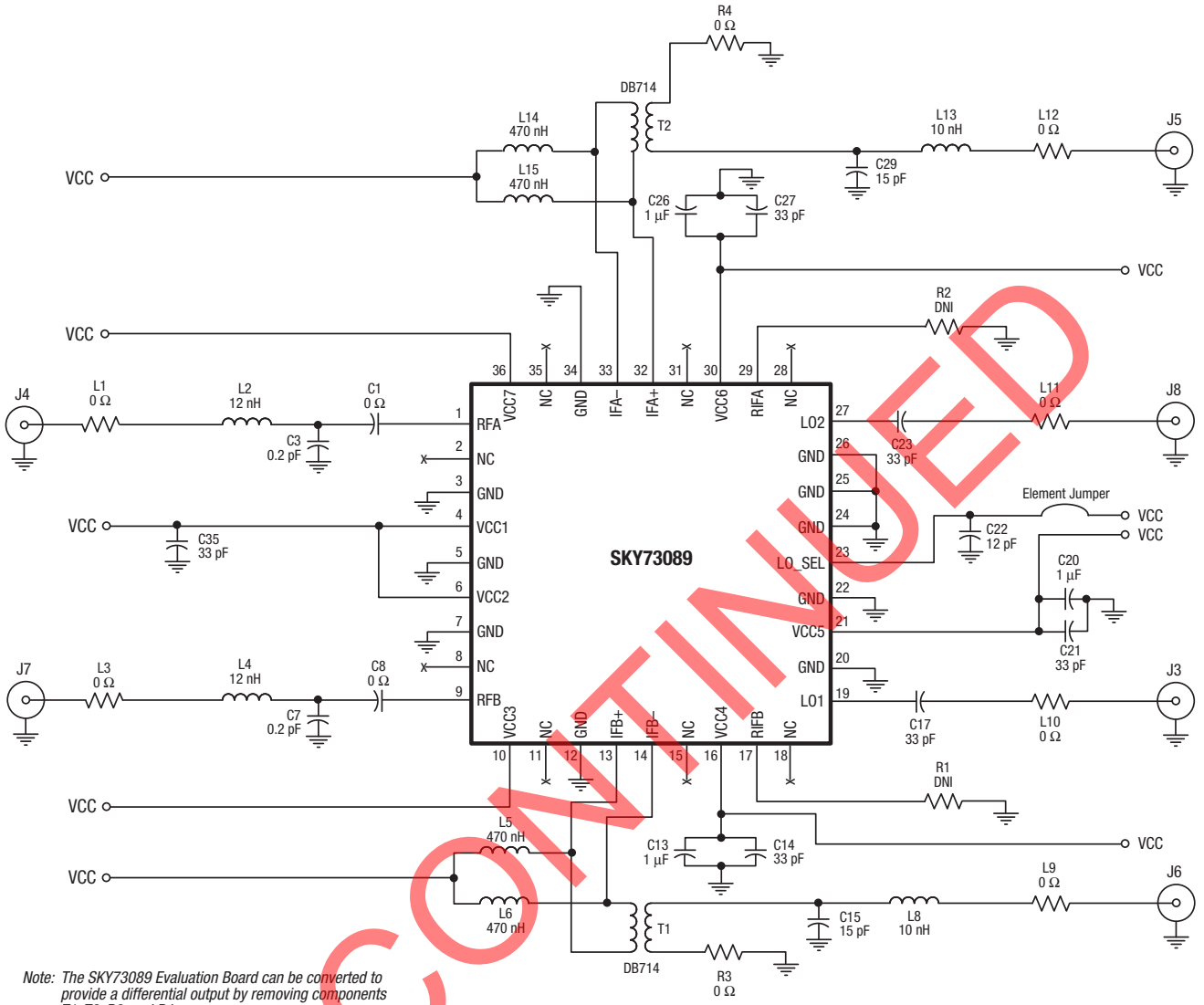
Layer 3: Power Plane



Layer 4: Solid Ground Plane

S904

Figure 31. SKY73089-11 Evaluation Board Layer Detail



Note: The SKY73089 Evaluation Board can be converted to provide a differential output by removing components T1, T2, R3, and R4.

Some component labels may be different than the corresponding component symbol shown here. Component values, however, are accurate as of the date of this Data Sheet.

Figure 32. SKY73089-11 Evaluation Board Schematic

Package Dimensions

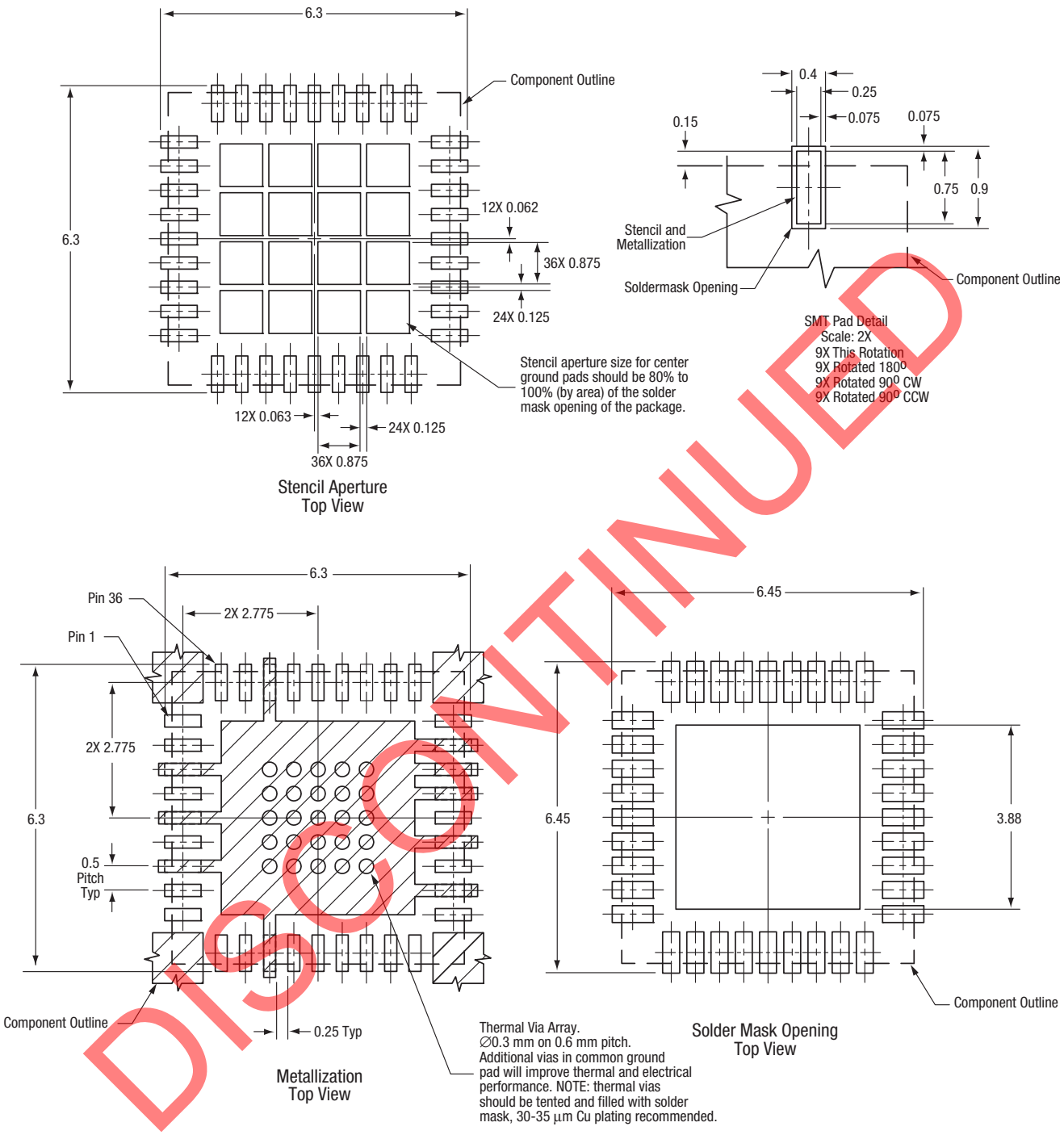
The PCB layout footprint for the SKY73089-11 is provided in Figure 33. Figure 34 shows the package dimensions, and Figure 35 provides the tape and reel dimensions.

Package and Handling Information

Since the device package is sensitive to moisture absorption, it is baked and vacuum packed before shipping. Instructions on the shipping container label regarding exposure to moisture after the container seal is broken must be followed. Otherwise, problems related to moisture absorption may occur when the part is subjected to high temperature during solder assembly.

The SKY73089-11 is rated to Moisture Sensitivity Level 3 (MSL3) at 260 °C. It can be used for lead or lead-free soldering. For additional information, refer to the Skyworks Application Note, *PCB Design & SMT Assembly/Rework Guidelines for MCM-L Packages*, document number 101752.

Care must be taken when attaching this product, whether it is done manually or in a production solder reflow environment. Production quantities of this product are shipped in a standard tape and reel format.

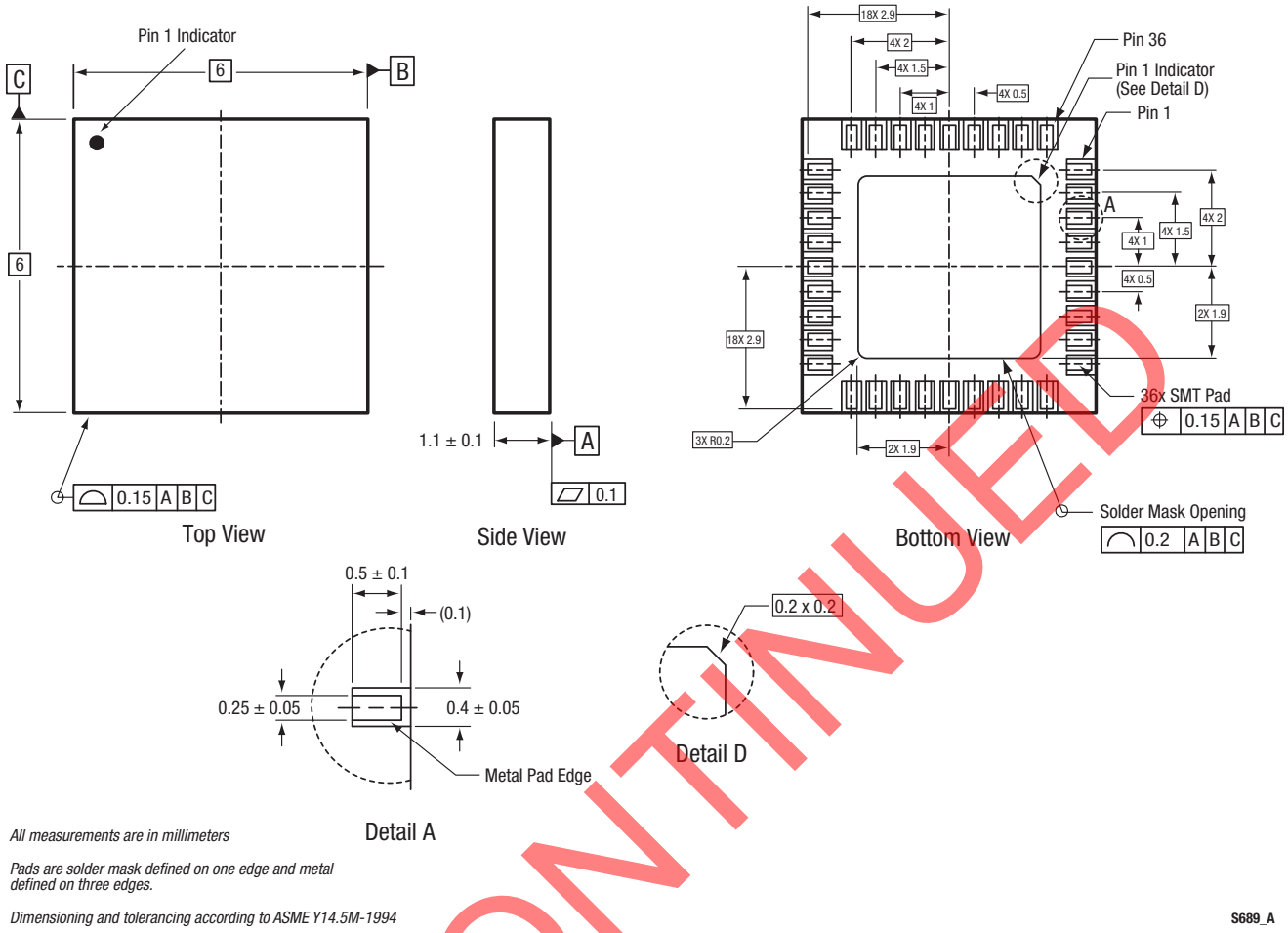


Note: The cross-hatched area represents the merger of the center ground pad +10 individual I/O ground pads. All I/O ground pads should have at least one via connected to internal ground planes for optimum electrical performance.

All measurements are in millimeters

S1125

Figure 33. PCB Layout Footprint for the SKY73089-11



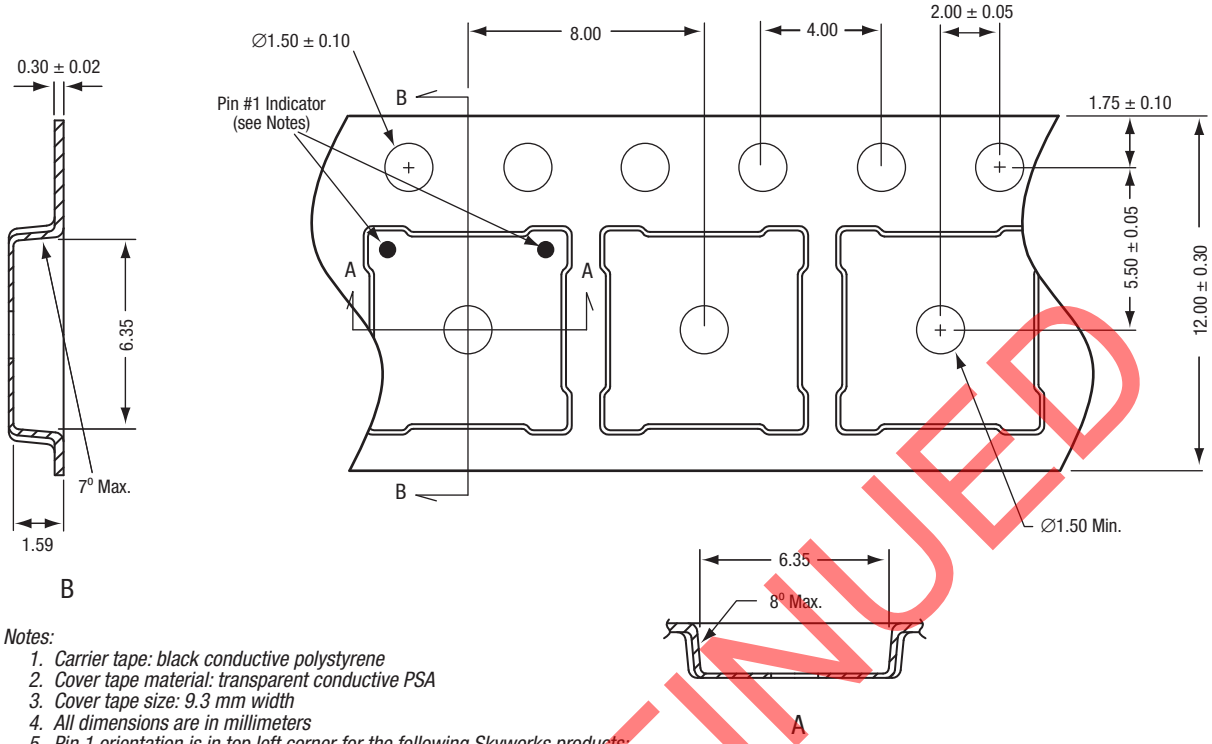
All measurements are in millimeters

Pads are solder mask defined on one edge and metal defined on three edges.

Dimensioning and tolerancing according to ASME Y14.5M-1994

S689_A

Figure 34. SKY73089-11 Package Dimensions



Notes:

1. *Carrier tape: black conductive polystyrene*
2. *Cover tape material: transparent conductive PSA*
3. *Cover tape size: 9.3 mm width*
4. *All dimensions are in millimeters*
5. *Pin 1 orientation is in top left corner for the following Skyworks products:*

   SKY73022-21, -31
   SKY73023-21, -31

   For all other 6 x 6 mm MCM/RFLGA products, pin 1 orientation is in top right corner.

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Figure 35. SKY73089-11 Tape and Reel Dimensions

Ordering Information

Product Description	Product Part Number	Evaluation Board Part Number
SKY73089-11 1200-1700 MHz Downconversion Mixer	SKY73089-11	SKY73089-11-EVB