

Applications

- DSSS 5 GHz WLAN (IEEE802.11a)
- Access Points, PCMCIA, PC cards

Features

- 5GHz Front End Module with PA and T/R Switch
- Integrated power amplifier enable pin (VPAON)
- Buffered, temperature compensated power detector
- High and Low-Linearity mode
- 3% EVM, @17dBm, 64 QAM, 54 Mbps
- 30 dB Typical Gain
- Lead Free and RoHS compliant, halogen free package
- 16 pin 3 mm x 3 mm x 0.9 mm QFN, MSL1

Ordering Information

Part Number	Package	Remark
SE5006L	16 Pin QFN	Samples
SE5006L-R	16 Pin QFN	Tape and Reel
SE5006L-EK1	Evaluation Kit	Standard

Product Description

The SE5006L is a 5GHz front end module offering high linear power for wireless LAN applications. It incorporates a power detector for control of the output power.

The SE5006L offers a high level of integration for a simplified design, providing quicker time to market and higher application board production yield. The device integrates the inter-stage match, a temperature compensated, load insensitive power detector with 20dB of dynamic range, a 3.8GHz notch filter, a T/R switch and an RX balun.

For wireless LAN applications, the device meets the requirements of IEEE802.11a and delivers approximately 17dBm of linear output power. It also features a low linearity mode control to reduce current consumption at low power modes.

The SE5006L integrates the reference voltage generator, allowing for a true CMOS compatible digital PAON (enable) function to turn the power amplifier on and off.

Functional Block Diagram

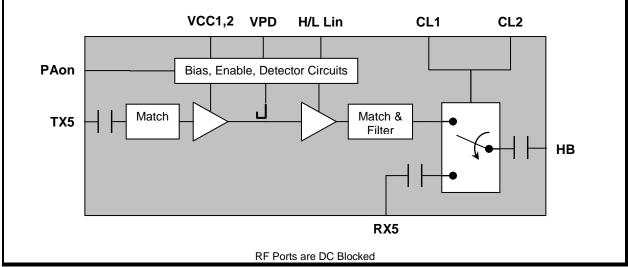


Figure 1: Functional Block Diagram



Pin Out Diagram

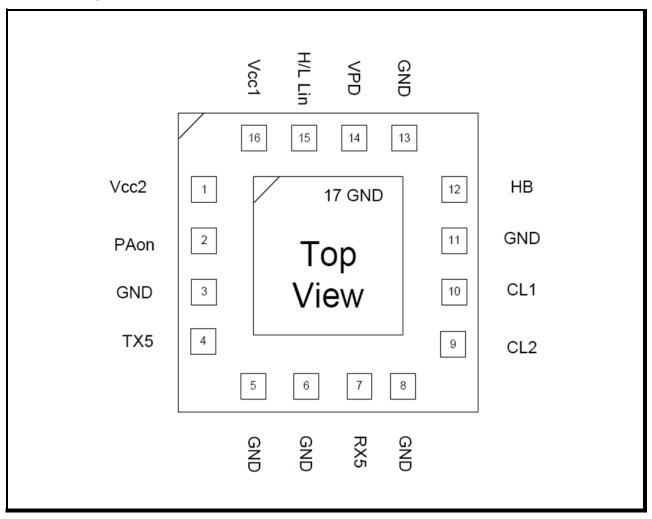


Figure 2: SE5006L Pin-Out Diagram

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Pin Out Description

Pin No.	Name	Description
1	VCC2	Bias & Driver Supply Voltage
2	PAon	PA Enable
3	GND	Ground
4	TX5	TX RF Input Signal
5	GND	Ground
6	GND	Ground
7	RX5	RX RF output signal
8	GND	Ground

Pin No.	Name	Description
9	CL2	Switch Control Logic 2
10	CL1	Switch Control Logic 1
11	GND	Ground
12	HB	5GHz Antenna output
13	GND	Ground
14	VPD	Power Detector Output
15	H/L Lin	High-Low linearity Control
16	VCC1	Power Stage Supply Voltage

Absolute Maximum Ratings

These are stress ratings only. Exposure to stresses beyond these maximum ratings for a long period of time may cause permanent damage to, or affect the reliability of the device. Avoid operating the device outside the recommended operating conditions defined below. This device is ESD sensitive. Handling and assembly of this device should be at ESD protected workstations.

Symbol	Defir	Min.	Max.	Unit	
Vcc1	Supply Voltage on Pin 16	-0.3	4.8		
Vcc2	Supply Voltage on pin 1 (\	-0.3	3.6	V	
PAON	DC input on Enable		-0.3	3.6	V
TX5	RF Input Power, RFout into 50Ω match		-	12	dBm
Tstg	Storage Temperature Range		-40	150	°C
	JEDEC JESD22-A114	Antenna Pin	-	1000	V
ESD _{HBM}	all pins	All Other Pins	-	350	V

Recommended Operating Conditions

Symbol	Parameter	Min.	Max.	Unit
Vcc	Supply Voltage on pin 16 (VCC1)	3.0	4.5	V
Vcc	Supply Voltage on pin 1 (VCC2)	3.0	3.6	V
TA	Ambient Temperature	-40	85	°C



Control Logic Characteristics

Conditions:	Vcc1 = Vcc2 = VPAON = VEN = 3.3 V, TA = 25 °C, as measured on Skyworks Solutions' SE5006L-EV1
	evaluation board, unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
		P _{OUT} = 17 dBm, 54 Mbps, 64 QAM, H/L Lin = 3.3V (High Linearity Mode)	-	195	-	
	Querra la Querra d	P _{OUT} = 13 dBm, 54 Mbps, 64 QAM, H/L Lin = 0V (Low Linearity Mode)	-	140	-	
ICC-802.11a	Supply Current	P _{OUT} = 5 dBm, 54 Mbps, 64 QAM, H/L Lin = 0V (Low Linearity Mode)	-	105	-	mA
		P _{OUT} = 19 dBm, 54 Mbps, 64 QAM, H/L Lin = 3.3V, VCC1 = 4.0V	-	220	-	
IOFF	Supply Current	VPAON = 0 V, No RF	-	0.5	10	μA
Vpaonh	Logic High Voltage	-	2.8	-	Vcc	V
Vpaonl	Logic Low Voltage	-	-0.3	-	0.3	V
Ipaonh	Input Current Logic High Voltage	-	-	-	400	μA
Ipaonl	Input Current Logic Low Voltage	-	-	<1	-	μA

Switch Logic Characteristics

Conditions: Vcc = VPAON = 3.3 V, TA = 25 °C, as measured on Skyworks Solutions' SE5006L-EK1 evaluation board, all unused ports terminated with 50 ohms, unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
ON	Low Loss Switch Control Voltage	High State = Vctl_on - Vctl_off	2.8	-	3.6	V
OFF	High Loss Switch Control Voltage	Low State = Vctl_OFF - Vctl_OFF	0	-	0.3	V
CCTL	Control Input Capacitance	-	-	-	100	pF
ICTL	Control Line Current	Vctl = Vctl_on	-	-	1	mA

Switch Control Logic Table

CL1	CL2	TX ↔ ANT	RX ↔ ANT	
OFF	OFF	OFF	OFF	
OFF	ON	OFF	ON	
ON	OFF	ON	OFF	
ON	ON	Not Supported		



AC Electrical Characteristics

Transmit Characteristics

Conditions:	Vcc1 = Vcc2 = VPAON = CL1 = H/L Lin = 3.3V, CL2 = 0V, TA = 25 °C, as measured on Skyworks
	Solutions' SE5006L-EV1 evaluation board, unless otherwise noted

Symbol	Parameter	Condit		Min.	Тур.	Max.	Unit
f∟-∪	Frequency Range	-		4.90	-	5.85	GHz
		802.11a,	EVM = 3%	-	17	-	
	Output Power,	64 QAM	EVM = 2%	-	15	-	
	High Linearity Mode H/L Lin = 3.3V	VCC1 = 4.0V, 3% EVM	EVM = 3%	-	19	-	
POUT	OFDM	MCS0, HT20, ma	sk compliant	-	21	-	dBm
		MCS0, HT40, ma	sk compliant	-	20	-	
	Output Power,	802.11a, 64 QAM	, EVM = 3%	-	13	-	
	Low Linearity Mode H/L Lin = 0V	MCS0, HT20, ma	sk compliant	-	17	-	
	OFDM	MCS0, HT40, ma	sk compliant	-	16	-	
P _{1dB}	Output 1dB compression point	No modulation		-	24	-	dBm
S11	Input Return Loss	Pıℕ = -25 dBm		-	14	-	dB
Sec.	Small Signal Gain,	High Linearity Mode		28	31	-	dB
S21	$P_{IN} = -25 dBm$	Low Linearity Mod	de	26	30	-	
Δ S 21	Small Signal Gain	Gain variation ove 40MHz channel	er single	-	-	0.5	dB
<u>1021</u>	Variation	Gain Variation ov	er band	-1.5	-	1.5	ŭĐ
S 21_3.8	Out of Band Gain	Gain at 3.8GHz		-	-	15	dB
2f	Harmonic	Роит = 17 dBm, C		-	-50	-42	dBm/MHz
3f	Tarrionic			-	-50	-42	UDITI/IVITIZ
tr, tf	Rise and Fall Time	-		-	0.5	-	us
STAB	Stability	Pout = 17 dBm, 5 QAM, VSWR = 6:		All non-harmonically related outputs less than -50 dBc/100 kHz			
Rugged- ness	Tolerance to output load mismatching	Constant P _{IN} equa dBm at 50ohms, 9 QAM, VSWR = 6	54 Mbps, 64	No damage			
Robust	Robustness to input power	P _{IN} = 12dBm, CW all phases	, VSWR = 6:1,		No c	lamage	



Receive Characteristics

Conditions: Vcc = CL2 = 3.3V, CL1 = VPAON = 0V, TA = 25 °C, as measured on Skyworks Solutions' SE5006L-EK1 evaluation board, all unused ports terminated with 50 ohms, unless otherwise noted.

Symbol	Parameter	Condition	Min.	Тур.	Max.	Unit
Fout	Frequency Range	-	5.15	-	5.75	GHz
RXı∟	Insertion Loss	-	-	1.0	1.5	dB
	Input Return Loss	At the Antenna port	10	12	-	
RXrl	Output Return Loss	At RX RF output	10	12	-	dB
T _{on/off}	T/R on/off switching speed	CL1, CL2 (50%) to RF output (10% or 90%)	-	-	500	nSec



Power Detector Characteristics

Conditions: Vcc = VPAON =CL1 = 3.3V, CL2 = 0V, f = 5.4 GHz, TA = 25 °C, as measured on Skyworks Solutions' SE5006L-EV1 evaluation board, unless otherwise noted

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
PDR	Pout detect range	-	0	-	P _{1dB}	dBm
VDET ₂₂	Detector voltage	Роит = 22 dBm	0.80	-	1.0	V
VDET ₁₆	Detector voltage	Роит = 16 dBm	0.55	-	0.60	V
VDET ₂	Detector voltage	Pout = 2 dBm	0.25	-	0.35	V
PDZout	Output Impedance	-	-	5	-	KΩ

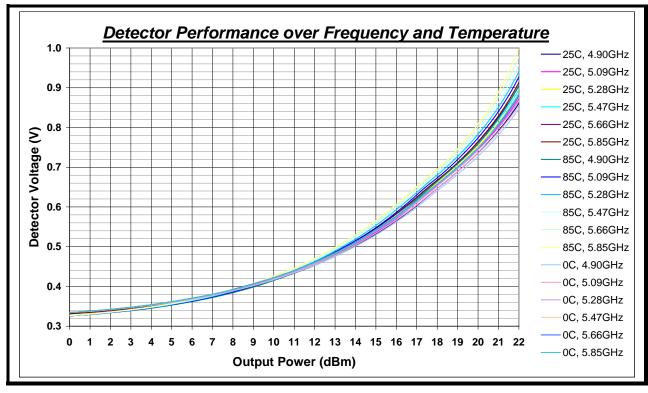


Figure 3: SE5006L Power Detector Characteristic



Package Diagram

This package is Pb free and RoHS compliant. The product is rated MSL1.

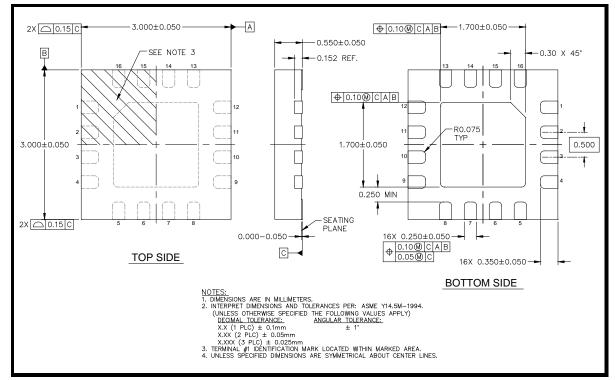


Figure 4: SE5006L Package Diagram



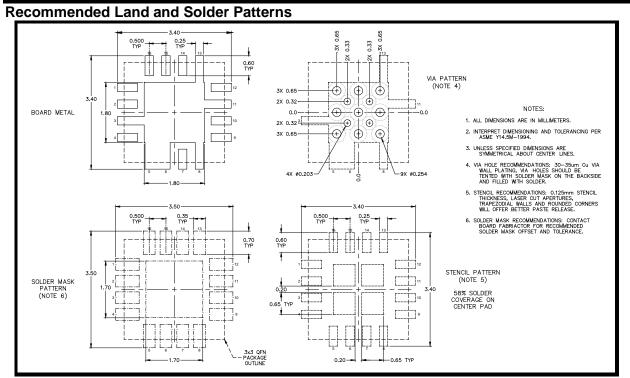


Figure 5: SE5006L Recommended Land and Solder Pattern

Package Handling Information

Because of its sensitivity to moisture absorption, instructions on the shipping container label must be followed regarding exposure to moisture after the container seal is broken, otherwise, problems related to moisture absorption may occur when the part is subjected to high temperature during solder assembly. The SE5006L is capable of withstanding a Pb free solder reflow. Care must be taken when attaching this product, whether it is done manually or in a production solder reflow environment. If the part is manually attached, precaution should be taken to insure that the device is not subjected to temperatures above its rated peak temperature for an extended period of time. For details on both attachment techniques, precautions, and handling procedures recommended, please refer to:

- "Quad Flat No-Lead Module Solder Reflow & Rework Information", Document Number QAD-00045
- "Handling, Packing, Shipping and Use of Moisture Sensitive QFN", Document Number QAD-00044



Branding Information

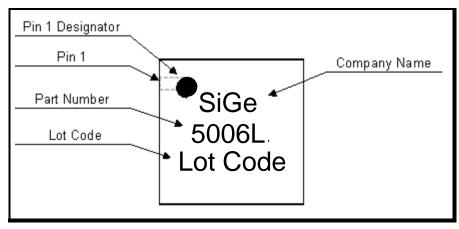


Figure 6: SE5006L Branding

Tape and Reel Information

Parameter	Value			
Devices Per Reel	3000			
Reel Diameter	13 inches			
Tape Width	12 millimeters			
pin 1 corner	Product Code Product Code Product Code Int Number Product Code Int Number Product Code Int Number			

Figure 7: SE5006L-R Tape and Reel Information

Document Change History

Revision	Date	Notes	
1.0	Feb 3, 2010	Created	
1.1	May 07, 2010	Updated pin-out and recommended land pattern Updated RF performance to reflect DVT1 Added MSL rating (MSL3)	
1.2	Oct 26, 2010	Updated switch control line current consumption	
1.3	Jan 03, 2011	Updated recommended operating temperature and recommended storag temperature Updated ESD classification	

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Revision	Date	Notes	
1.4	Feb 24, 2011	Added 4V operation, Mask performance Added "DC Blocked" to block diagram.	
1.5	Apr 03, 2012	Updated with Skyworks logo and disclaimer statement	

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