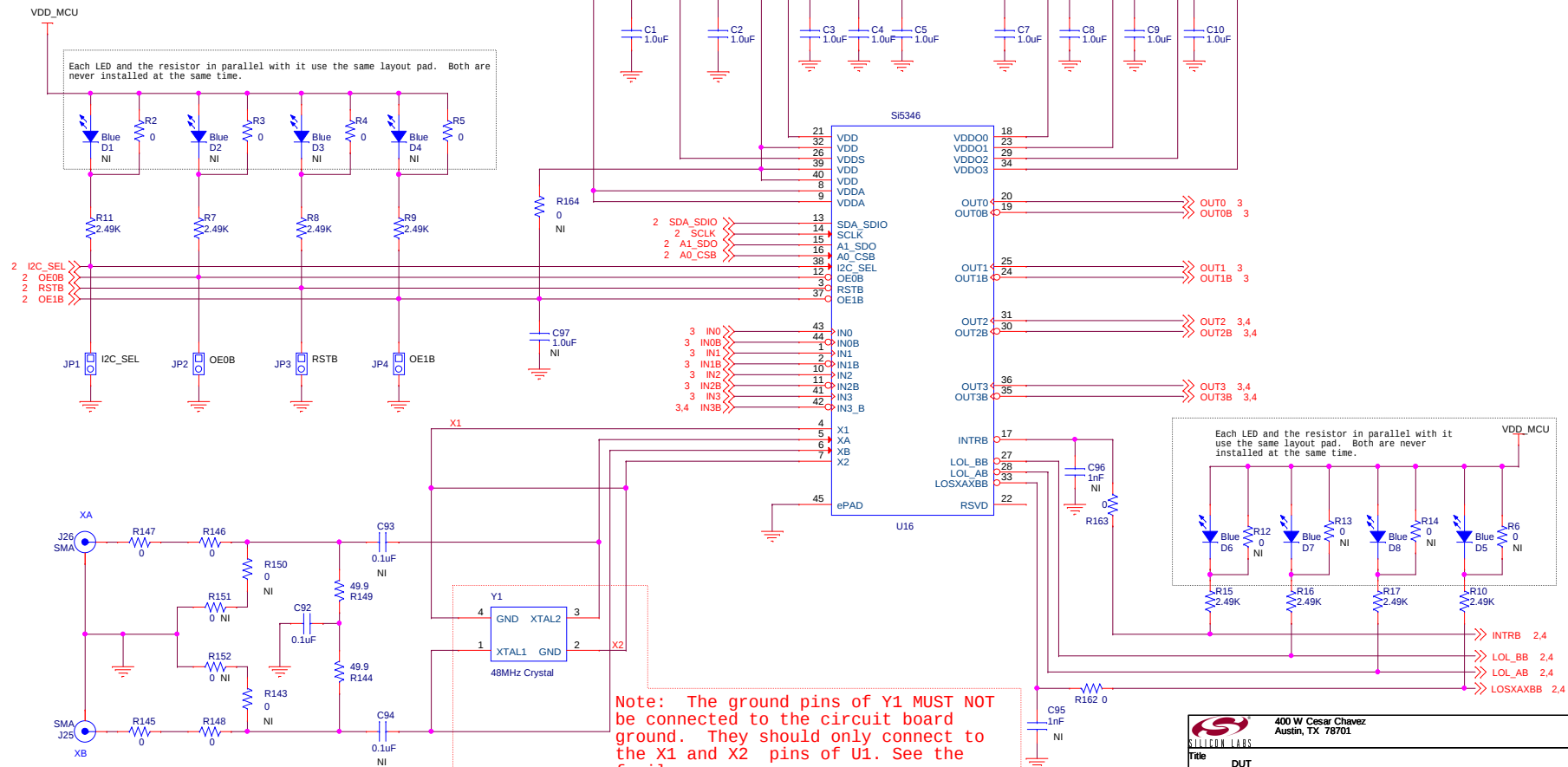


Si5346-EB REV 3.0

LB1
PTL-14-477 Label

Si5346-D-EB

The text of Label LB1 should
match the assembly part number.



400 W Cesar Chavez
Austin, TX 78701

Title

DUT

Size

Document Number

Si5346-D-EB

<Variant Name>

Rev

3.3

Date: Tuesday, April 12, 2016

Sheet 1 of 6

Board Address
101110 = 46d

ID0
ID1
ID2
ID3
ID4
ID5

1 A0_CS#
1 SDA_SDIO
1 AI_SDO
1 SCLK

J20
NI

J21
USB Type B

+V
D-
D+
GND

D10
SP0503BAHT

SW PUSHBUTTON
MCU Reset

VDD_MCU

R71 1K

R74 1K

R73 1K

C52 1uF

J22
5x2 Shrouded Header
MCU PROGRAM

J17
NI

1 NSS
2 SDA/MOSI
3 MISO
4 SCL/USCLK

J20
NI

J21
USB Type B

J22
5x2 Shrouded Header
MCU PROGRAM

J23
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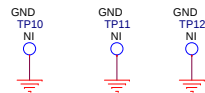
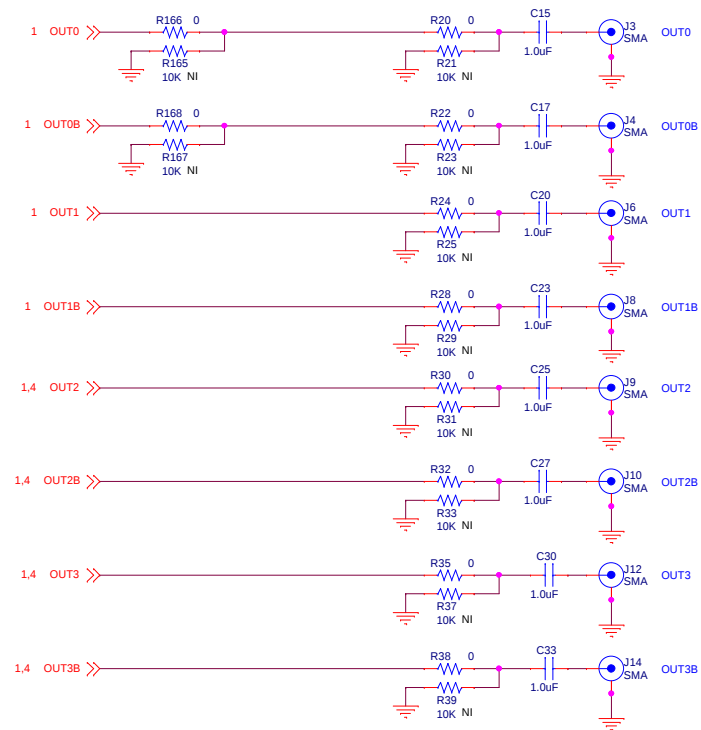
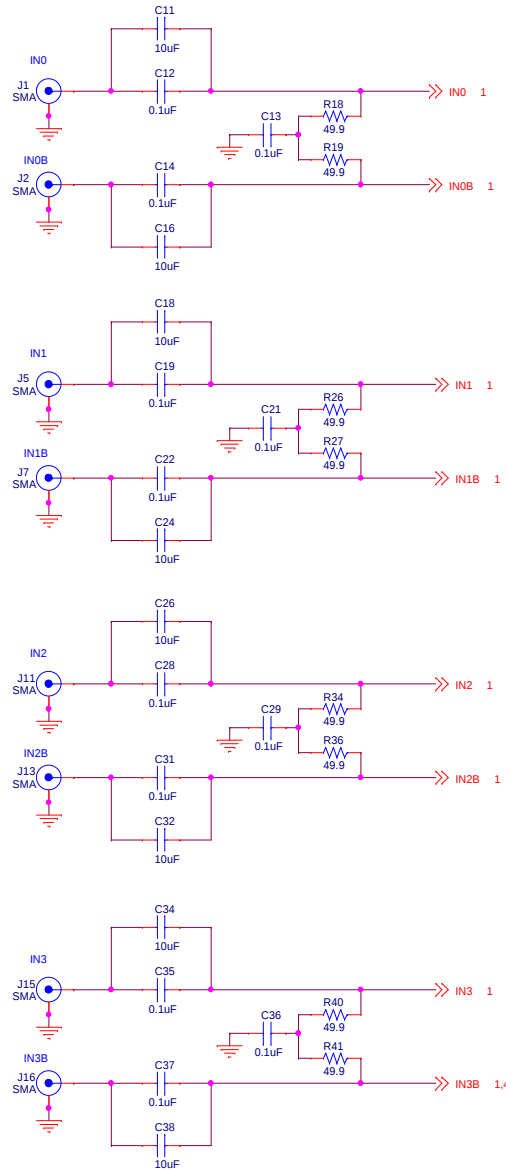
J281
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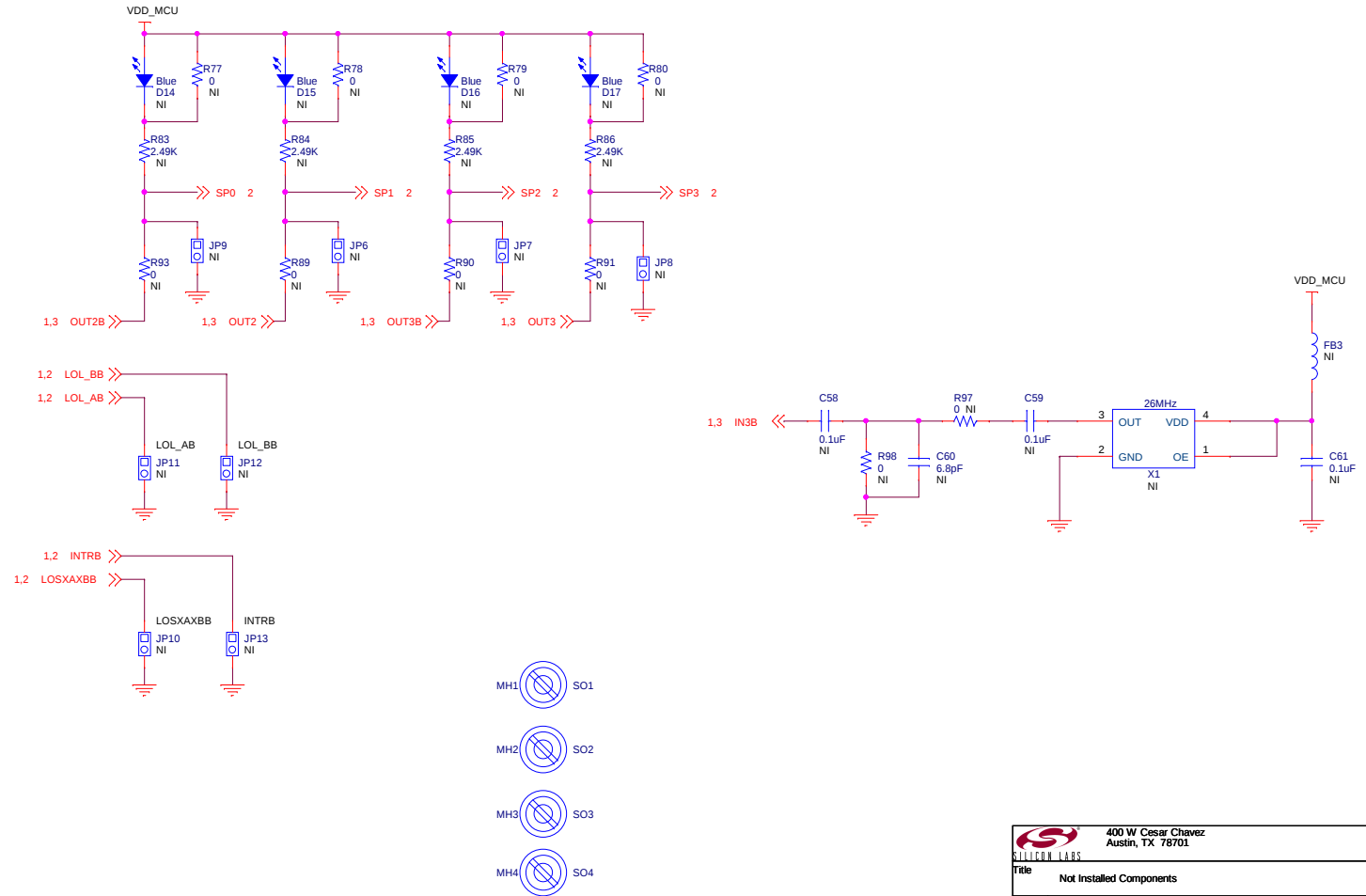




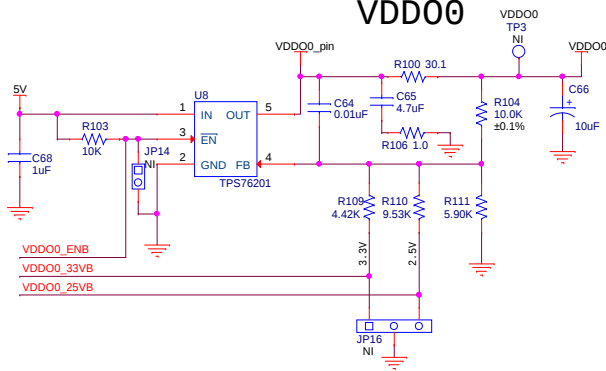
400 W Cesar Chavez
Austin, TX 78701

Title DUT IO		Rev 3.3
Size B	Document Number SIS346-D-EB	<Variant Name>
Date Monday, January 11, 2016	Sheet 3	of 6

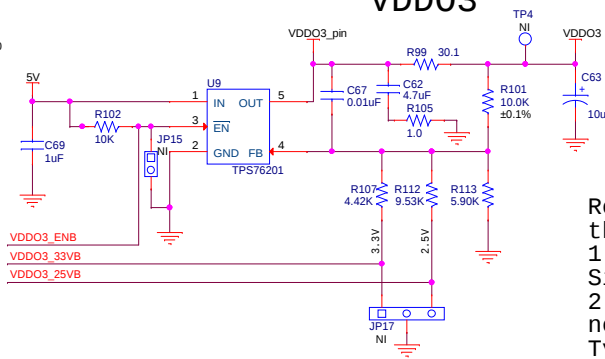
Note: All components on this page are not installed except for the standoffs



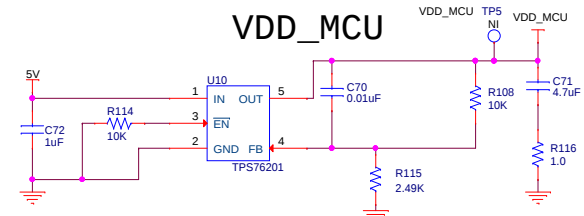
VDD00



VDD03



VDD_MCU

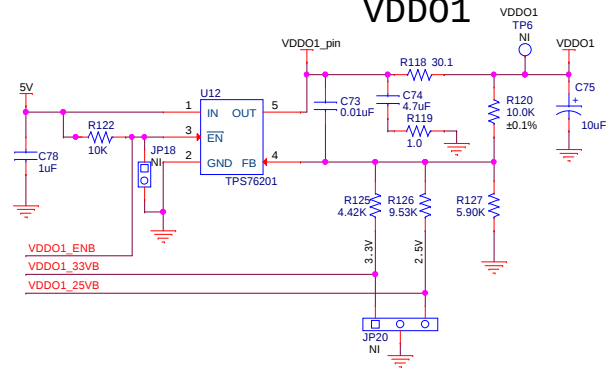


Regulators on this EVB are included to allow the EVB software to

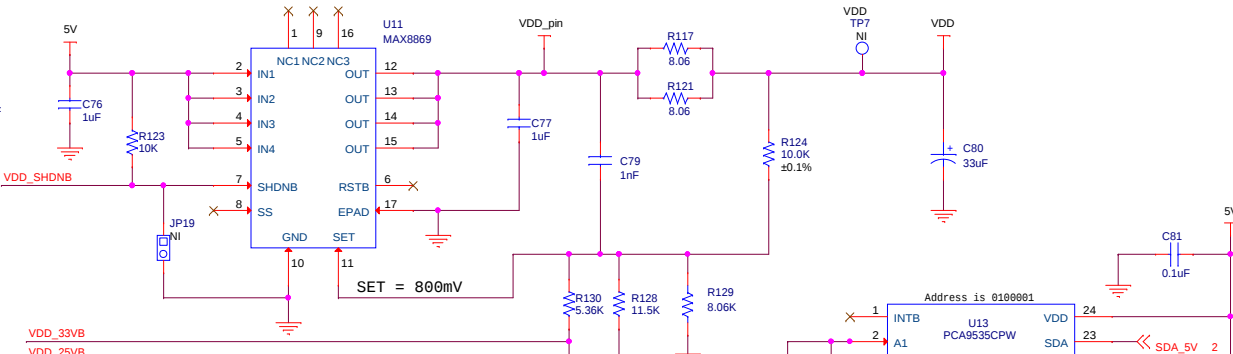
1. Measure the current to each VDD pin of the Si5346
2. Independently control VDD, VDD00/1/2/3 , as needed by customer configurations.

Typical applications will not require independent regulation for each Si5346 VDD supply.

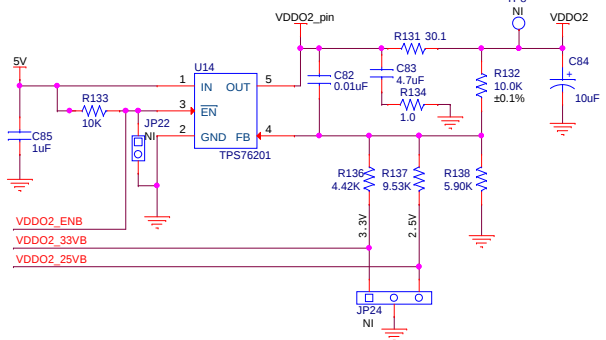
VDD01



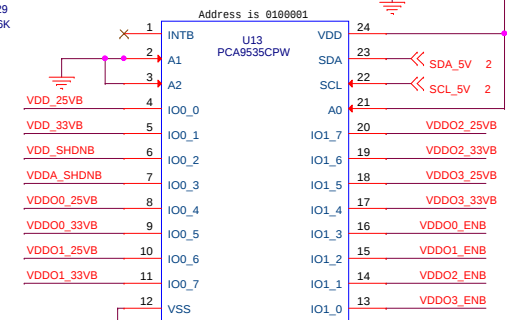
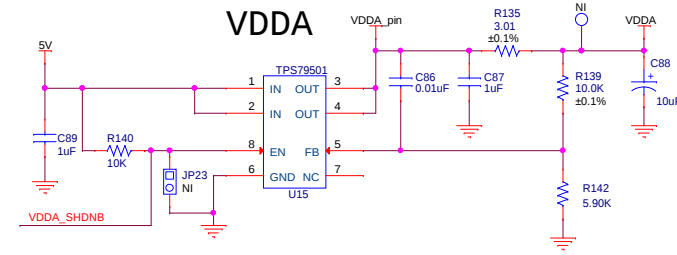
VDD



VDD02



VDDA



 SILICON LABS		400 W Cesar Chavez Austin, TX 78701	
Title		Voltage Regulators	
Size B	Document Number SI5346-D-EB	<Variant Name>	Rev 3.3
Date:	Monday, January 11, 2016	Sheet 5	of 6

REVISION HISTORY

Rev 3.2 - Jan 11, 2016, Initial Board Release
Includes PCB and label on BOM.

Rev 3.3 - April 13, 2016, changed U3 to custom
part number CF380P0997AGQ

		400 W Cesar Chavez Austin, TX 78701	
SILICON LABS			
Title		Revision History	
Size B	Document Number SI5346-D-EB	<Variant Name>	Rev 3.3
Date:	Thursday, April 14, 2016		Sheet 6 of 6