

SI5394-EB REV 1.0

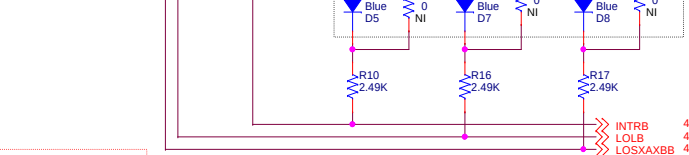
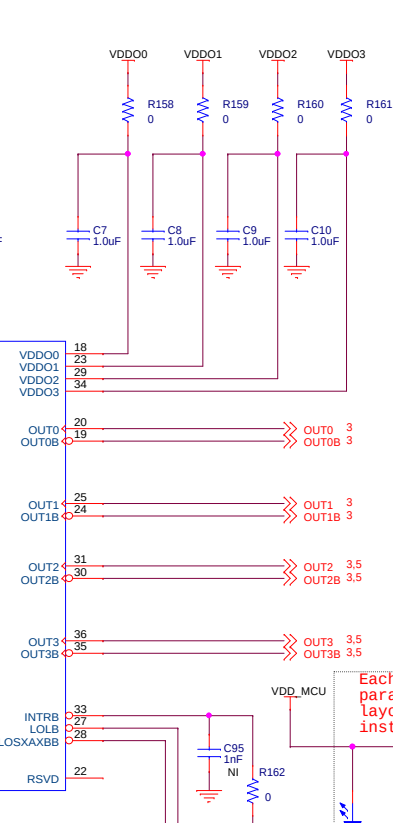
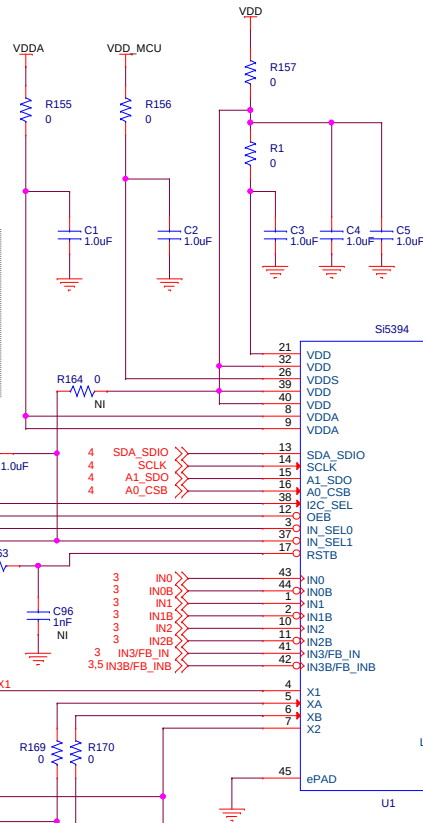
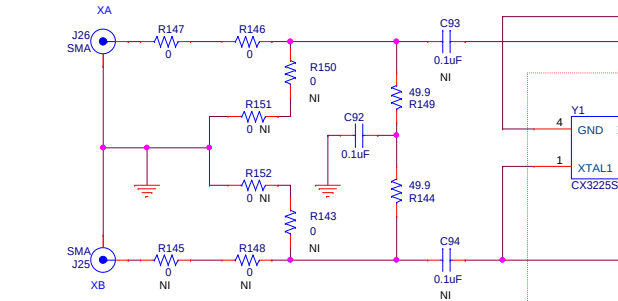
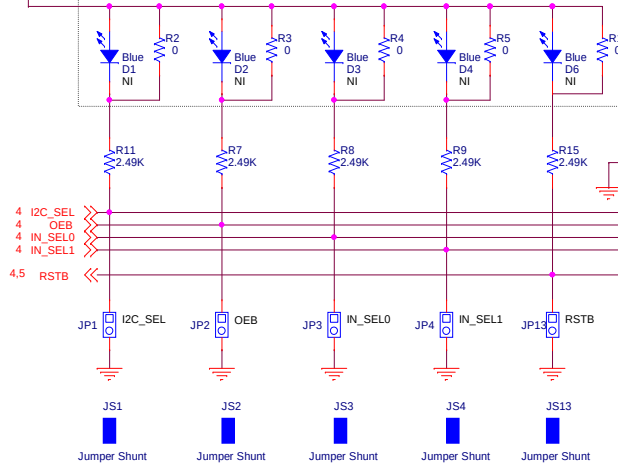
LB1

PTL-14-477 Label

SI5394P-A-EB

The text of Label LB1 should match the assembly part number.

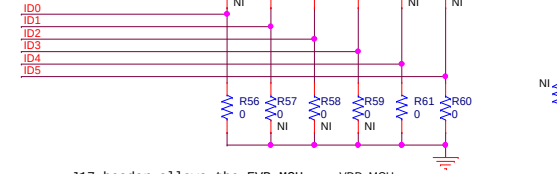
Each LED and the resistor in parallel with it use the same layout pads. Both are never installed at the same time.



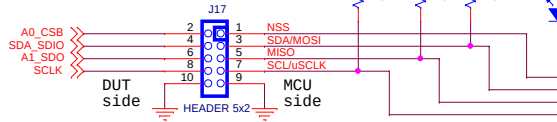
Each LED and the resistor in parallel with it use the same layout pads. Both are never installed at the same time.

Note: The ground pins of Y1 MUST NOT be connected to the circuit board ground. They should only connect to the X1 and X2 pins of U1. See the family reference manual for details on the layout for Y1.

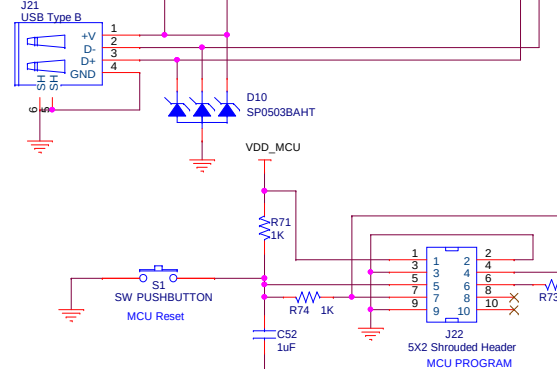
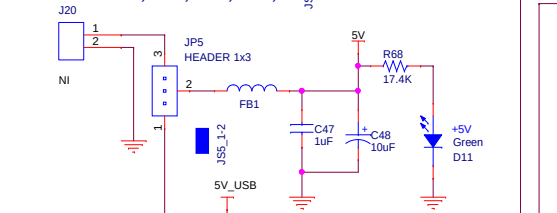
Board Address is
001110 = 14d



J17 header allows the EVB MCU to be connected to an external Si5344. By doing this the Silicon Labs GUI software can read and write to an external Si5344.

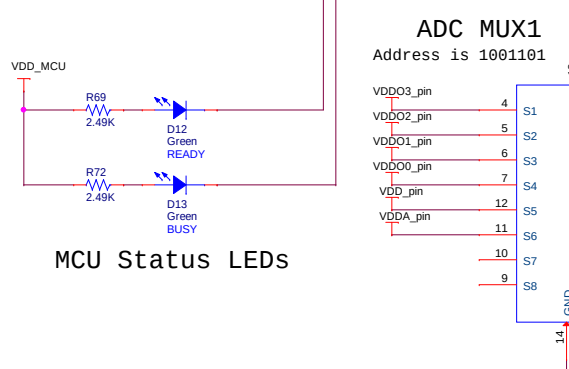
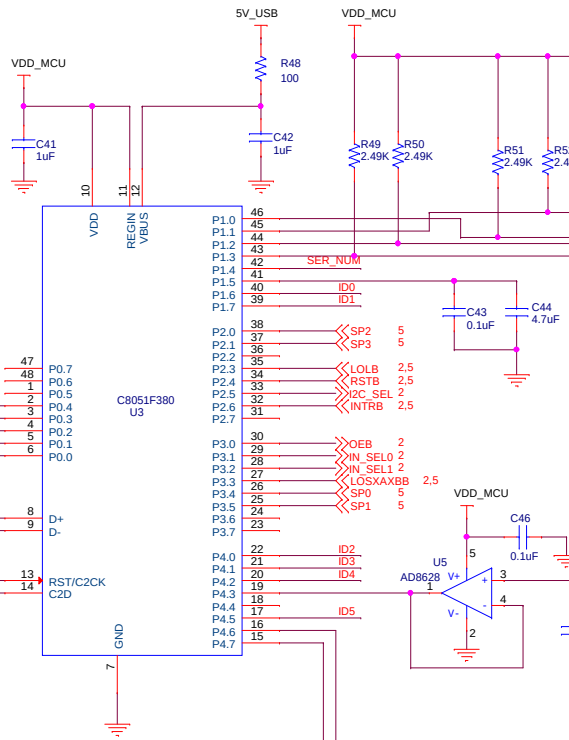


Jumper Shunts



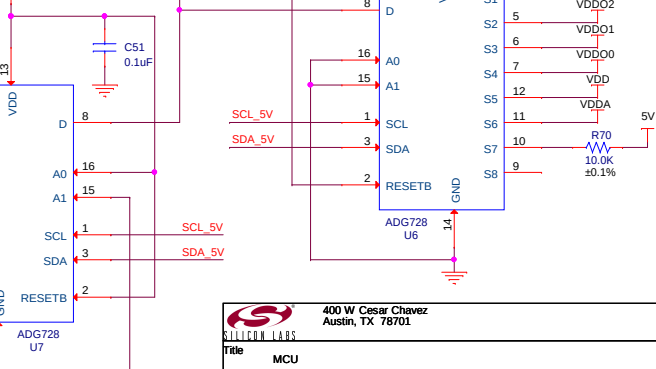
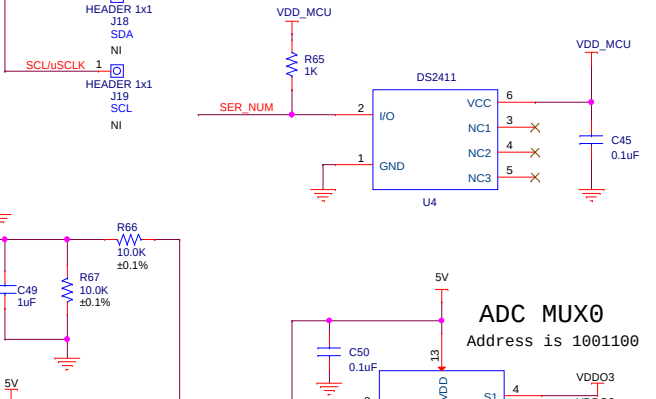
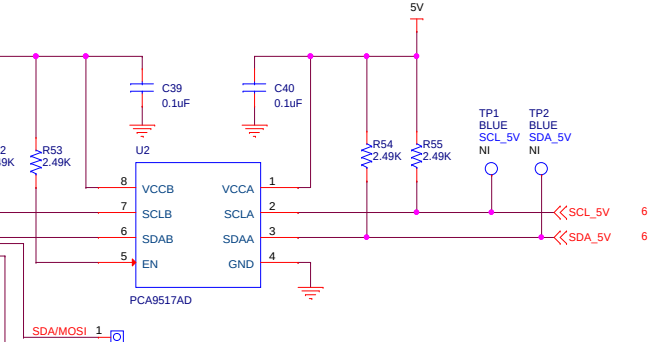
MCU Reset

MCU Programming



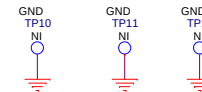
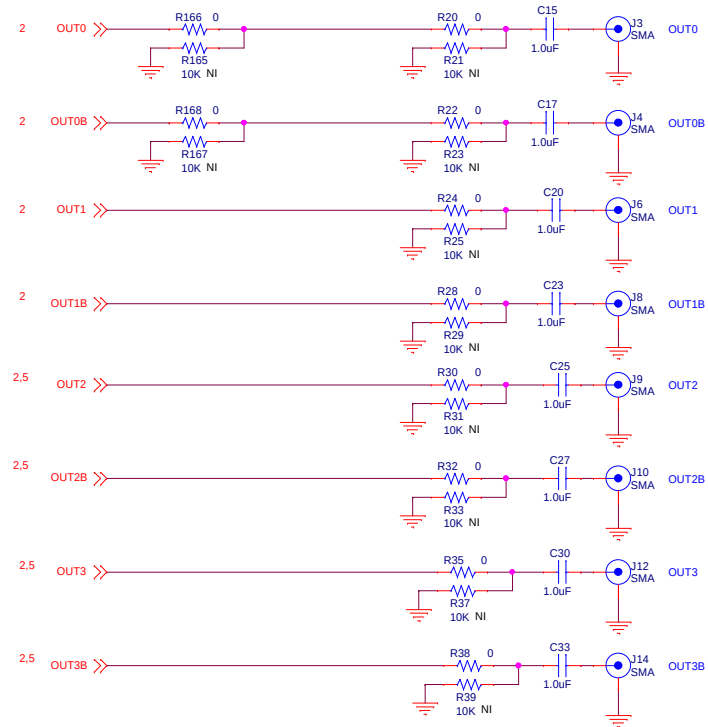
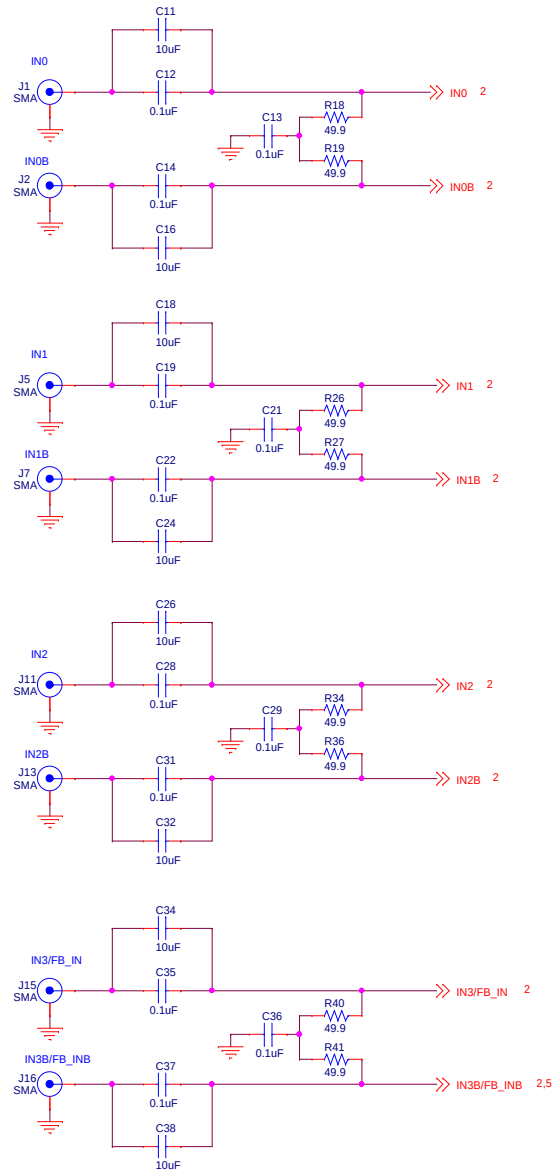
MCU Status LEDs

MCU Status LEDs

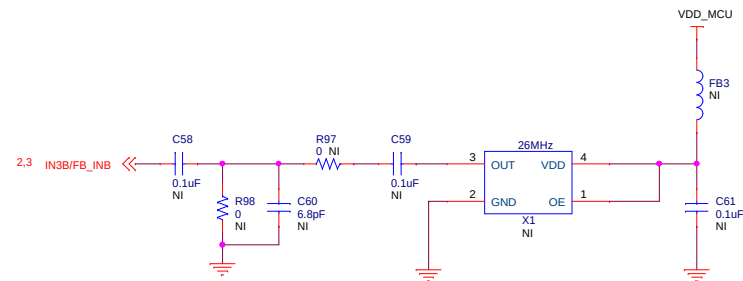
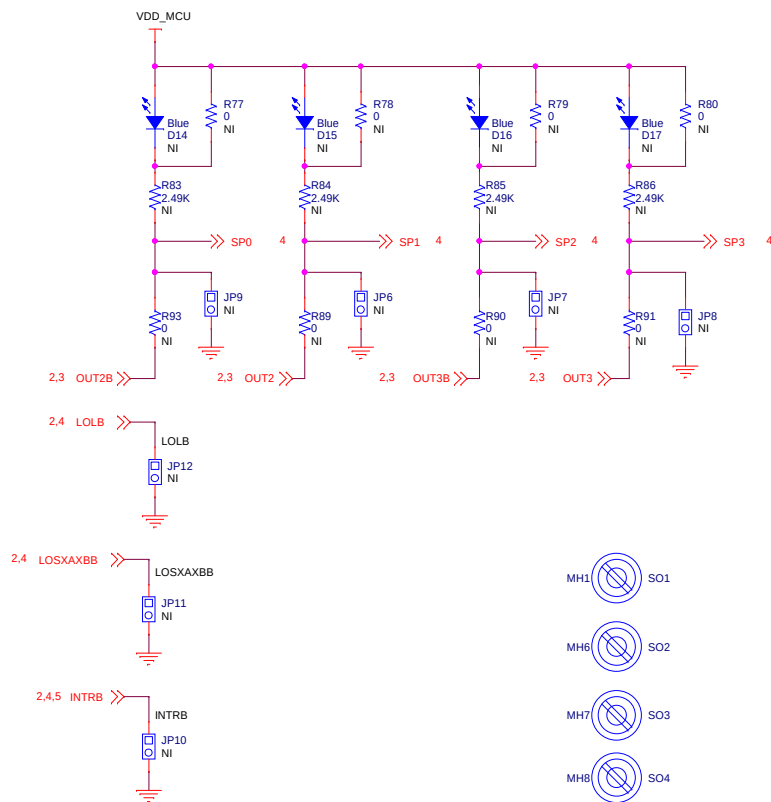


ADC MUX0

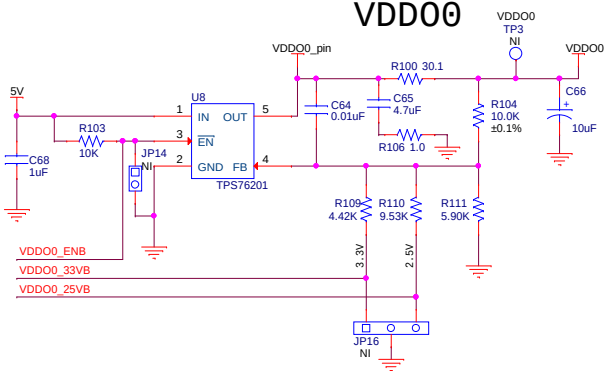
ADC MUX1



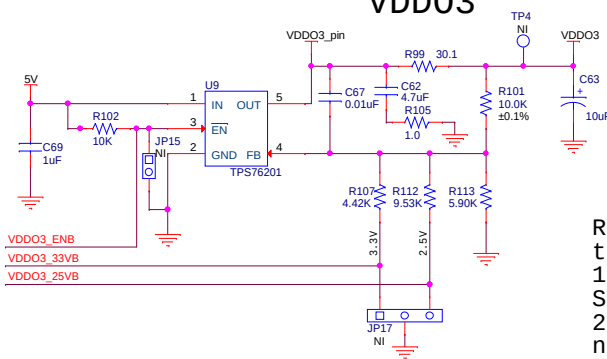
Note: All components on this page are not installed except the standoffs.



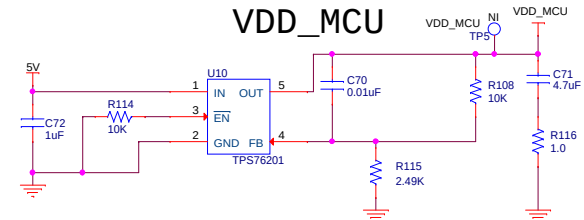
VDD00



VDD03



VDD_MCU

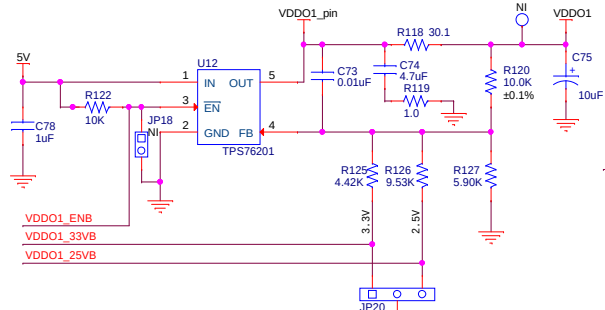


Regulators on this EVB are included to allow the EVB software to

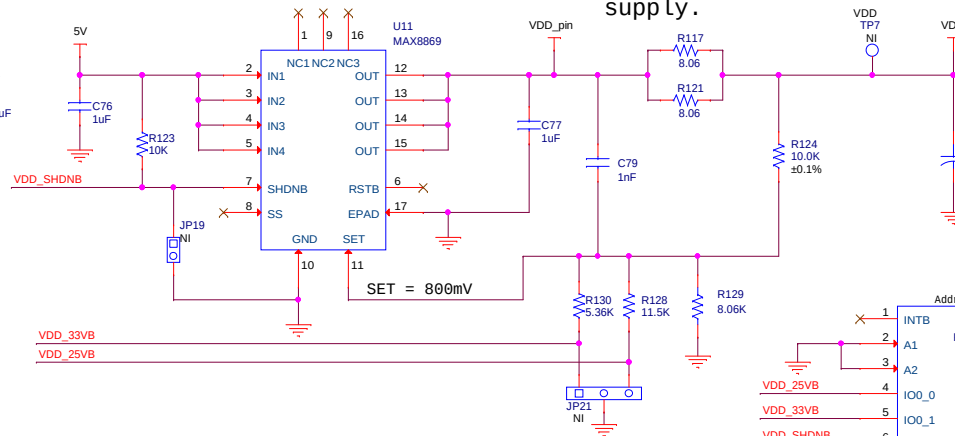
1. Measure the current to each VDD pin of the Si5344
2. Independently control VDD, VDD00/1/2/3, as needed by customer configurations.

Typical applications will not require independent regulation for each Si5344 VDD supply.

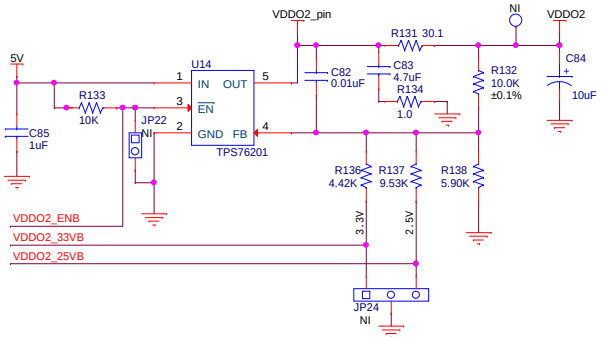
VDD01



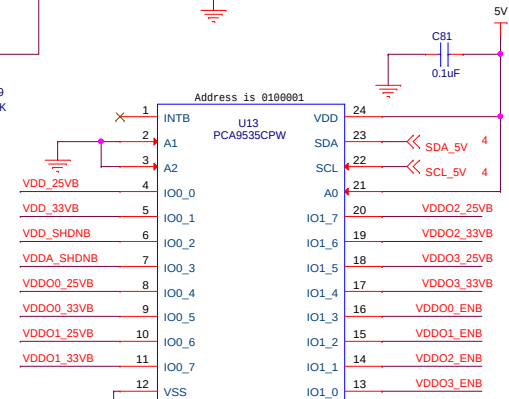
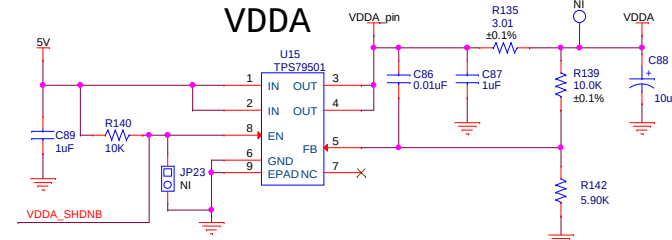
VDD



VDD02



VDDA



REVISION HISTORY

- Rev 1.0 - Nov 13, 2017, Initial Board Release. Copied Si5344-E-EB schematic and added series resistors to the XA and XB traces. Updated DUT, PCB, label, and
- Rev 1.1 - May 29, 2018, Changed U2 from PCA9517D to PCA9517AD,118 (this was needed to make sure the NXP part is ordered instead of the TI version)
Board ID resistors. tturner