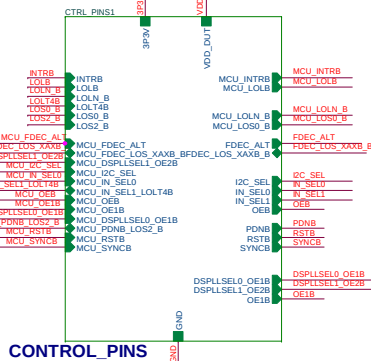
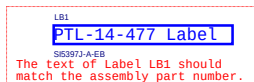
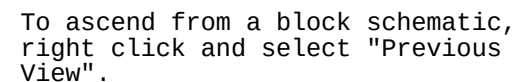




REVISION CONTROL

- Rev 1.0 - Jan 10, 2018, Initial Board Release
Copied from the SI5397A-A-EB and
updated the DUT and label on BOM. tturner
- Rev 1.1 - May 29, 2018, Changed U5 from PCA9517D to
PCA9517AD,118 (this was needed to make sure the NXP
part is ordered instead of the TI version)
- Rev 1.2 - June 19, 2018, Made all components for the reference
input Not Installed, updated the 5397J symbol to change
reference pins to NC.

<Core Design>



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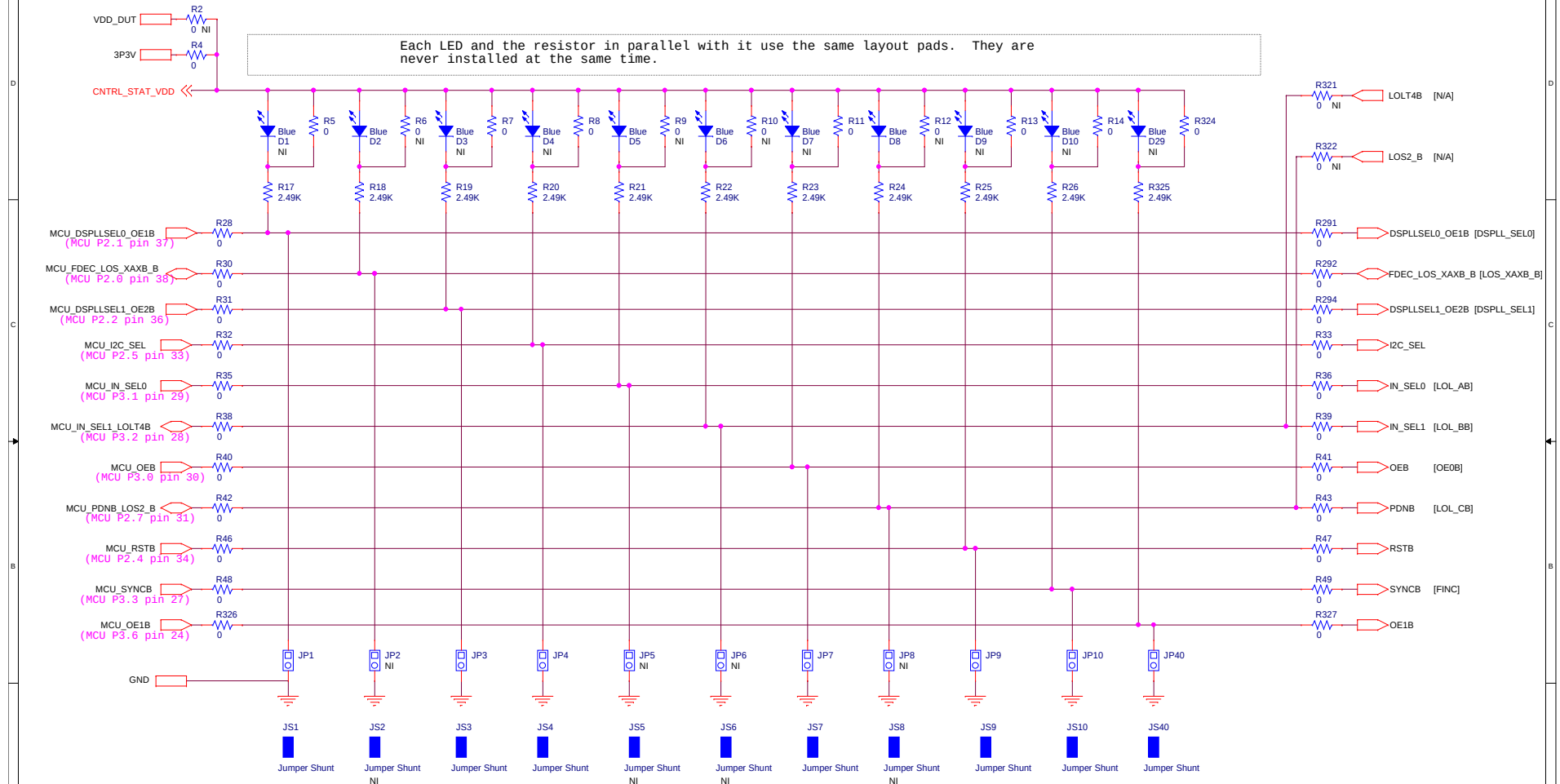
SILICON LABS

Title SI5347-EB_TOP_REVISION

Size	Document Number	Rev
B	SI5397J-A-EB	1.2

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CONTROL PINS CONNECTIONS



<Core Design>



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SILICON LABS

Title CONTROL PINS_P1

Size B

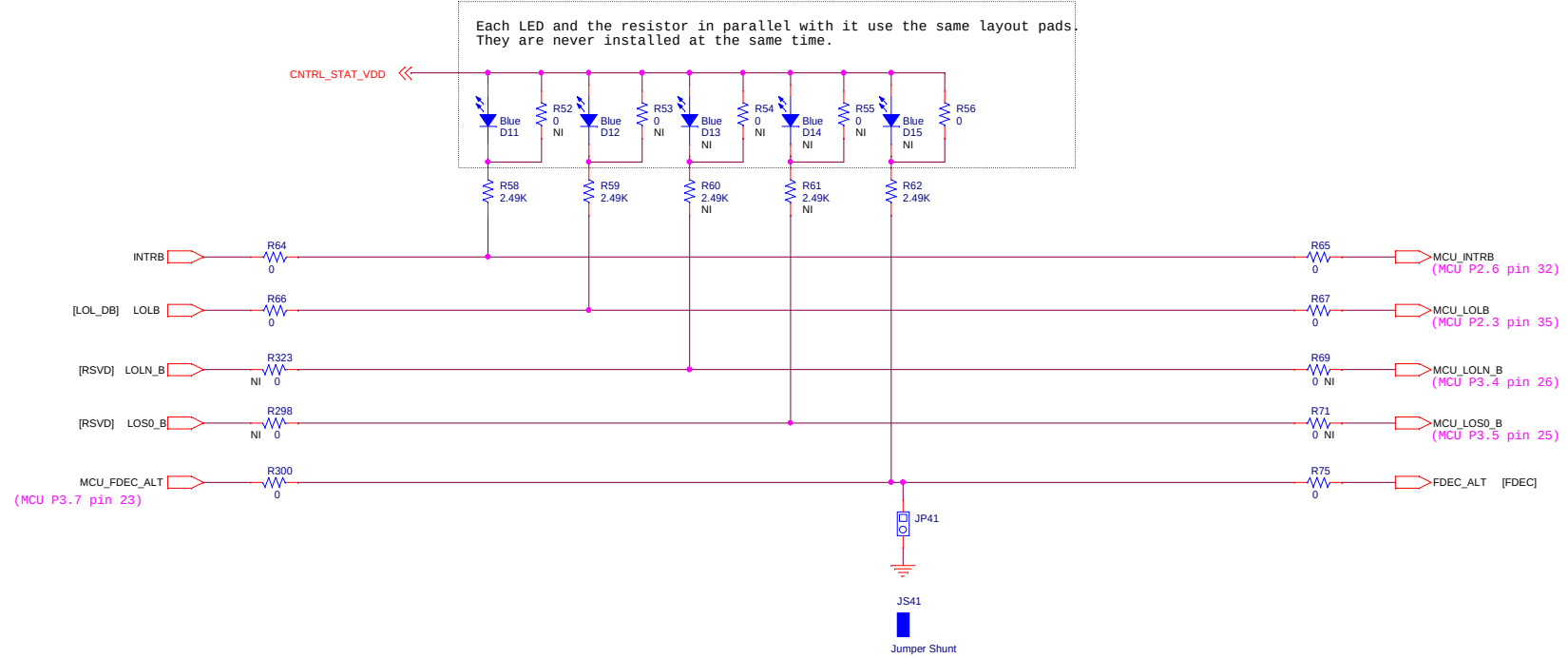
Document Number SI5397J-A-EB

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CONTROL PINS CONNECTIONS



Note:

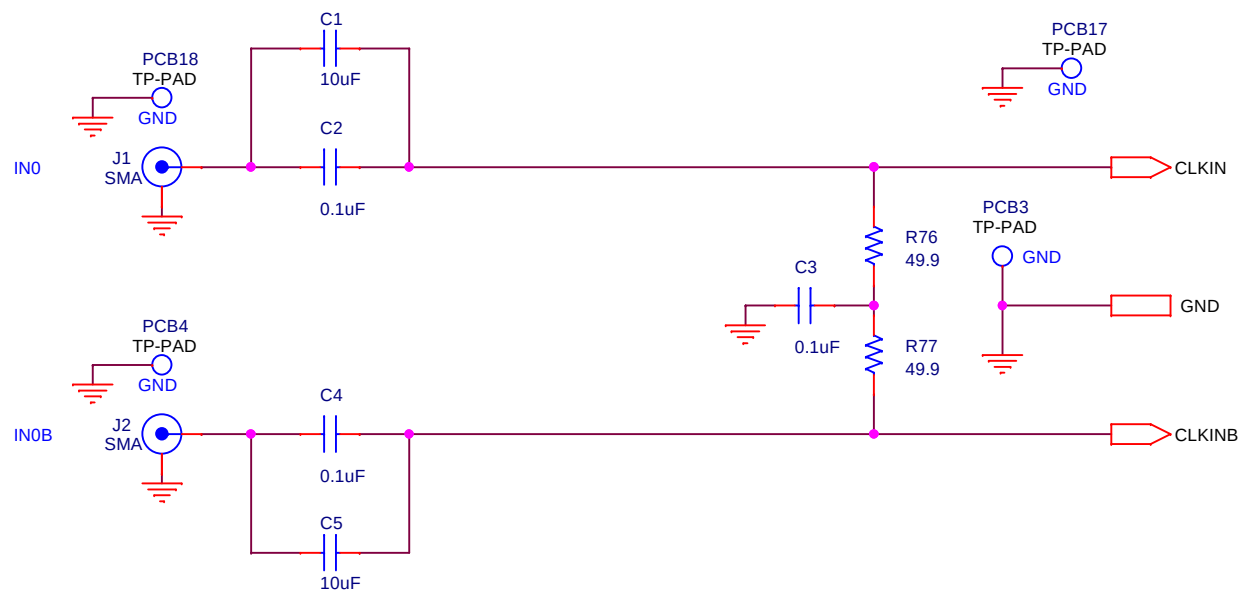
1. If the net name differs from the DUT pin name, the corresponding DUT pin name is enclosed in brackets. [DUT Pin Name]
2. "N/A" in brackets indicates that the function is not used for the DUT. [N/A]

<Core Design>



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Title			CONTROL_PINS_P2
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<Core Design>



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Title

DIFF_INPUT_CLK

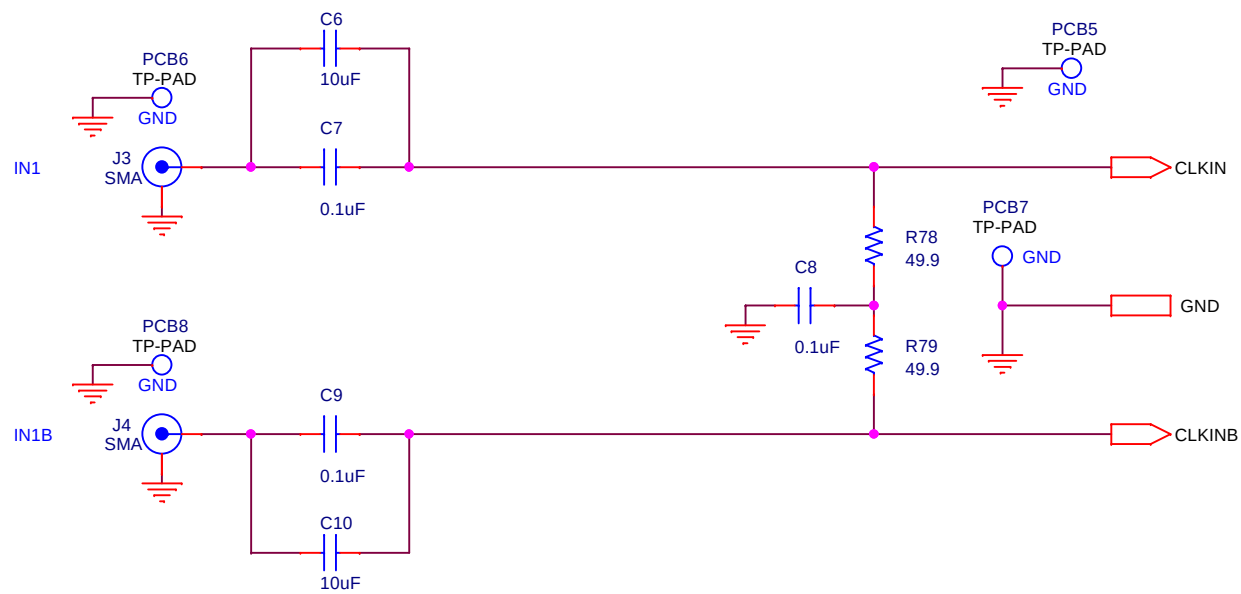
Size
A

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<Core Design>



400 W Cesar Chavez
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Title

DIFF_INPUT_CLK

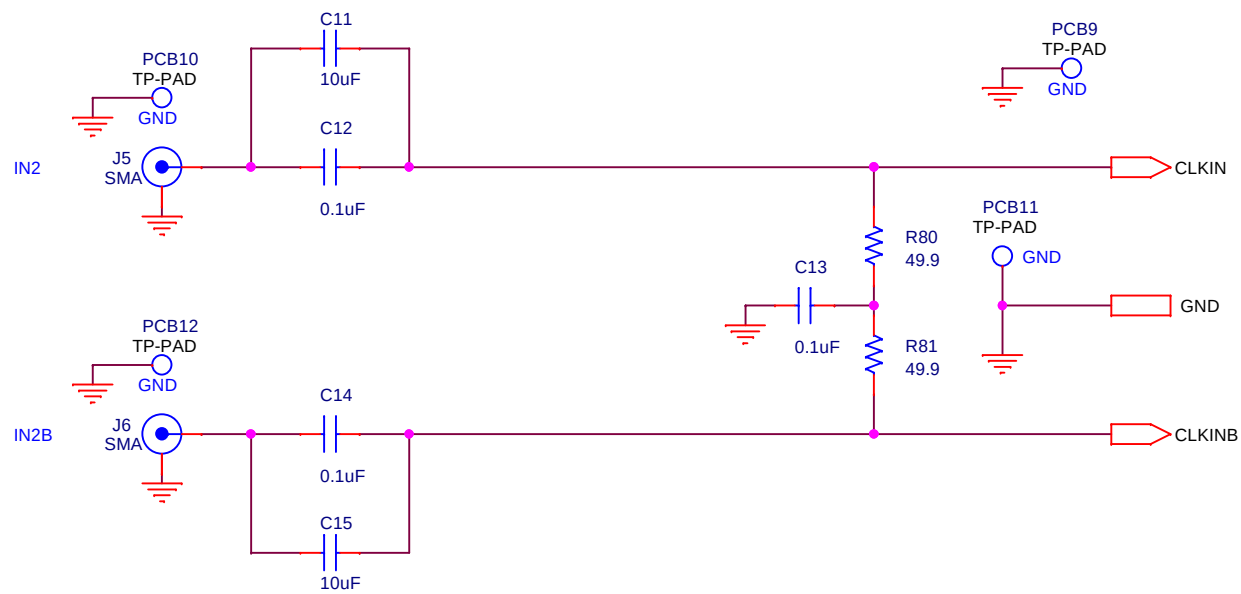
Size
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<Core Design>



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Austin, TX 78701

Title

DIFF_INPUT_CLK

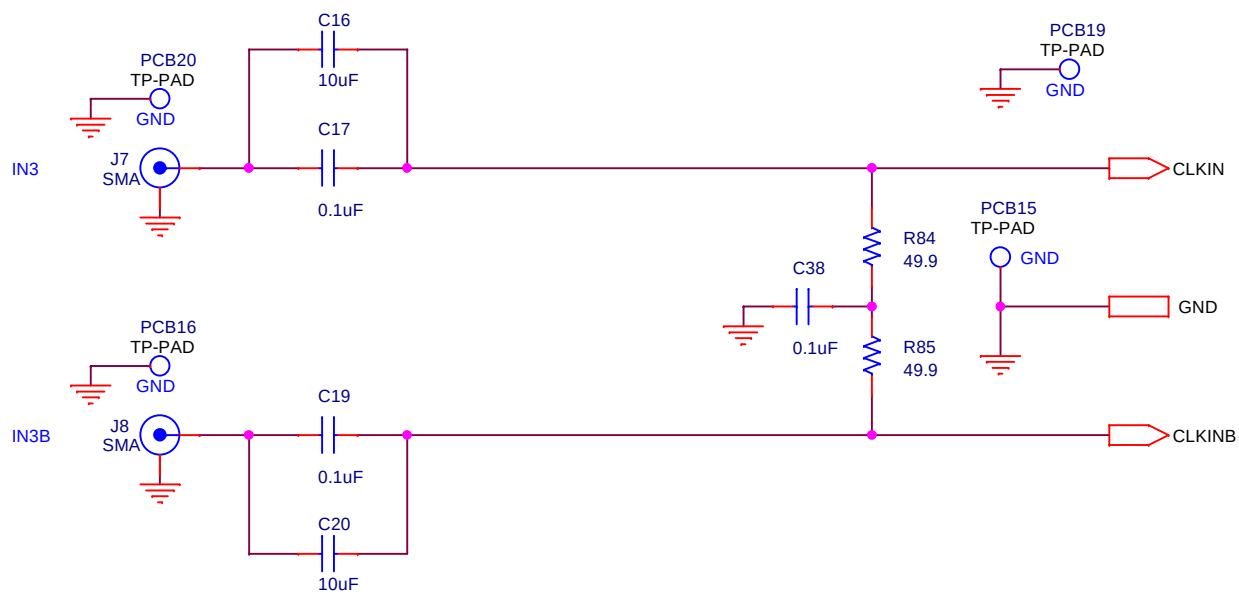
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Document Number
SI5397J-A-EB

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1.2

Date: Tuesday, June 19, 2018

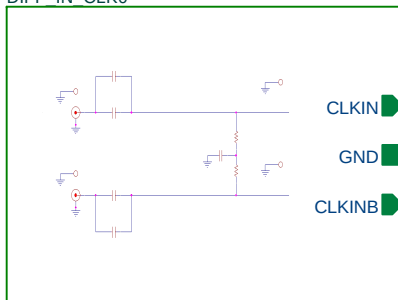
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<Core Design>

 400 W Cesar Chavez Austin, TX 78701		
Title DIFF_INPUT_CLK		
Size A	Document Number SI5397J-A-EB	Rev 1.2
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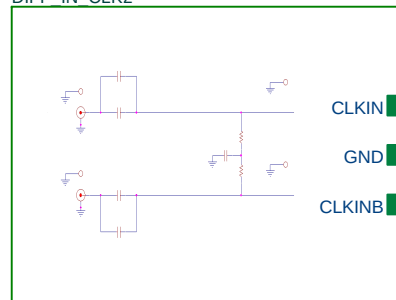
DIFF IN CLK0



CLKIN → CLKIN0
 GND → GND
 CLKINB → CLKIN0B

DIFF_INPUT_CLK

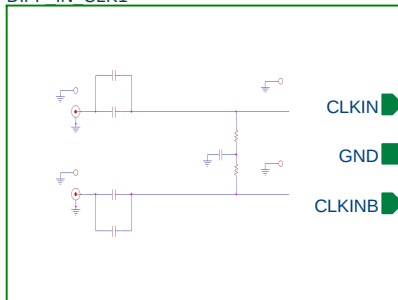
DIFF IN CLK2



CLKIN → CLKIN2
 GND → GND
 CLKINB → CLKIN2B

DIFF_INPUT_CLK

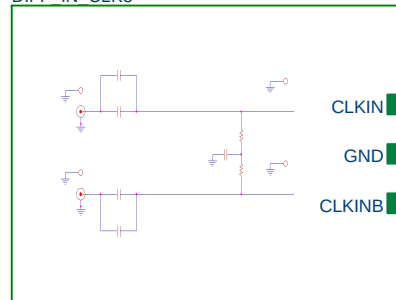
DIFF IN CLK1



CLKIN → CLKIN1
 GND → GND
 CLKINB → CLKIN1B

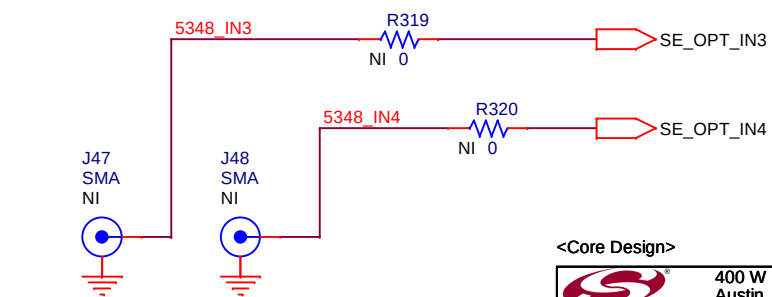
DIFF_INPUT_CLK

DIFF IN CLK3



CLKIN → CLKIN3
 GND → GND
 CLKINB → CLKIN3B

DIFF_INPUT_CLK



Single-ended clocks only.

<Core Design>



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Title

DIFF_INPUT_CLOCKS_4

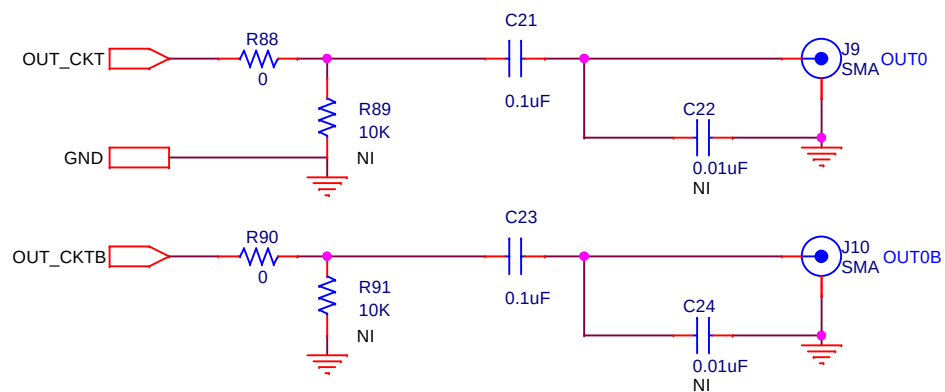
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Document Number
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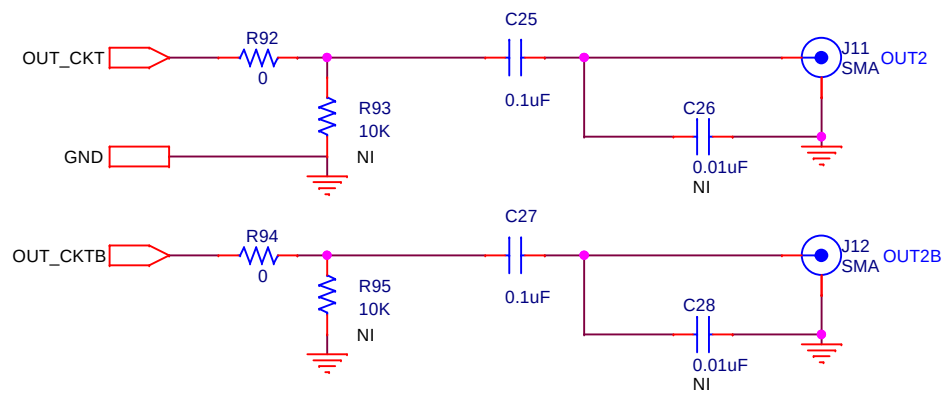
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Title

DIFF_OUTPUT_CLK

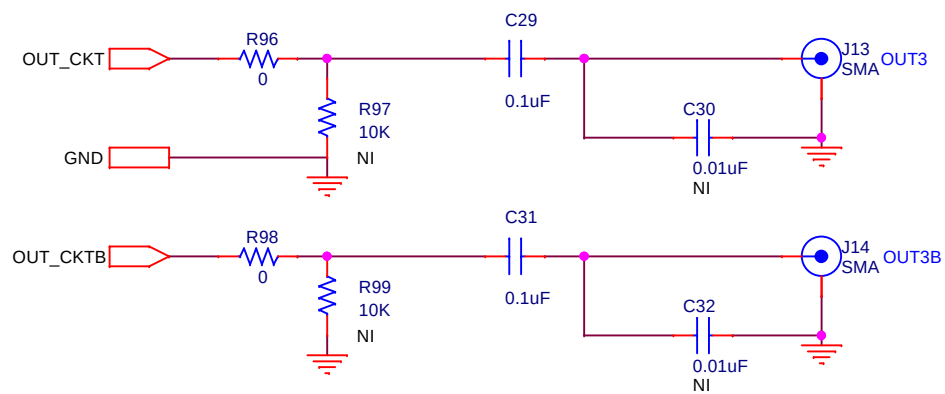
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Document Number
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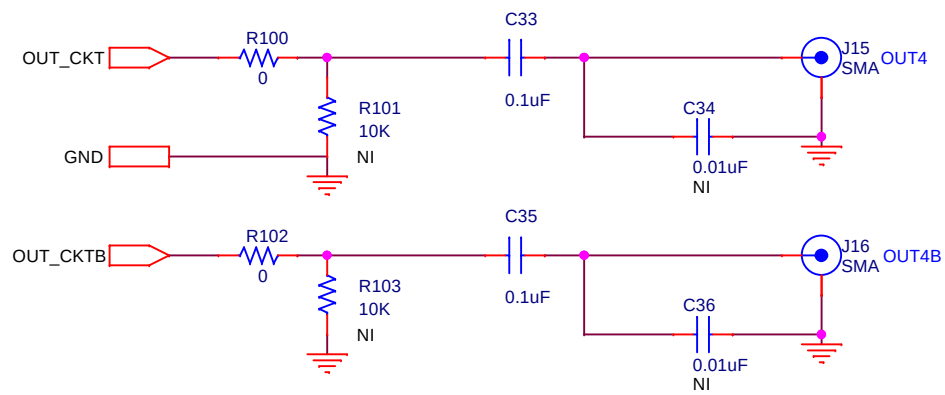
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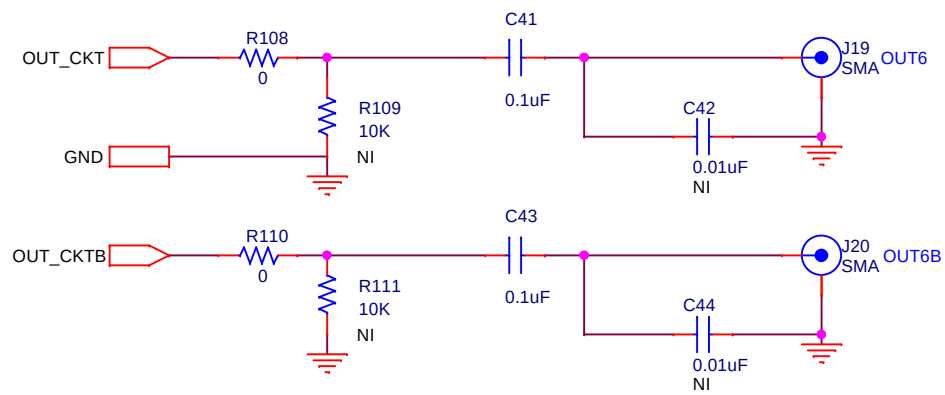
400 W Cesar Chavez
Austin, TX 78701

Title
DIFF_OUTPUT_CLK

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<Core Design>



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Title

DIFF_OUTPUT_CLK

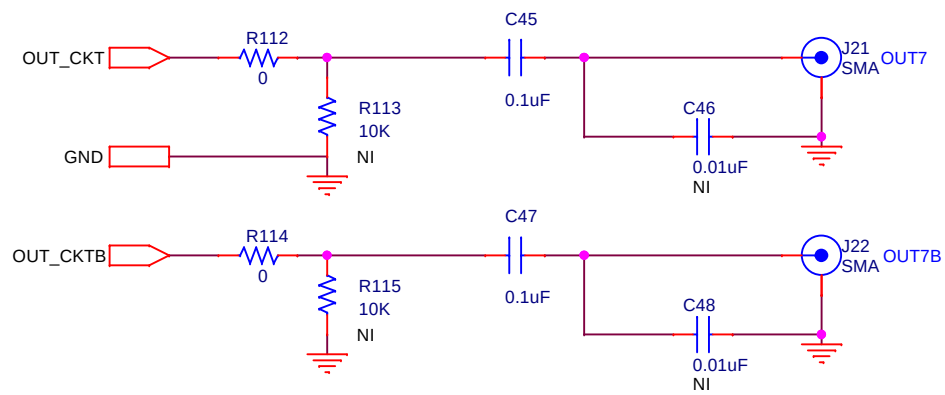
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400 W Cesar Chavez
Austin, TX 78701

Title

DIFF_OUTPUT_CLK

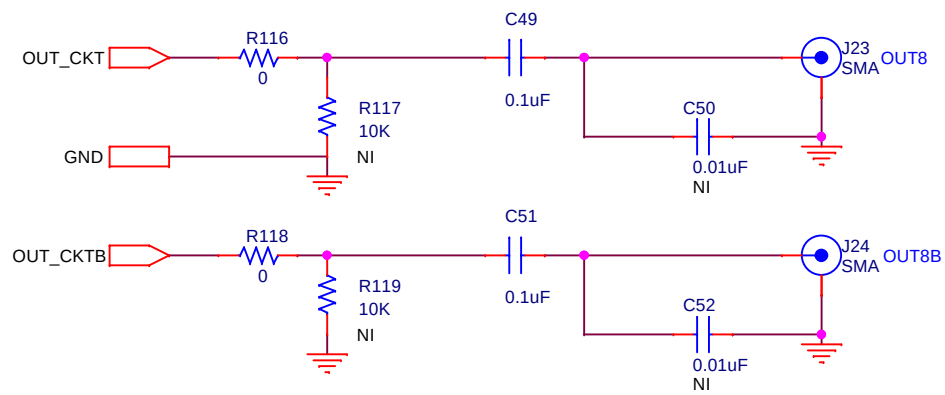
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<Core Design>



400 W Cesar Chavez
Austin, TX 78701

Title

DIFF_OUTPUT_CLK

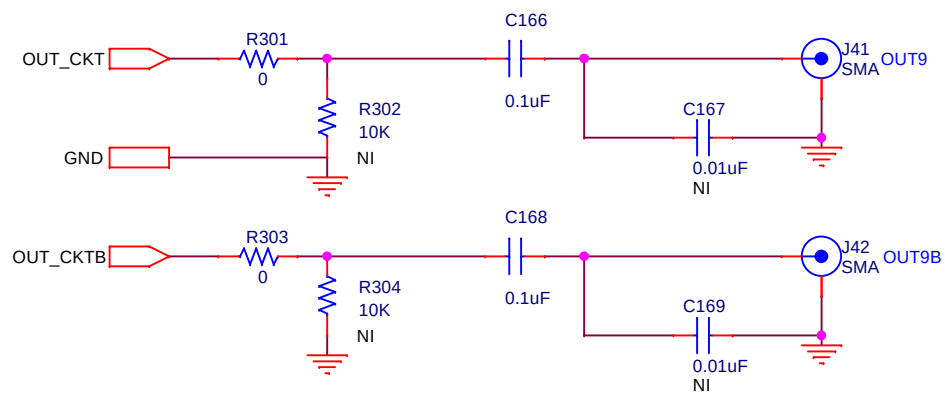
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400 W Cesar Chavez
Austin, TX 78701

Title

DIFF_OUTPUT_CLK

Size
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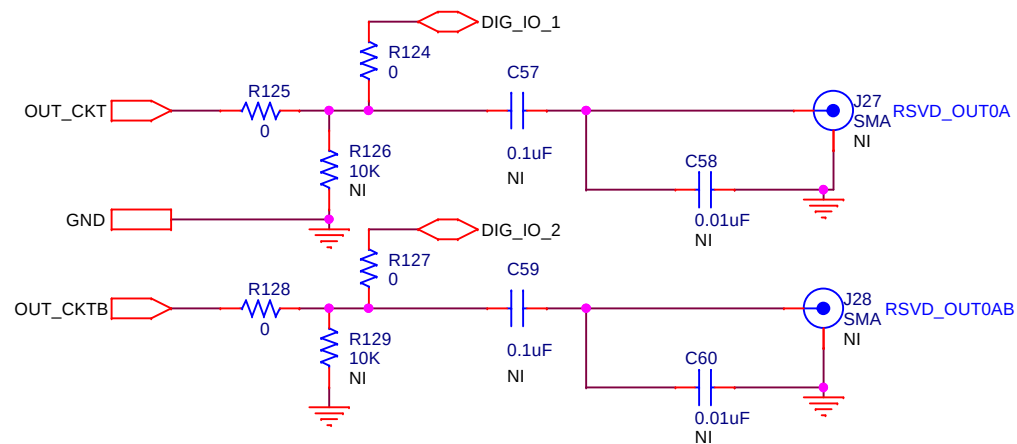
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DIFF_OUTPUT_CLK_DIG_IO



<Core Design>



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Title

DIFF_OUTPUT_CLK_DIG_IO

Size
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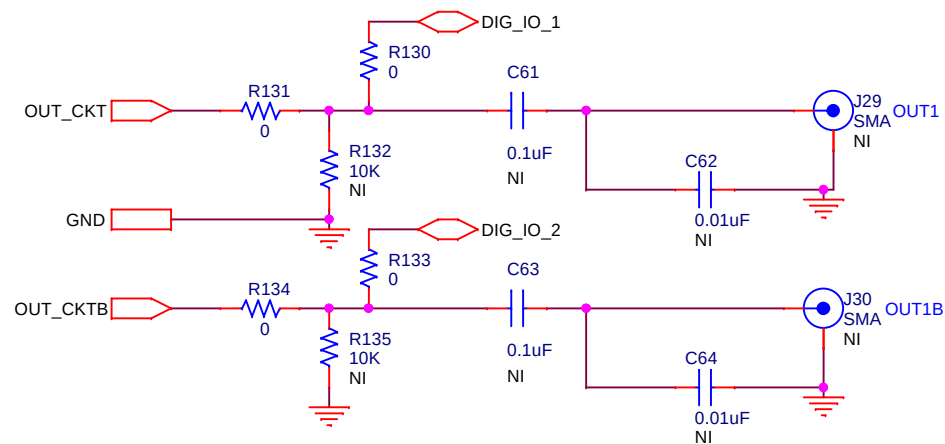
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DIFF_OUTPUT_CLK_DIG_IO



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Title

DIFF_OUTPUT_CLK_DIG_IO

Size
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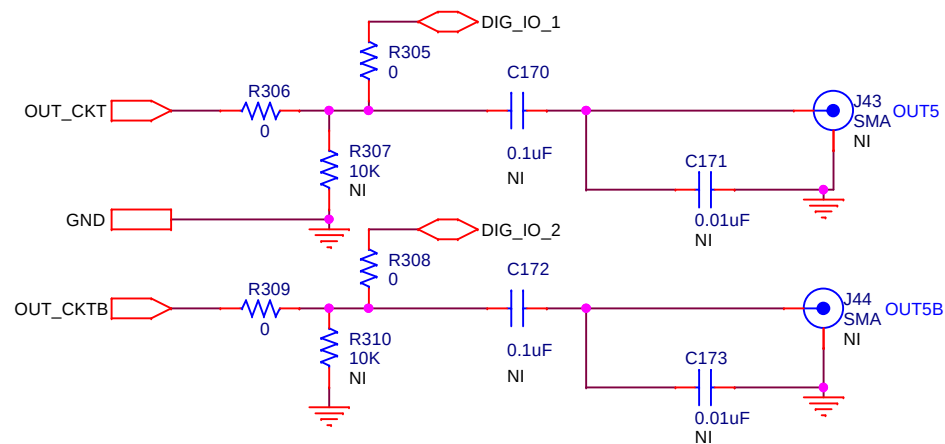
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DIFF_OUTPUT_CLK_DIG_IO



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Title
DIFF_OUTPUT_CLK_DIG_IO

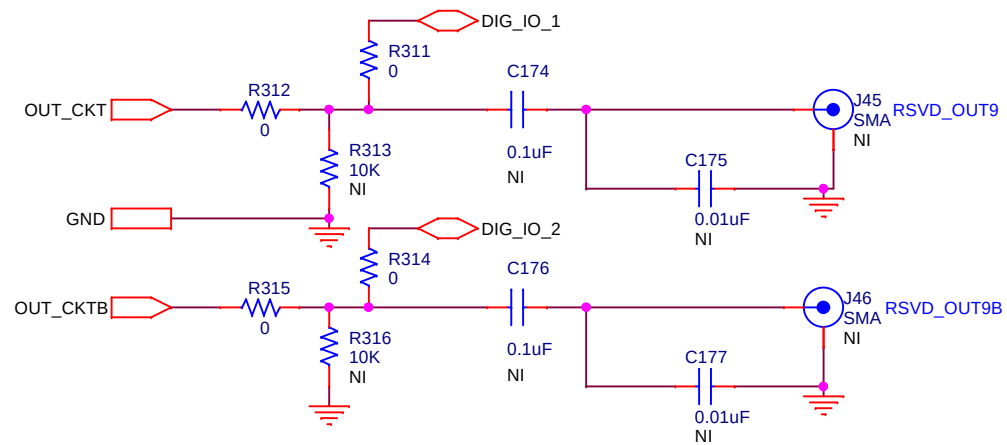
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DIFF_OUTPUT_CLK_DIG_IO



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Austin, TX 78701

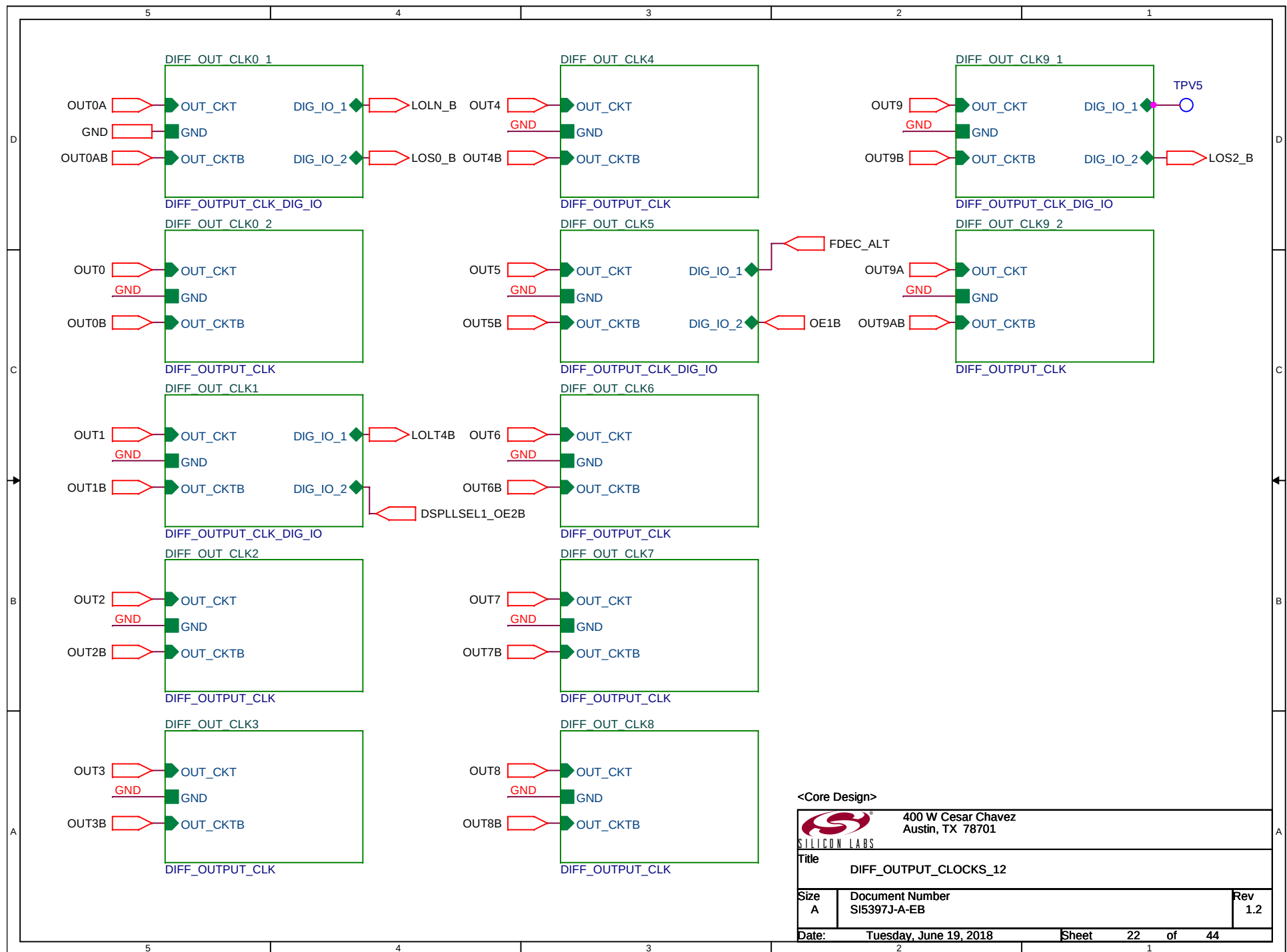
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Size
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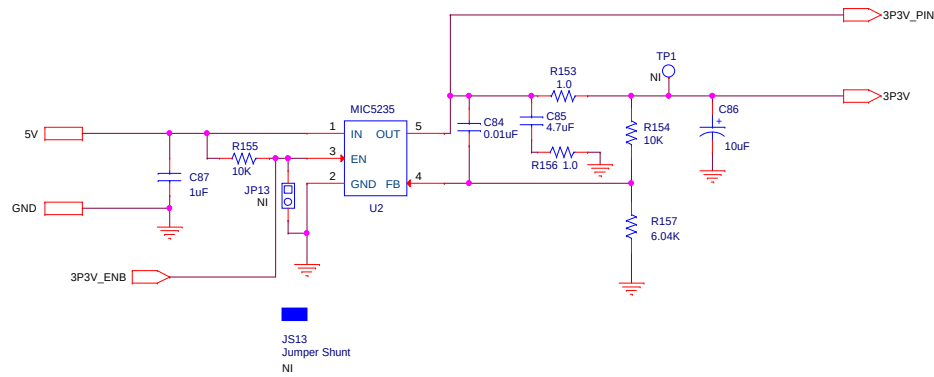
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 400 W Cesar Chavez Austin, TX 78701		A
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Size A	Document Number SI5397J-A-EB	Rev 1.2
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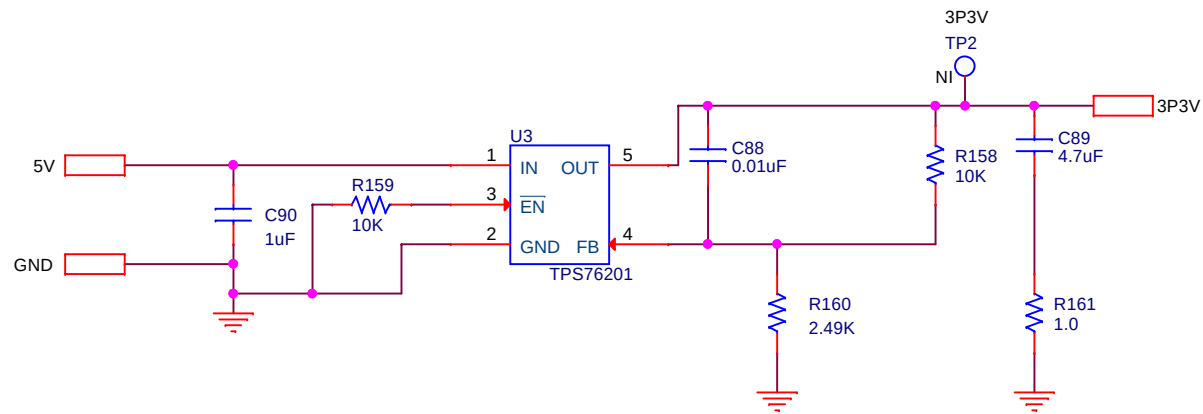
Fixed 3.3V 150 mA Voltage Regulator



3P3V_DUT (In Schematic)
VDDA DUT (Board Silkscreen)

 400 W Cesar Chavez Austin, TX 78701			
Title VDD_REG_TPS76201			
Size B	Document Number SI5397J-A-EB	<Core Design>	Rev 1.2
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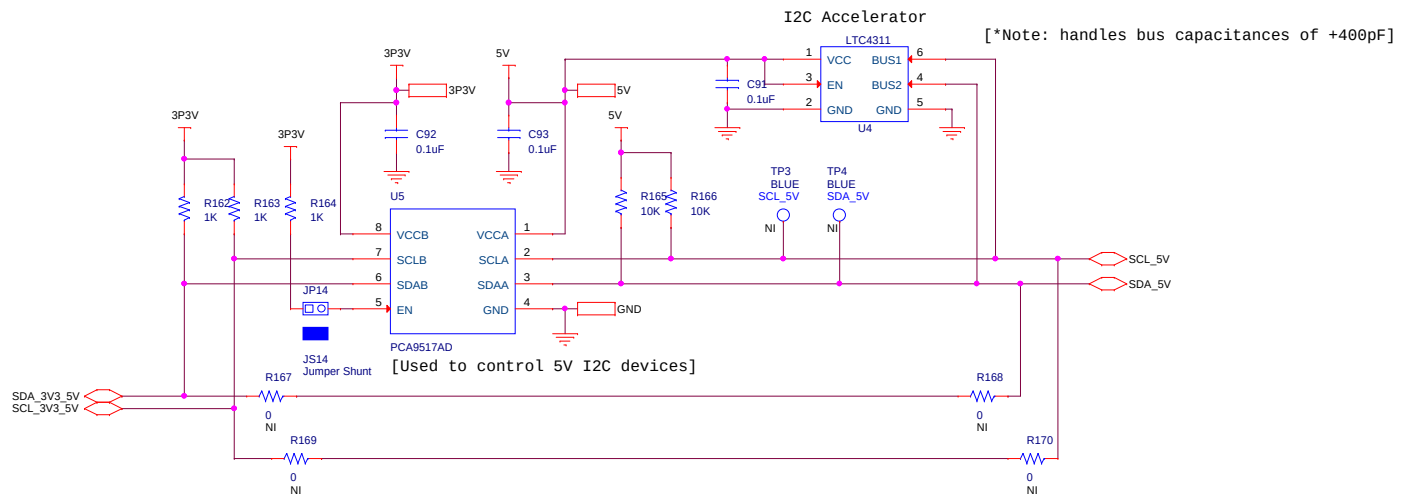
Fixed 3.3V 100 mA Voltage Regulator (Enabled, no current measurement supported.)



3P3V (In Schematic)
3P3V (Board Silkscreen)

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Title			
FIXED_3V3_REG_TPS76201_EN_NO_IM			
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I2C LEVEL TRANSLATORS



<Core Design>



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SILICON LABS

Title I2C_TRANSLATORS

Size B

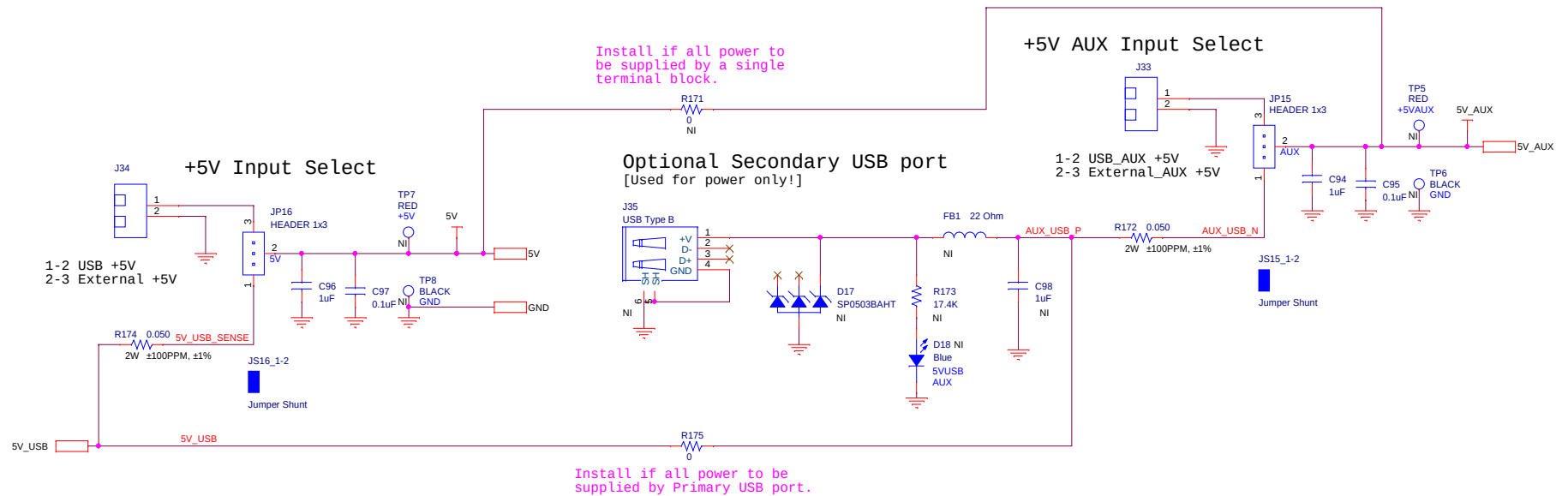
Document Number SI5397J-A-EB

Rev 1.2

Date: Tuesday, June 19, 2018

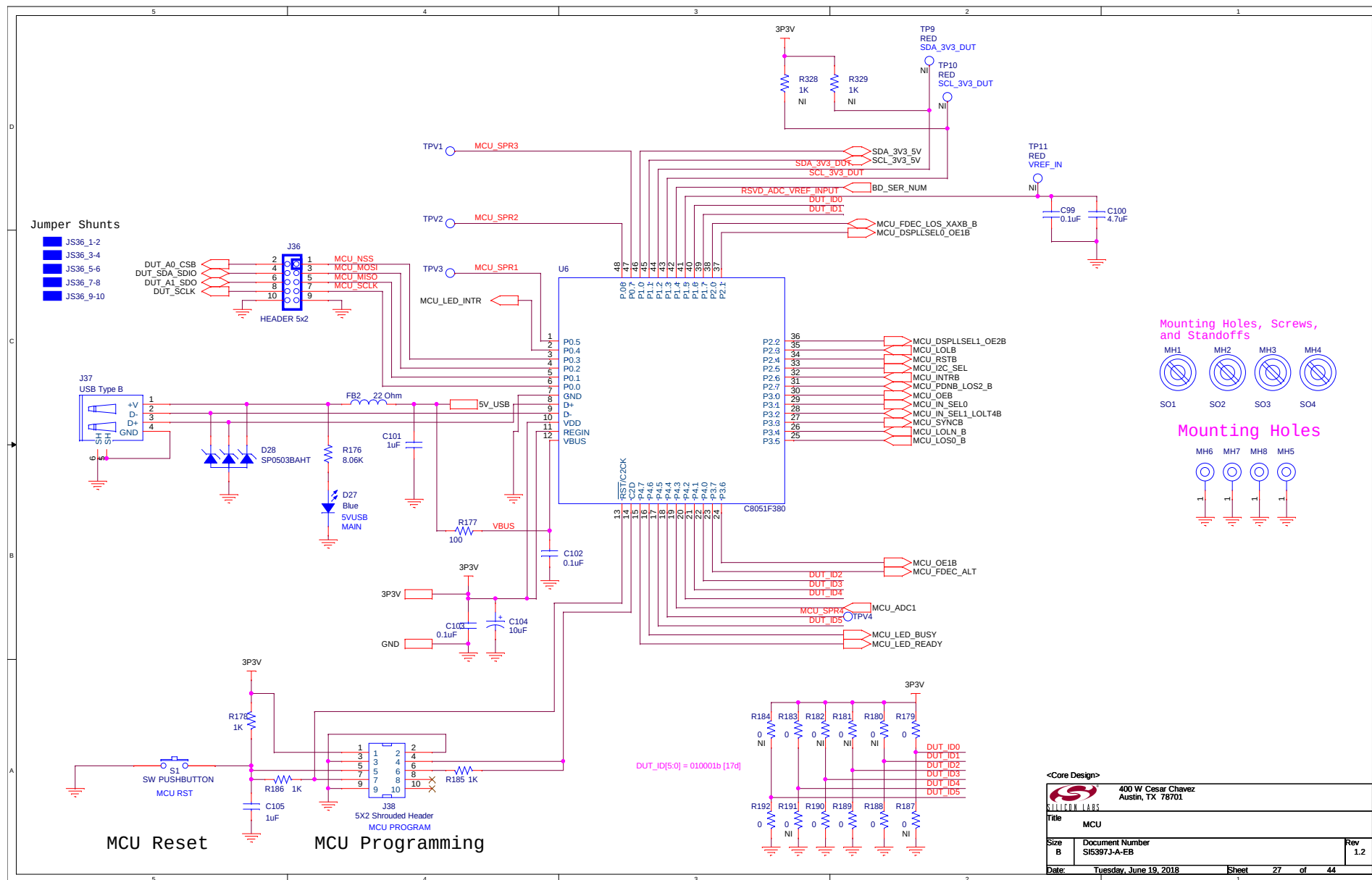
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MAIN POWER INPUT SELECTS EXTERNAL OR USB

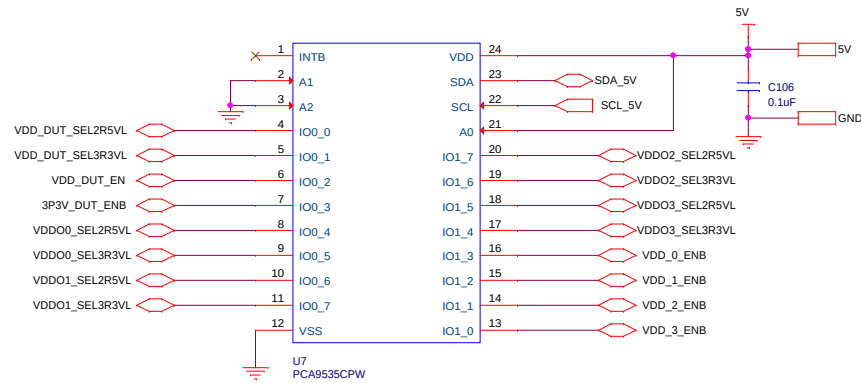


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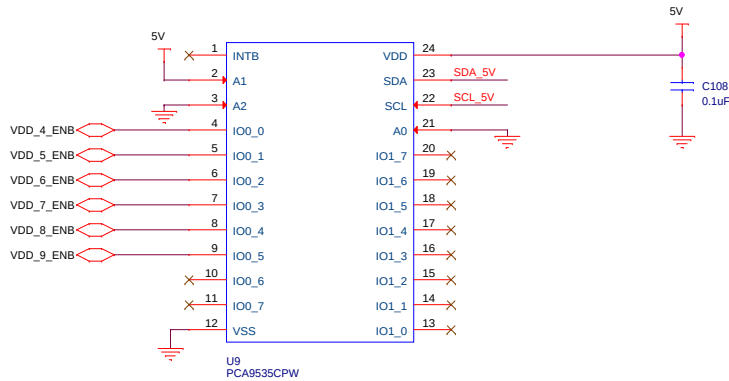
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Title		
MAIN_POWER_CONNECTIONS		
Size	Document Number	Rev
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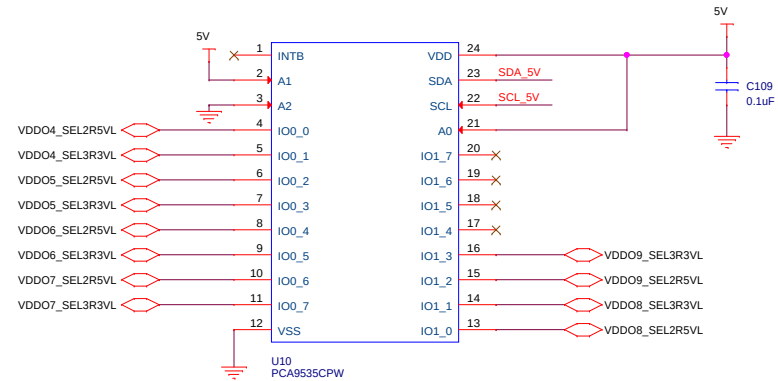
PERIPHERALS



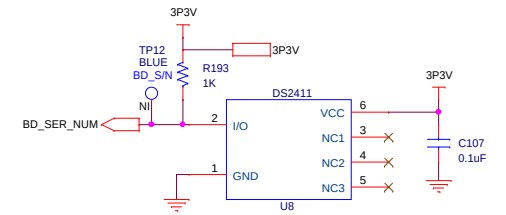
I2C VDD ENABLES/SELECTS FOR REGULATORS
I2C address: 0100001



I2C VDD ENABLES/SELECTS FOR REGULATORS
I2C address: 0100010



I2C VDD ENABLES/SELECTS FOR REGULATORS
I2C address: 0100011



Board Serial Number

<Core Design>



400 W Cesar Chavez
Austin, TX 78701

Title
PERIPHERALS

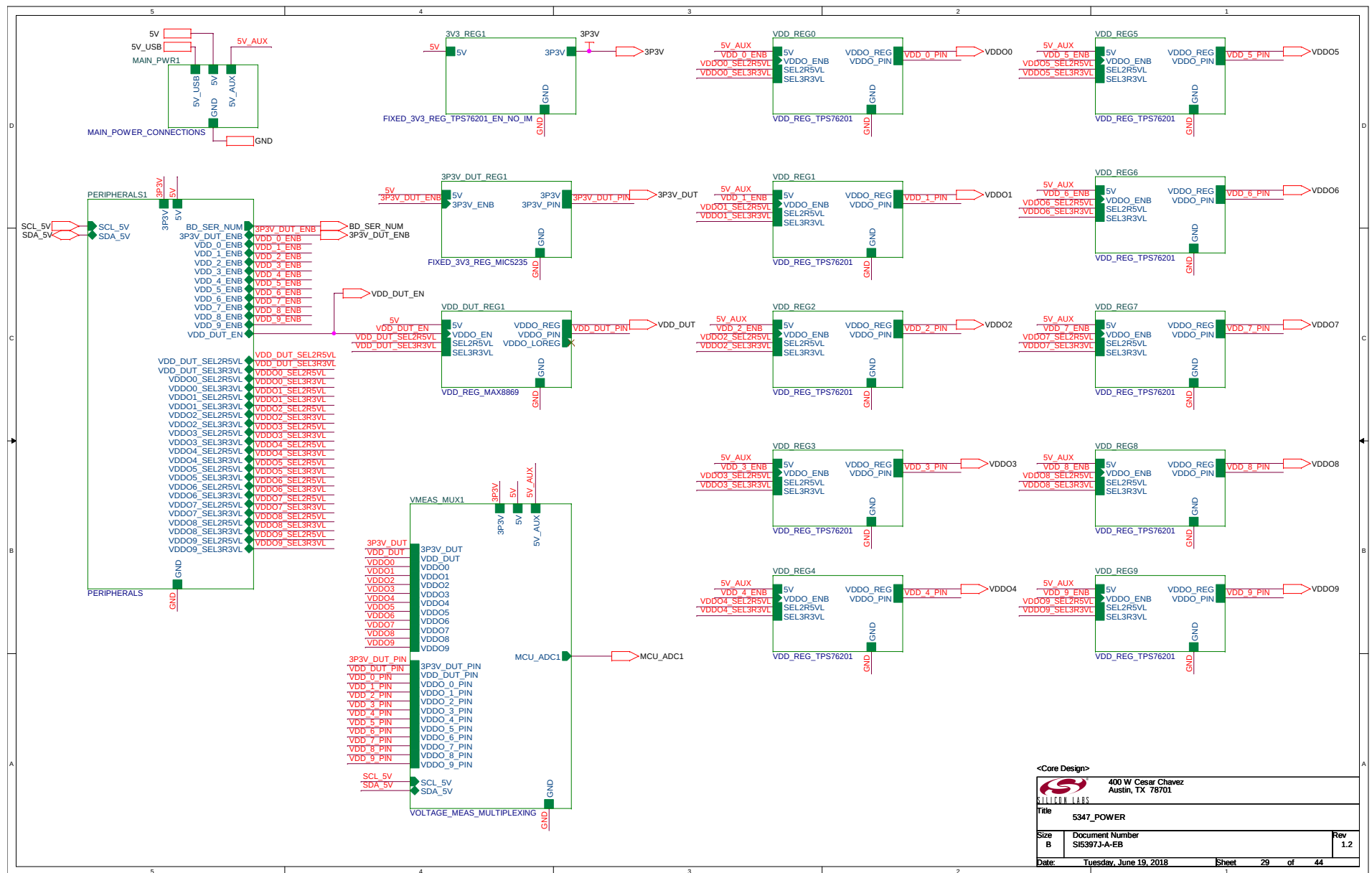
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Document Number
SI5397J-A-EB

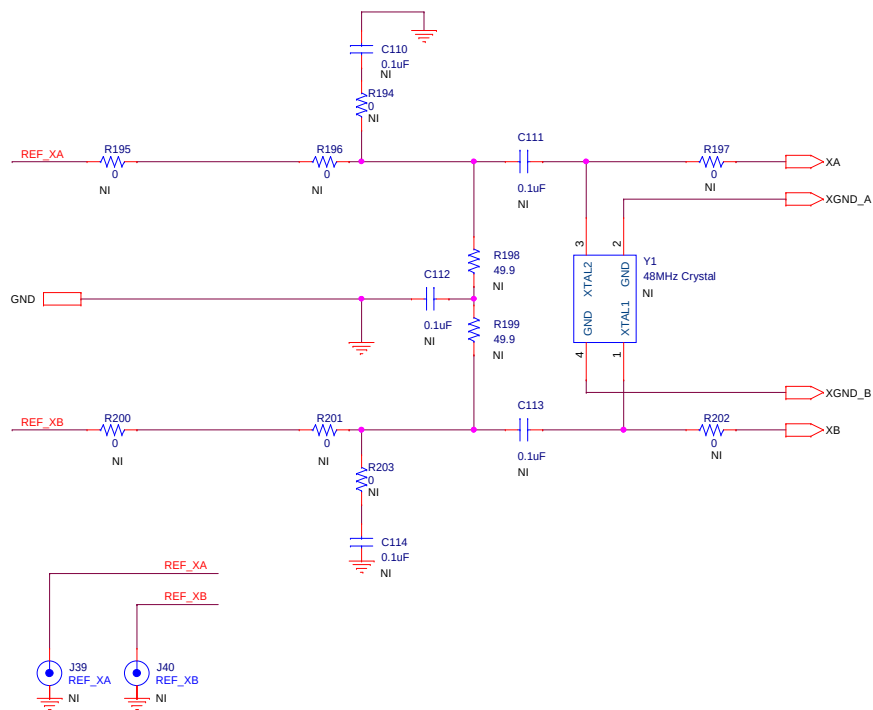
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REFERENCE INPUT CONNECTIONS



<Core Design>



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Title

REFERENCE

Size
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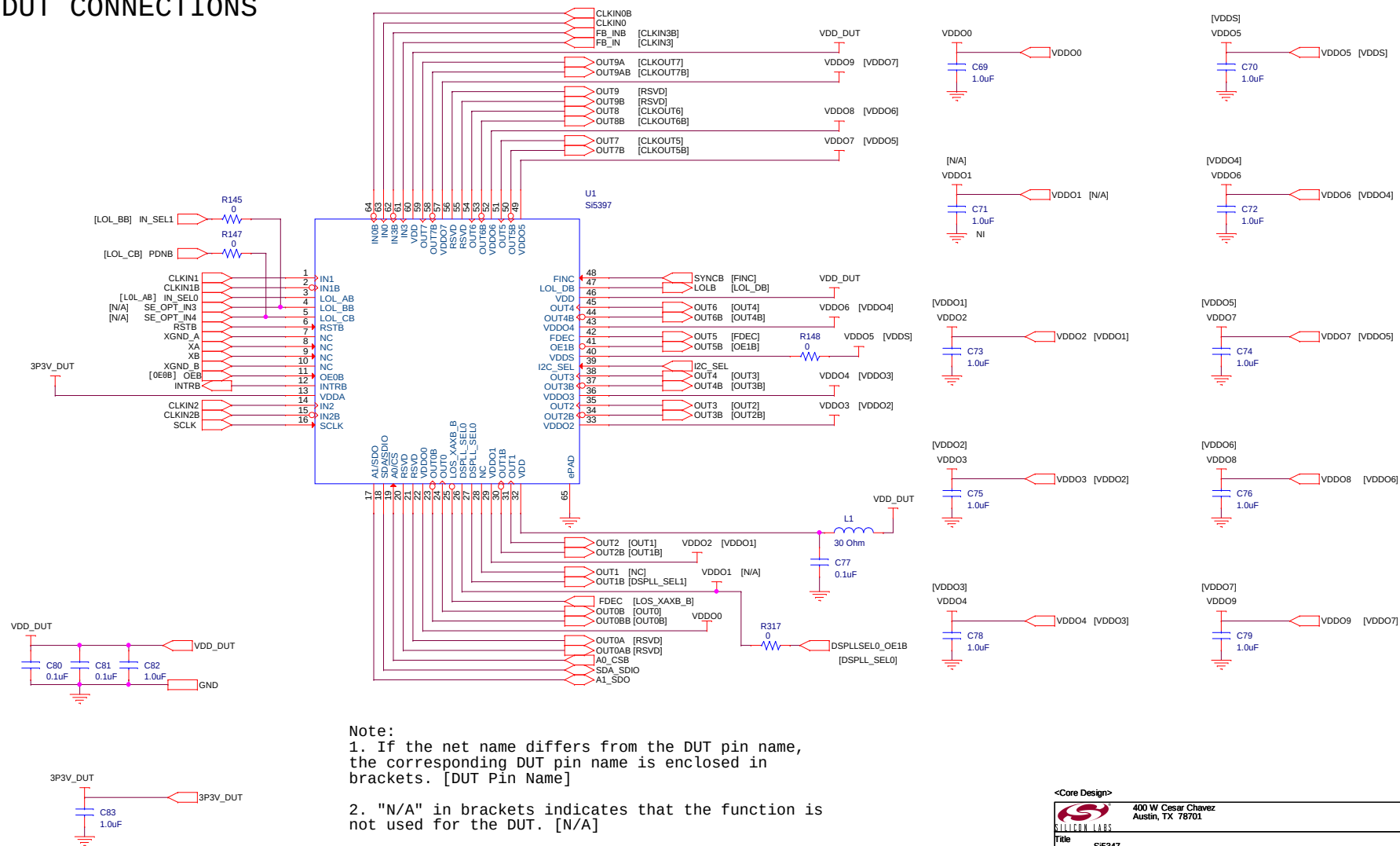
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Rev
1.2

Date: Tuesday, June 19, 2018

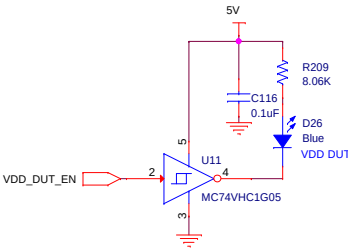
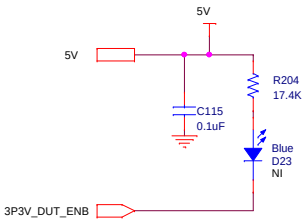
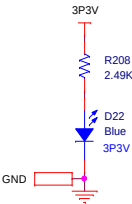
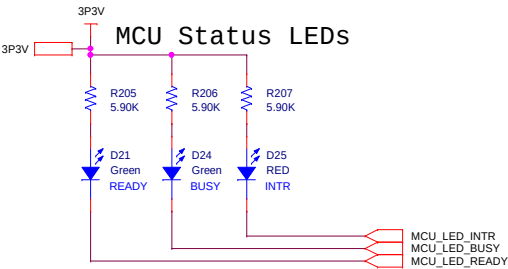
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DUT CONNECTIONS

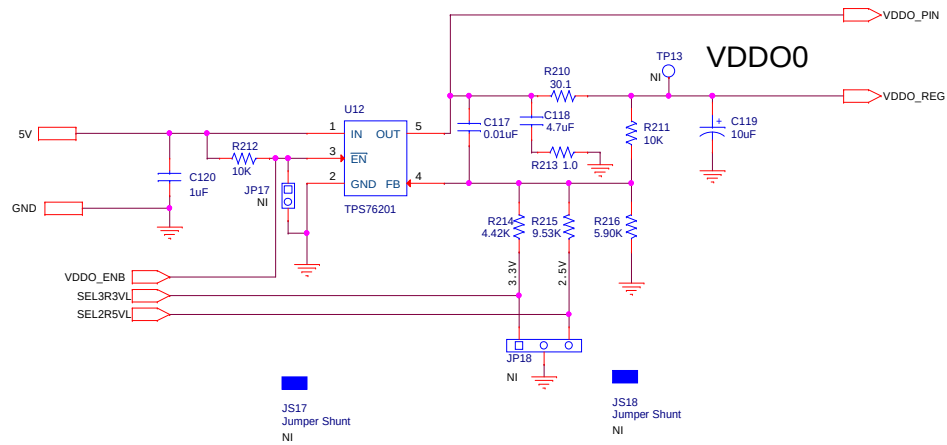


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Title		
SI5397		
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LEDS - SUPPLIES & STATUS

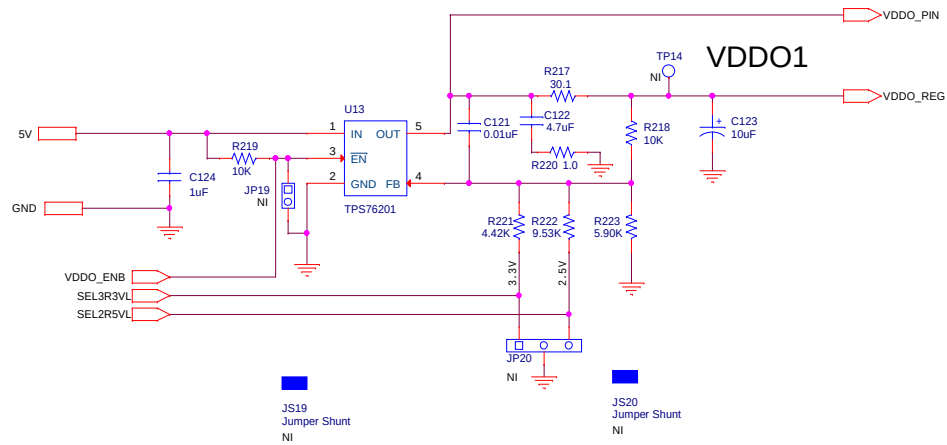


100 mA Adjustable Voltage Regulator



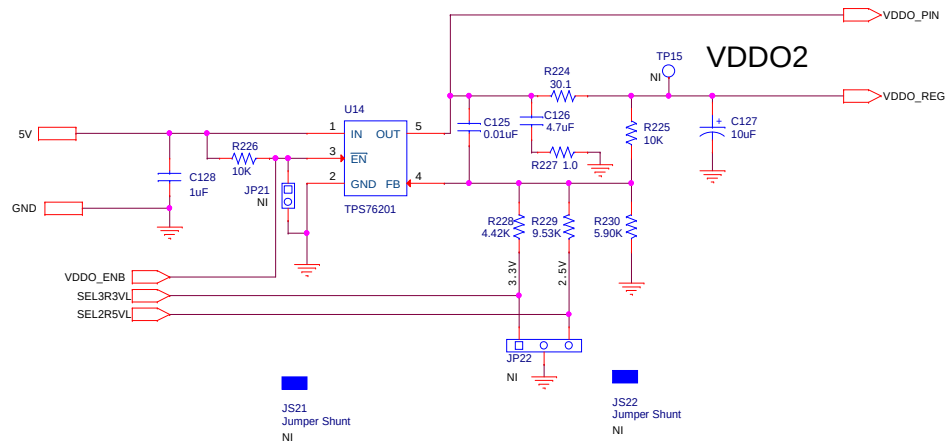
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Title VDD_REG_TPS76201			
Size B	Document Number SI5397J-A-EB	<Core Design>	Rev 1.2
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100 mA Adjustable Voltage Regulator



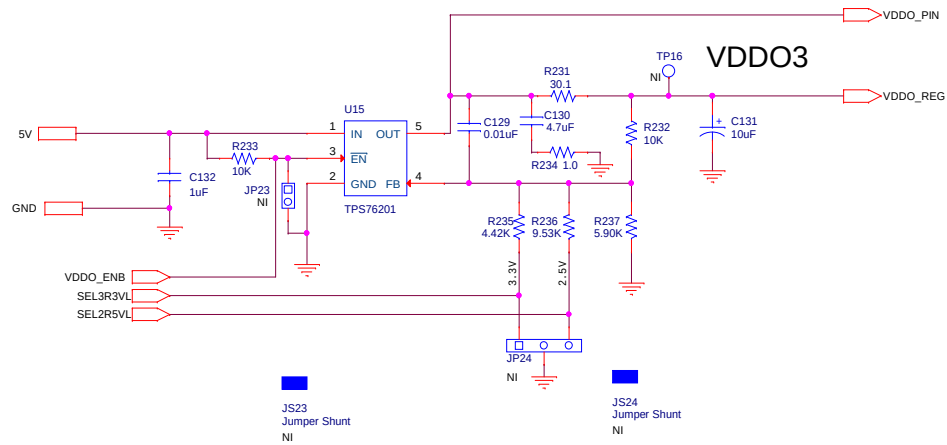
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Size B	Document Number SI5397J-A-EB	<Core Design>	Rev 1.2
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100 mA Adjustable Voltage Regulator



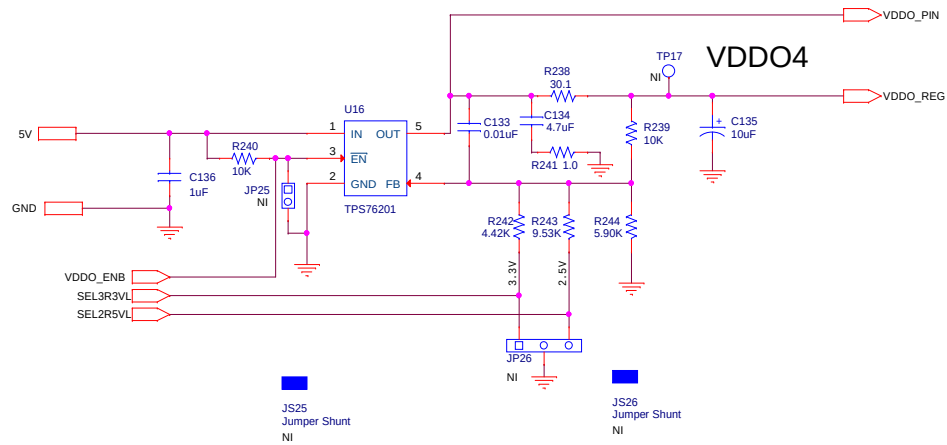
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Size B	Document Number SI5397J-A-EB	<Core Design>	Rev 1.2
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100 mA Adjustable Voltage Regulator



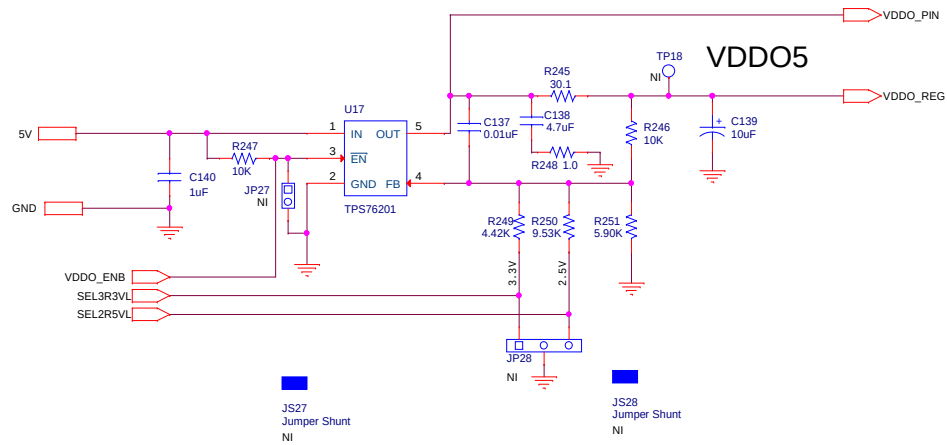
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Size B	Document Number SI5397J-A-EB	<Core Design>	Rev 1.2
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100 mA Adjustable Voltage Regulator



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Title VDD_REG_TPS76201			
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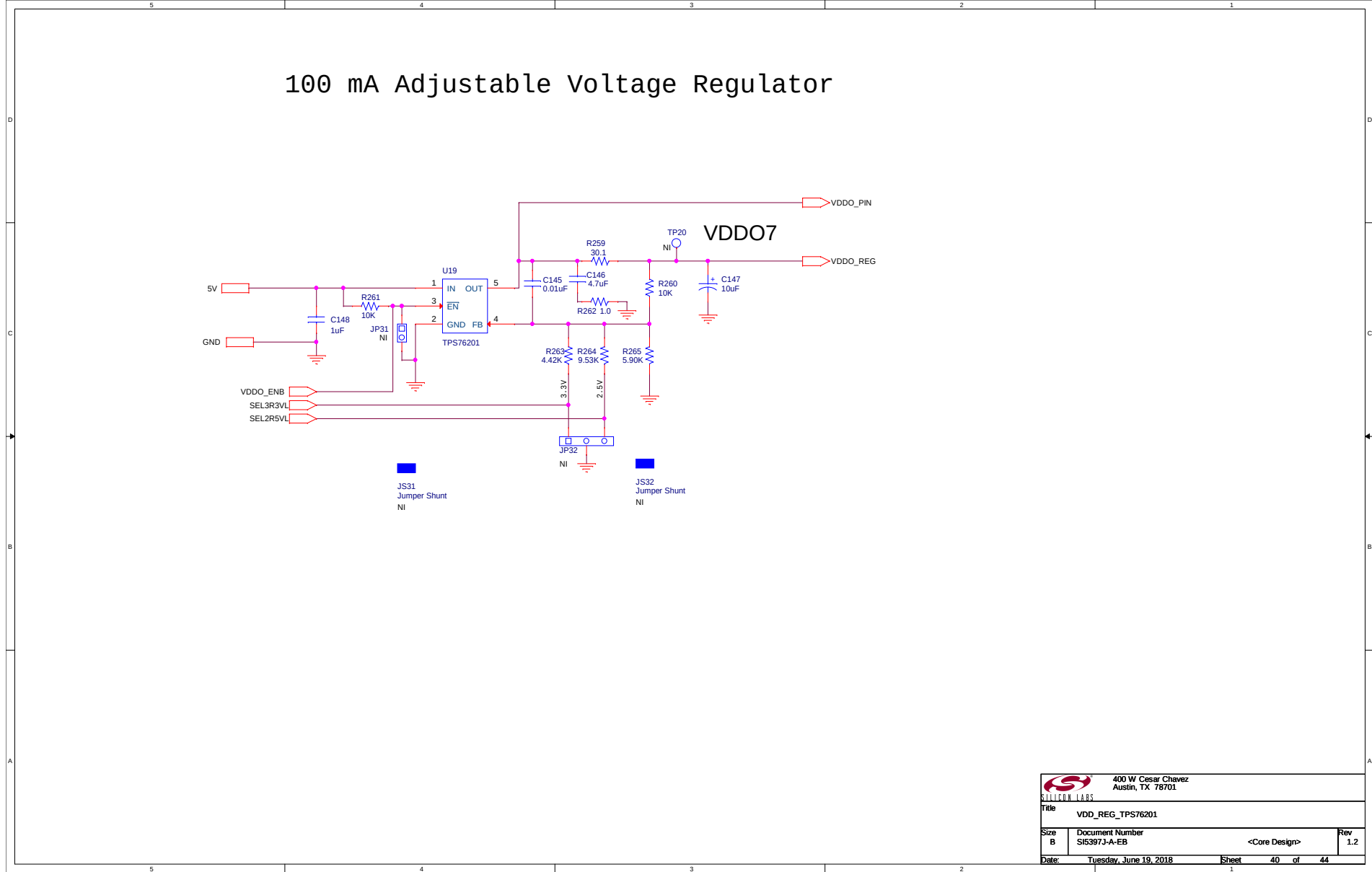
100 mA Adjustable Voltage Regulator



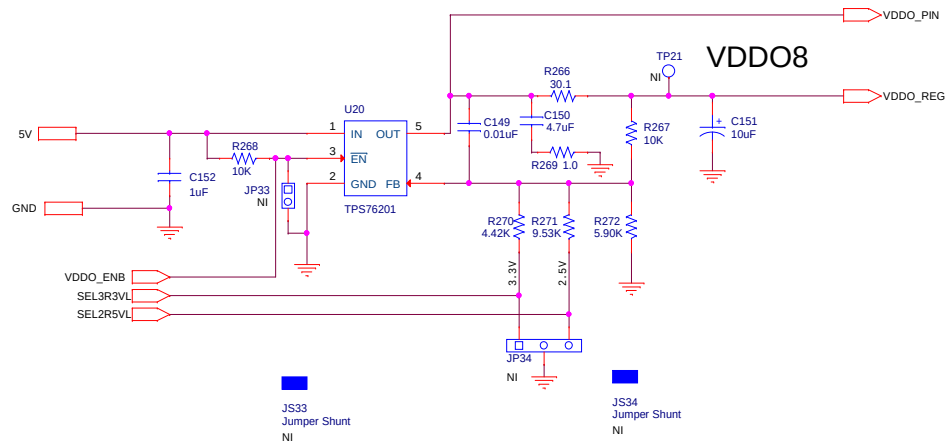
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Title			
VDD_REG_TPS76201			
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JS29
Jumper Shunt
NI

JS30
Jumper Shunt
NI

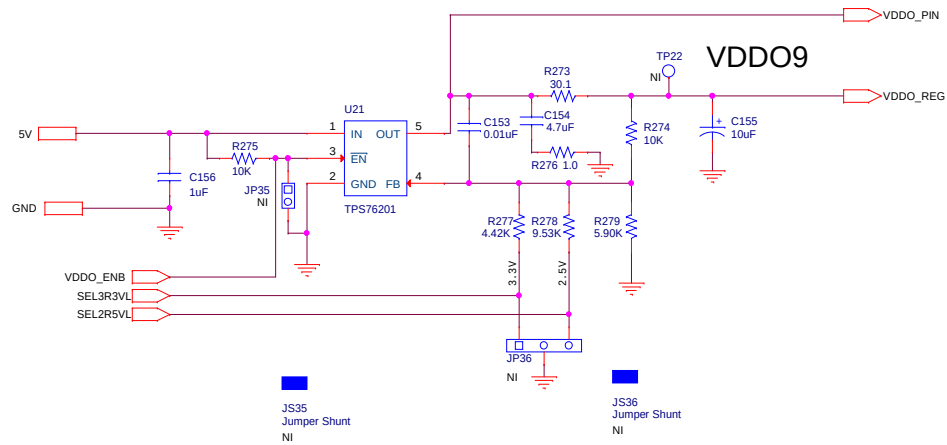
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100 mA Adjustable Voltage Regulator

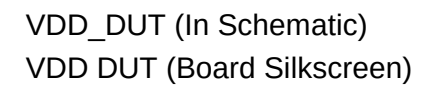


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Title			
VDD_REG_TPS76201			
Size	Document Number	<Core Design>	Rev
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100 mA Adjustable Voltage Regulator



 400 W Cesar Chavez Austin, TX 78701			
Title VDD_REG_TPS76201			
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VOLTAGE MEASUREMENT MULTIPLEXING

