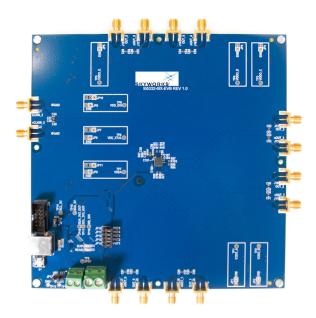


UG328: Si5332-6IX-EVB User's Guide

The Si5332-6IX-EVB is used for evaluating the Si5332-GM1 grade E, F, G or H embedded crystal Low Jitter Any-Frequency Clock Generator. The Si5332-GM1 uses the patented Multisynth[™] technology to generate up to six independent clock frequencies each with 0 ppm synthesis error. The Si5332-6IX-EVB has one input clock. The Si5332-6IX-EVB can be controlled and configured using the Clock Builder Pro[™] (CB Pro[™]) software tool.

EVB FEATURES

- Powered from USB port or external power supply.
- Embedded 50 MHz XTAL allows free-run mode of operation on the Si5332-GM1 or up to 1 input clocks for synchronous clocking.
- CBPro[™] GUI programmable VDD supply allows device to operate from 3.3, 2.5, or 1.8 V.
- CBPro GUI programmable VDDO supplies allow each of the 10 outputs to have its own power supply voltage selectable from 3.3, 2.5, or 1.8 V.
- CBPro GUI-controlled voltage, current, and power measurements of VDD and all VDDO supplies.
- SMA connectors for input and output clocks.



1. Functional Block Diagram

Below is a functional block diagram of the Si5332-6IX-EVB. This EVB can be connected to a PC via the main USB connector for programming, control and monitoring. See section 5. Installing ClockBuilderPro (CBPro) Desktop Software for more information.

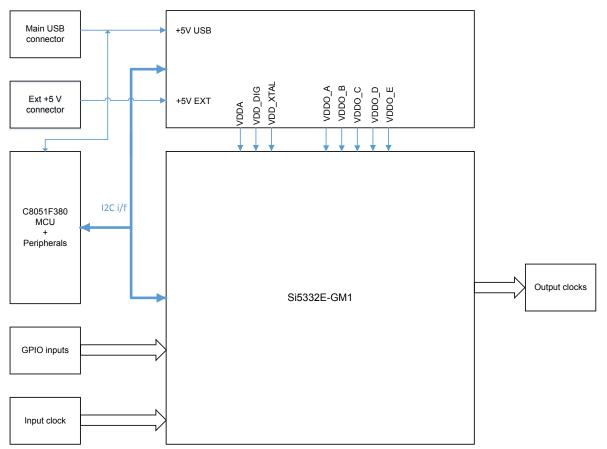


Figure 1.1. Si5332-6IX-EVB Functional Block Diagram

2. Si5332 CBPro[™]

The Si5332 is intended to be part of the CBPro software and this initial software release "showcases" that trait. This software contains:

- 1. An EVB GUI that communicates and controls the EVB by allowing the user to set VDD supplies.
- 2. The ability to modify frequency plan (from the starting point CBPro file provided with this limited release) from an existing CBPro file.

CIOckBuilder Pro Wizard - Skyworks	– 🗆 ×
Image: ClockBuilder Pro WizarImage: ClockBuilder Pro VizarImage: Cl	d skyworks
Work With a Design	Quick Links
Create New Design	<u>Clock Generators & Jitter Attenuators</u> Knowledge Base
🖶 <u>Open Design Project File</u>	ClockBuilder Go iOS App
ex <u>Open Sample Design</u>	Applications Documentation
Evaluation Board Detected Si5332 EVB Open Default Plan EVB GUI	10/40/100G Line Card White Paper Clock Generators for Cloud Data Centers White Paper Optimizing Si534x Jitter Performance App Note SyncE and IEEE 1588 App Note
	ClockBuilder Pro Documentation
	CBPro Overview CBPro Tools & Support for In-System Programming UPDATE CLI User's Guide UPDATE Release Notes • Knowledge Base
0.	Version 2.12.0.200 Built on 12/15/2016

Figure 2.1. CBPro Start Screen

UG328: Si5332-6IX-EVB User's Guide • Si5332-6IX-EVB Schematics

3. Si5332-6IX-EVB Schematics

The schematic and layout files are provided here: schematics and layouts.

Please review the files, especially the DUT page, in order to get familiar with using the EVB through CBPro[™].

4. Si5332 CBPro[™] EVB GUI

The EVB GUI can be used to communicate the part for register access:

The first page shows the board's identity.

CB SI5332-GM3	EVB - ClockB	uilder Pro		_		-				- 🗆 <mark>- X</mark> -
File Help										
Info DUT Re	gister Editor	Regulators	GPIO							Ŧ
Board Identifi	cation:									
Board ID (Code: 1	(Si5332)								
Board SN:	0	0-00-1A-BB-88	-70							
Package I	2: QFN-48									
Log										
Filtered	Auto Scro	oll: On 🔽 📔	Insert Marke	r Clear	Copy to Clipboard	Pause				
Timestamp	Source	Message								
17:29:35.434	EVB	finished Read	d_Voltage_Le	vel(regulator=VD	D_DIG) => V3P30					
17:29:35.434	EVB			vel(regulator=VD						
17:29:35.442	EVB	finished Read	d_Voltage_Le	vel(regulator=VD	DO_C) => V3P30					•
Si5332OFN-48								ClockBuilde	r Pro v2.15.4.20	00 [2017-06-19]

Figure 4.1. Board ID Page

The other pages for for register access, VDD control, and GPIO control.

CB Si5332-GM3 EVB - ClockBuilder Pro	
File Help	
Info DUT Register Editor Regulators GPIO	*
Register Peek/Poke	
Hex Decimal	
Address: 0x0000 0	
# Bytes: 1 Read Write	
Unsigned Int: 0	
Hex:	
15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0	
Binary: 000000000000000000000000000000000000	
(binary edit is only supported with 16 bits or less)	
Log	
Filtered 📱 Auto Scroll: On 📱 Insert Marker Clear Copy to Clipboard	Pause
Timestamp Source Message	
17:29:35.434 EVB finished Read_Voltage_Level(regulator=VDD_DIG) => V3P30	<u>م</u>
17:29:35.434 EVB Starting Read_Voltage_Level(regulator=VDDO_C)	
17:29:35.442 EVB finished Read_Voltage_Level(regulator=VDDO_C) => V3P30	v
Si5332QFN-48	ClockBuilder Pro v2.15.4.200 [2017-06-19]

Figure 4.2. Register Access

UG328: Si5332-6IX-EVB User's Guide • Installing ClockBuilderPro (CBPro) Desktop Software

5. Installing ClockBuilderPro (CBPro) Desktop Software

To install the CBOPro software on any Windows 7 (or above) PC:

Go to https://www.skyworksinc.com/en/Application-Pages/Clockbuilder-Pro-Software and download ClockBuilderPro software. Both installation instructions and User's Guide for ClockBuilderPro can be found at this link. Follow the instructions as indicated.

SKYWORKS

ClockBuilder Pro

Customize Skyworks clock generators, jitter attenuators and network synchronizers with a single tool. With CBPro you can control evaluation boards, access documentation, request a custom part number, export for in-system programming and more!

www.skyworksinc.com/CBPro



C

Portfolio www.skyworksinc.com/ia/timing

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Quality www.skyworksinc.com/quality



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